

ANNEXURE-I

CERTIFICATE FROM THE ORGANISATION WHERE THE CANDIDATE IS EMPLOYED

Certified that <sup>✓</sup>Mr./Ms./Mrs. YERRABOINA SREENIVASULU is  
employed as Assoc - prof (Designation) in the  
Electronics and communication Engg. (Department/Division Name)  
of Sreenidhi Institute of Science and Technology  
(Institution/Industry Name).

We have no objection in forwarding his/her application for the Ph.D. Research Programme.

FOR FULL TIME:

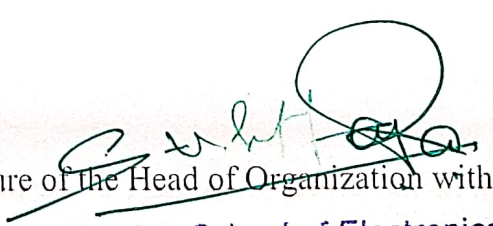
The candidate will be sanctioned leave for the duration of the research programme and will be relieved from duty from \_\_\_\_\_ to \_\_\_\_\_ to undertake the full time research work in the University.

<sup>✓</sup>FOR PART TIME:

The candidate will be permitted to undertake part time study in the University/College and will be allowed to be present for discussions with the supervisor, attending course works, conduct of experiments and participations in seminars and related presentations. Further the required facilities at our organization will also be provided to the candidate for doing research.

Date: 27/6/2020

Signature of the Head of Organization with office seal

  
Director, School of Electronics,  
Sreenidhi Institute of Science & Technology,  
Yamnapet, Ghatkesar, Hyderabad - 501 301.