The **Annexure 1** format for a PhD thesis in the Electronics and Communication Engineering (ECE) department serves as a supplementary section that provides detailed information, technical data, or supporting materials related to the research. Below is a generic format for **Annexure 1** that you can customize based on your specific research project.

Annexure 1: [Title of Annexure]

(For example: "Detailed Experimental Setup and Results," "Simulation Parameters," or "Circuit Diagrams and Specifications")

1.1 Title of First Section

- Provide a brief description of the first item or component included in Annexure 1.
- This could include technical details, diagrams, charts, or any supplementary information that supports the main thesis.
- Example: *Circuit Design Schematic of the Proposed Wireless Communication System.*
- **Diagram/Figure**: Include a high-resolution circuit schematic or block diagram.
- **Description**: A brief explanation of the design, components used, and its role in the overall research.

1.2 Title of Second Section

- Another section with relevant supporting materials, such as simulation setups, algorithm details, or measurement procedures.
- Example: *Simulation Setup and Parameters.*
- **Simulation Software**: State the software used for simulations (e.g., MATLAB, HFSS, Cadence).
- **Simulation Parameters**: List parameters such as frequency, power levels, signal types, etc.
- Example Table:

Parameter	Value	Unit
Input Frequency	2.4	GHz
Signal Power	-10	dBm
Bandwidth	20	MHz

1.3 Title of Third Section

- Data, results, or experimental measurements that are too detailed to include in the main body of the thesis.
- Example: *Test Results from Hardware Implementation*.
- **Table of Results**: A table summarizing test or experiment results.

Test Condition	Frequency (GHz)	Power (dBm)	Signal-to-Noise Ratio (dB)	Bit Error Rate (BER)
Condition 1	2.4	-3	30	10^-6
Condition 2	5.0	-5	25	10^-4

1.4 Title of Fourth Section

- Additional information, such as a block diagram, algorithm description, or hardware setup.
- Example: *Block Diagram of System Architecture.*
- **Diagram/Figure**: Include relevant figures or flowcharts to explain complex processes.

1.5 Title of Fifth Section (Optional)

- Any other supplementary information that is relevant to your research.
- Example: *Raw Data from Experiments* or *MATLAB Code for Algorithm Simulation.*

Formatting Guidelines:

1. **Labeling**: Each annexure should be clearly labeled (Annexure 1, Annexure 2, etc.), with subsections like 1.1, 1.2, etc.
2. **Cross-Referencing**: Ensure you reference each annexure properly in the main thesis document where relevant.
3. **Presentation**: Maintain a clean, professional layout. Use consistent formatting for tables, figures, and descriptions.

Example Outline for Annexure 1 in PhD ECE:

Annexure 1: Circuit Diagrams and Test Results of the Communication System

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1.1 Circuit Schematic of the Transmitter Module

- Detailed circuit diagram of the transmitter used in the wireless communication system.
- **Figure**: Circuit schematic showing component placement and connections.

1.2 Simulation Setup and Parameters

- Parameters used for simulating the communication system.
- **Table**:

Parameter	Value	Unit
Input Frequency	2.4	GHz
Signal Power	-10	dBm
Bandwidth	20	MHz

1.3 Measured Test Results

- Results from hardware testing under different conditions.
- **Table**:

Test Condition	Frequency (GHz)	Power (dBm)	Signal-to-Noise Ratio (dB)	Bit Error Rate (BER)
Condition 1	2.4	-3	30	10^-6
Condition 2	5.0	-5	25	10^-4

1.4 Block Diagram of System Architecture

- A high-level block diagram showing the overall system architecture.
- **Figure**: System block diagram.

Notes:

- The annexure should include technical materials like schematics, code, test results, or any detailed supplementary information that would support the findings presented in the main thesis.
- Make sure the annexure is numbered correctly and referred to throughout the document for clarity.