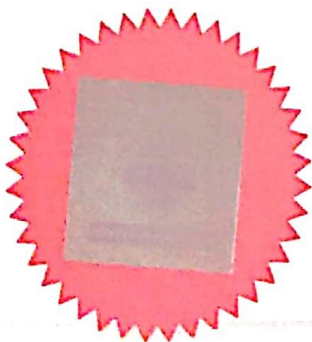




Reg.No. 623017419002/RG

The Syndicate of the Anna University hereby makes known that
SILAMBARASAN A *has been admitted to the* **DEGREE OF MASTER**
OF ENGINEERING *in* **VLSI DESIGN** *under the Faculty of*
Information and Communication Engineering, having completed the
prescribed programme of study and having been certified by the duly appointed
examiners to be qualified to receive the same, and has been placed in **FIRST**
CLASS *at the Examination held in* **APRIL 2019** .

Given under the Seal of the University

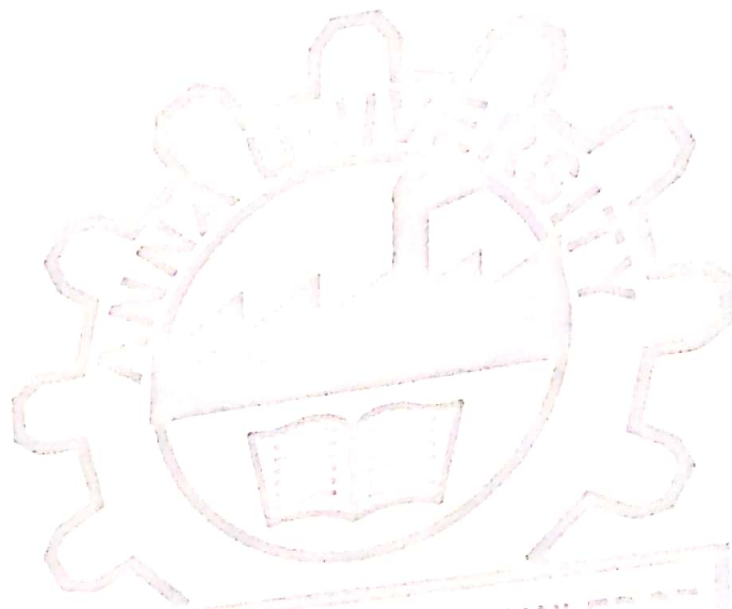


Chennai 600 025
India
December 2019

Controller of Examinations

Registrar

Vice-Chancellor



SI.No: MJ 754258

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