



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY**  
HYDERABAD - 500 072.  
ANDHRA PRADESH, INDIA

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

Roll No. : A 105384

Bachelor of Technology in **ELECTRONICS & COMMUNICATION ENGINEERING**

Sekid No. : 619450857228  
Name : **SANDEPAGA HEMANTH**  
Hall Ticket No. : 02H1A0439

Name of the College : **A C E T, MOINABAD**  
Month & Year of Final Exam : **November, 2006**  
Class Awarded : **SECOND CLASS**

Year of Admission : 2002-2003

| Sl. No. | COURSE TITLE | INTERNAL MARKS |     | TOTAL | CREDITS | Sl. No. | COURSE TITLE | INTERNAL MARKS |     | TOTAL | CREDITS |
|---------|--------------|----------------|-----|-------|---------|---------|--------------|----------------|-----|-------|---------|
|         |              | THEORY         | LAB |       |         |         |              | THEORY         | LAB |       |         |
|         | THEORY       | 20             | 80  | 100   |         |         | THEORY       | 20             | 80  | 100   |         |
|         | LAB          | 25             | 50  | 75    |         |         | LAB          | 25             | 50  | 75    |         |

**I YEAR**

|   |                                |    |    |    |   |    |                                   |    |    |    |   |
|---|--------------------------------|----|----|----|---|----|-----------------------------------|----|----|----|---|
| 1 | ENGLISH                        | 16 | 30 | 46 | 6 | 2  | MATHEMATICS - I                   | 18 | 48 | 66 | 6 |
| 3 | SOLID STATE PHYSICS            | 16 | 28 | 44 | 4 | 4  | INFORMATION TECH. & NUME. METHODS | 14 | 35 | 49 | 6 |
| 5 | C & DATA STRUCTURES            | 14 | 56 | 70 | 6 | 6  | NETWORK THEORY                    | 13 | 28 | 41 | 6 |
| 7 | ELECTRONICS DEVICES & CIRCUITS | 14 | 45 | 59 | 6 | 8  | ENGINEERING DRAWING PRACTICE      | 21 | 31 | 52 | 4 |
| 9 | COMPUTER PROGRAMMING (LAB)     | 16 | 27 | 43 | 8 | 10 | ELECTRONICS DEVICES & CKTS (LAB)  | 17 | 34 | 51 | 4 |

**I SEMESTER**

**II YEAR**

**II SEMESTER**

|   |                                     |    |    |    |   |   |                                    |    |    |    |   |
|---|-------------------------------------|----|----|----|---|---|------------------------------------|----|----|----|---|
| 1 | MATHEMATICS - II                    | 12 | 29 | 41 | 4 | 1 | MATHEMATICS - III                  | 12 | 52 | 64 | 4 |
| 2 | ELECTRONIC CIRCUIT ANALYSIS         | 13 | 41 | 54 | 4 | 2 | MANAGERIAL ECONOMICS & FINANCIAL A | 16 | 48 | 64 | 4 |
| 3 | PROBABILITY THEORY & STOCHASTIC PRO | 12 | 47 | 59 | 4 | 3 | ELECTRICAL TECHNOLOGY              | 12 | 50 | 62 | 4 |
| 4 | SIGNALS & SYSTEMS                   | 9  | 31 | 40 | 4 | 4 | CONTROL SYSTEMS                    | 14 | 28 | 42 | 4 |
| 5 | SWITCHING THEORY & LOGIC DESIGN     | 13 | 28 | 41 | 4 | 5 | EM WAVES & TRANSMISSION LINES      | 11 | 29 | 40 | 4 |
| 6 | PULSE & DIGITAL CIRCUITS            | 11 | 38 | 49 | 4 | 6 | COMMUNICATION THEORY               | 14 | 23 | 37 | 4 |
| 7 | ELECTRONIC CIRCUITS (LAB)           | 19 | 31 | 44 | 2 | 7 | NETWORKS & ELECTRICAL ENGG. (LAB)  | 22 | 39 | 61 | 2 |
| 8 | PULSE & DIGITAL CIRCUITS (LAB)      | 22 | 42 | 64 | 2 | 8 | ANALOG COMMUNICATIONS (LAB)        | 20 | 40 | 60 | 2 |

**I SEMESTER**

**III YEAR**

**II SEMESTER**

|   |                              |    |    |    |   |   |                                    |    |    |    |   |
|---|------------------------------|----|----|----|---|---|------------------------------------|----|----|----|---|
| 1 | MANAGEMENT SCIENCE           | 12 | 48 | 60 | 4 | 1 | COMMUNICATION SYSTEMS              | 12 | 41 | 53 | 4 |
| 2 | COMPUTER ORGANIZATION        | 12 | 43 | 55 | 4 | 2 | ELECTRONIC MEAS. & INSTRUMENTATION | 14 | 30 | 44 | 4 |
| 3 | DIGITAL IC APPLICATIONS      | 12 | 45 | 57 | 4 | 3 | DIGITAL SIGNAL PROCESSING          | 12 | 21 | 33 | 4 |
| 4 | ANTENNAS & WAVE PROPAGATION  | 16 | 31 | 47 | 4 | 4 | MICROWAVE ENGINEERING              | 12 | 40 | 52 | 4 |
| 5 | LINEAR IC APPLICATIONS       | 14 | 30 | 44 | 4 | 5 | VLSI DESIGN                        | 15 | 28 | 43 | 4 |
| 6 | DIGITAL COMMUNICATIONS       | 13 | 28 | 41 | 4 | 6 | MICROPROCESSORS AND INTERFACING    | 16 | 36 | 52 | 4 |
| 7 | LINEAR IC APPLICATIONS (LAB) | 19 | 33 | 52 | 2 | 7 | MICROPROCESSORS (LAB)              | 21 | 39 | 60 | 2 |
| 8 | DIGITAL COMMUNICATIONS (LAB) | 23 | 40 | 63 | 2 | 8 | ELECTRONIC CAD (LAB)               | 20 | 38 | 58 | 2 |

**I SEMESTER**

**IV YEAR**

**II SEMESTER**

|   |                               |    |    |    |   |   |                         |    |     |     |   |
|---|-------------------------------|----|----|----|---|---|-------------------------|----|-----|-----|---|
| 1 | OPERATING SYSTEMS             | 15 | 38 | 53 | 4 | 1 | CELLULAR & MOBILE COMM. | 15 | 45  | 60  | 4 |
| 2 | COMPUTER NETWORKS             | 17 | 28 | 45 | 4 | 2 | RADAR ENGINEERING       | 16 | 30  | 46  | 4 |
| 3 | TV ENGINEERING                | 16 | 34 | 50 | 4 | 3 | PROJECT & SEMINAR #     | 33 | 140 | 173 | 6 |
| 4 | OPTICAL COMMUNICATIONS        | 14 | 42 | 56 | 4 |   |                         |    |     |     |   |
| 5 | SATELLITE COMMUNICATIONS      | 17 | 44 | 61 | 4 |   |                         |    |     |     |   |
| 6 | DIGITAL IMAGE PROCESSING      | 17 | 28 | 45 | 4 |   |                         |    |     |     |   |
| 7 | DIGITAL SIGNAL PROCESSING LAB | 23 | 40 | 63 | 2 |   |                         |    |     |     |   |
| 8 | MICROWAVE/OPTICAL COMM. LAB   | 21 | 42 | 63 | 2 |   |                         |    |     |     |   |

Number of Credits registered for : 212 (# Project Internal = 40, External = 160)

Aggregate Marks Secured for best 212 credits 2889 OUT OF 5075 (56.93 %)

Date of Declaration of Result December 18, 2006

*[Signature]*  
CONTROLLER OF EXAMINATIONS

CC 110645  
ON ON ZR SX FR FY

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY



HYDERABAD, ANDHRA PRADESH, INDIA

College Code: H3 ( A C E T, MOINABAD )

*Mr. Sandepaga Hemanth*  
*S/o Vijay Shankar*

having fulfilled the academic requirements and passed the examination  
held during *November - 2006* in *\*\* Second Class \*\**  
has this day been admitted by the Executive Council to the Degree of

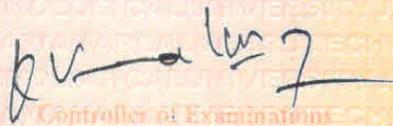
*Bachelor of Technology*  
*( Electronics & Communication Engineering )*

Given under the Seal of the University

H.T.No : **02H31A0439**

0612908

Date : 30 March, 2007

  
Controller of Examinations

  
Registrar

  
Registrar