

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 626720



H.T No.: **11D01D5728**

College Code : D0 (S M C E T, DESHMUKHI)

Mr. Rajesh V
S/o. V Muneaih

having fulfilled the academic requirements and passed the examination held during *April, 2014* in *First Class With Distinction* has this day been admitted by the Executive Council to the Degree of

Master Of Technology
VLSI System Design

Given under the Seal of the University

Date: 04-09-2014



DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR

CMM No
20198005558



45200198155220

PC No
30198005395

Anna University



Reg. No.51408631036/RG

The Syndicate of the Anna University hereby makes known that **RAJESH VEMULA** has been admitted to the **DEGREE OF MASTER OF BUSINESS ADMINISTRATION** under the Faculty of Management Sciences, having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in **FIRST CLASS** at the Examination held in **JUNE 2010**.

Given under the Seal of the University



Chennai 600 025

India

December 2010

GPM6310365

Controller of Examinations

Registrar

Vice-Chancellor

SRILAKSHMIDEVI@ANNAUNIVERSITY



The Syndicate of the Anna University hereby makes known that
RAJESH VEMULA *has been admitted to the* **DEGREE OF BACHELOR**
OF ENGINEERING *in* **ELECTRONICS AND COMMUNICATION**
ENGINEERING *under the Faculty of Information and Communication*
Engineering having completed the prescribed programme of study and having
been certified by duly appointed examiners to be qualified to receive the same and
having been placed by them in the **FIRST CLASS** *at the Examination held in*
NOVEMBER 2006.

Given under the Seal of the University



Chennai 600 025
 India
 March 2007

CSUM410601603

V. Jayaraman

Controller of Examinations

K. J. S. S. S.

Registrar

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Vice-Chancellor

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