



FACULTY OF ENGINEERING AND TECHNOLOGY

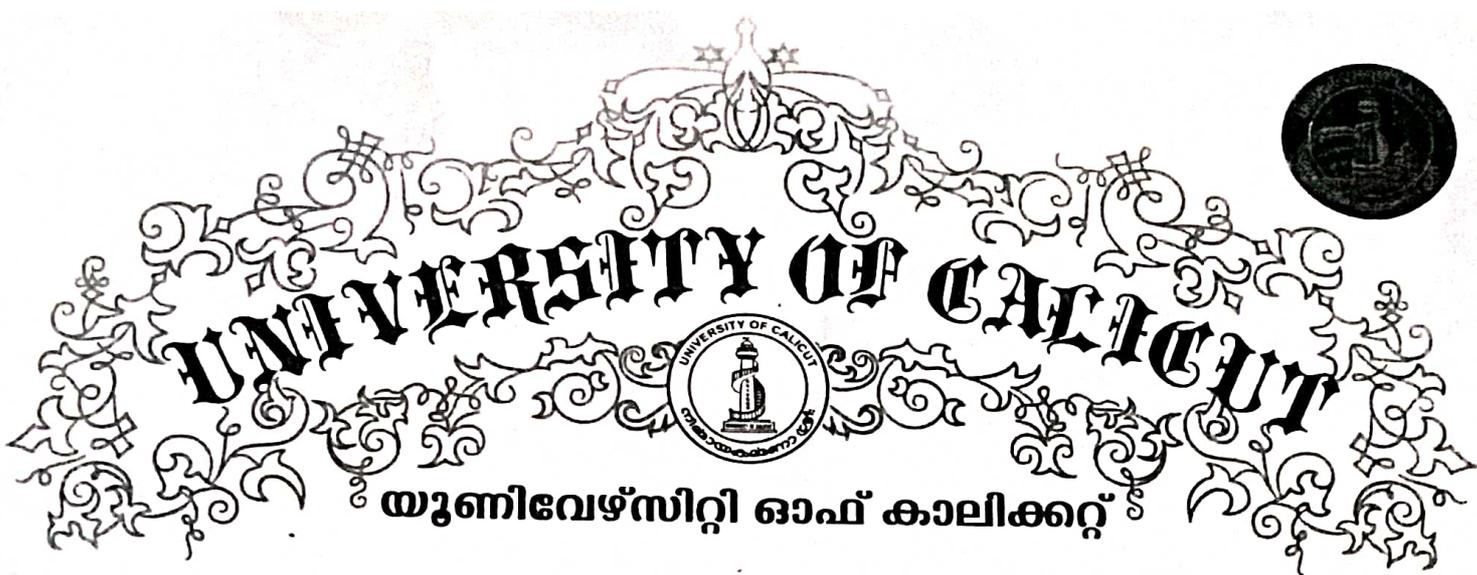
The Senate of the University of Kerala hereby makes known that **Malu, U.** has been admitted to the Degree of Bachelor of Technology under **Electronics and Communication Engineering** Branch, she having been certified by duly appointed examiners to be qualified to receive the same and having been by them placed in the **First Class** at the examination held in **June 2007**.

Given under the seal of the University

University Buildings
Thiruvananthapuram June 30, 2008




Vice Chancellor



FACULTY OF ENGINEERING

Whereas it has been certified by duly appointed Examiners that

Malu. U.

is qualified to receive

the Degree of Master of Technology (M.Tech.)

in Branch *VLSI Design*

she having been placed in **First Class with Distinction** with

Cumulative Grade Point Average (CGPA) **8.16**

at the Examination held in **August 2013** (Reg.No. **GWALCVL009**)

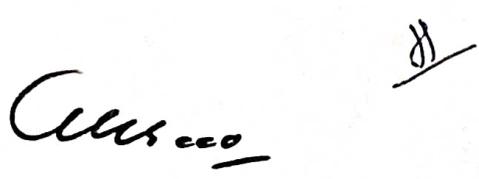
The Senate of the University of Calicut hereby confers on her

the Degree of

Master of Technology

with all the Rights, Privileges and Honours thereunto appertaining.

Given under the seal of the University



Vice Chancellor



CALICUT UNIVERSITY P.O.
Kerala, INDIA
PIN - 673 635
15/07/2014
Date

No. 0452776