

19th November 2024

TO WHOM SO EVER IT MAY CONCERN

This is to certify that **Mr. CHIRALA CHAITANYA LAXMI PRASAD (9923152003)**, Second Year student of **M.Tech - VLSI Design, Kalasalingam Academy of Research and Education, Krishnankoil** has successfully completed his internship program on **Digital Design and Verification using Verilog HDL** at **MSPAND Technologies Private Limited** from **23.07.2024 to 16.11.2024**.

During the internship period, he worked on the below mentioned projects **64-bit ALU, Traffic Signal Controller** using Verilog HDL.

We wish him the best for his future endeavors and hope that the experience gained by him during the internship at **MSPAND Technologies Private Limited** will serve as a solid foundation for his career growth.

For MSPAND Technologies Private Limited



Ramesh G

Technical Manager

MSPAND Technologies Private Limited

