



ANNA UNIVERSITY CHENNAI, CHENNAI - 600 025

B.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF MARKS

Roll No. TP218509
REGISTRATION NO. 71305106014

NAME OF THE CANDIDATE		REGISTER NO.					
JENITHA R		71305106014					
COLLEGE OF STUDY		MONTH & YEAR OF LAST APPEARANCE					
713 M.P.Nachimuthu M.Jaganathan Engineering College		April 2010					
PROGRAMME & BRANCH		REGULATIONS					
B.E. Electronics and Communication Engineering		2004					
SEM.	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF REBAND	MONTHS & YEAR OF REBAND
01*	CE1101	Engineering Chemistry	100	50	075	APR 2007	APR 2009
01*	CE1101	Electron Devices	100	50	070	APR 2007	NOV 2009
01*	CE1102	Circuit Analysis	100	50	077	APR 2007	NOV 2009
01*	CE1101	Engineering Graphics	100	50	063	APR 2007	NOV 2009
01*	CE1103	Computer Programming	100	50	074	APR 2007	NOV 2009
01*	CE1101	Technical English	100	50	066	APR 2007	NOV 2009
01*	MA1101	Engineering Mathematics - I	100	50	076	APR 2007	NOV 2009
01*	PH1101	Engineering Physics	100	50	084	APR 2007	NOV 2009
01*	CE1103	Engineering Practices Laboratory	100	50	095	APR 2007	NOV 2009
01*	PC1101	Physics and Chemistry Laboratory	100	50	093	APR 2007	NOV 2009
03	CS1131	Data Structures	100	50	078	NOV 2007	NOV 2009
03	CE1101	Environmental Science and Engineering	100	50	089	NOV 2007	NOV 2009
03	CE1101	Digital Electronics	100	50	076	NOV 2007	NOV 2009
03	EE1101	Electrical Circuits - I	100	50	071	NOV 2007	NOV 2009
03	EE1101	Electrical Machines	100	50	069	NOV 2007	NOV 2009
03	MA1101	Mathematics - III	100	50	095	NOV 2007	NOV 2009
03	CS1132	Data Structure Lab	100	50	068	NOV 2007	NOV 2009
03	EE1104	Electronic Devices and Circuits Lab I	100	50	064	NOV 2007	NOV 2009
03	EE1101	Electrical Machines Lab	100	50	070	APR 2008	APR 2009
04	EE1101	Electronic Circuits II	100	50	081	APR 2008	APR 2009
04	EE1101	Signals and Systems	100	50	065	APR 2008	APR 2009
04	EE1101	Electromagnetic Fields	100	50	077	APR 2008	APR 2009
04	EE1101	Linear Integrated Circuits	100	50	087	APR 2008	APR 2009
04	MA1104	Measurements and Instrumentation	100	50	067	APR 2008	APR 2009
04	EE1101	Random Processes	100	50	093	APR 2008	APR 2009
04	EE1101	Electronic circuits II and simulation lab	100	50	095	APR 2008	APR 2009
04	EE1101	Linear Integrated Circuit Lab	100	50	095	APR 2008	APR 2009
04	EE1101	Digital Electronics lab	100	50	057	NOV 2008	NOV 2009
05	EE1101	Communication Theory	100	50	071	NOV 2008	NOV 2009
05	EE1101	Digital Signal Processing	100	50	067	NOV 2008	NOV 2009
05	EE1101	Microprocessors and Its Applications	100	50	081	NOV 2008	NOV 2009
05	EE1101	Control Systems	100	50	080	NOV 2008	NOV 2009
05	EE1101	Transmission Lines and Waveguides	100	50	091	NOV 2008	NOV 2009
05	MA1101	Nominal Methods	100	50	091	NOV 2008	NOV 2009
05	EE1101	Digital Signal Processing Lab	100	50	092	NOV 2008	NOV 2009
05	EE1101	Microprocessors and Application Lab	100	50	085	APR 2009	APR 2009
06	CS1131	Computer Architecture	100	50	082	APR 2009	APR 2009
06	EE1101	Computer Networks	100	50	085	APR 2009	APR 2009
06	EE1101	Digital Communication	100	50	087	APR 2009	APR 2009
06	EE1101	Antenna and Wave Propagation	100	50	070	APR 2009	APR 2009
06	MS1131	Principles of Management	100	50	077	APR 2009	APR 2009
06	EE1101	Medical Electronic	100	50	098	APR 2009	APR 2009
06	EE1101	Communication System Lab	100	50	096	APR 2009	APR 2009
06	EE1101	Networks Lab	100	50	096	APR 2009	APR 2009
06	EE1101	Electronic System Design Lab	100	50	096	APR 2009	APR 2009

* Annual Pattern is followed in the First Year of study and Semester Pattern is followed from Second Year onwards.
 ** End of Statement**
 Clarification : **FIRST CLASS WITH DISTINCTION**
 Total Marks (from Jrd to 8th semester) : 3979 / 4900
 Percentage (rounded to nearest integer) : 81



(Handwritten signature)