

UNIVERSITY OF KERALA

Book No.

1

Serial No.

37

EE II C

Thiruvananthapuram,

Register Number 22835

Dated 19 AUG 2002



MEMORANDUM

Rajeev . S . K

The following marks were awarded to Shri / Smt. _____
at the VIII Semester (Final) B. Tech. Degree Examination of April - May / October - November 2002

Branch : Applied Electronics & Instrumentation Engineering.

Subject	Marks Awarded		
	Written	Sessional	Total
A. Modern Control Theory	62	36	98
B. Digital Instrumentation Techniques	44	37	81
C. Computerised Process Control	72	31	103
D. Optical Instrumentation	63	35	98
E. Micro Controller Based Instrumentation	59	35	94
F. Elective II Artificial Neural Network	48	36	84
G. Process Control Lab.	58	38	96
H. Project & Viva-Voce	91	30	121
Grand Total			775
Carry over of marks I to VII Semesters			4634
Aggregate			5409
In words			Five, Four, Zero, Nine

Maximum Marks : 100 for Written I Practical and 50 for Sessional for each subject (1200 marks)

Pass Minimum : In whole appearance Complete Pass : 40% of Written I Practical of each subject and 50% of the aggregate (ie. 600 marks) Pass in individual subject(s): 40% of Written I Practical and 50% of the total marks of each subject. Aggregate Maximum 8600.

Prepared by _____

Compared by _____

Section Officer _____

Assistant Registrar _____

K. U. P. 172/2002-'03 100 x 15 Bks.

CONTROLLER OF EXAMINATIONS



ANNA UNIVERSITY TIRUNELVELI

DEGREE EXAMINATIONS

CONSOLIDATED STATEMENT OF MARKS



Folio No. : D07000471

NAME OF THE CANDIDATE	RAJEEV.S K	REGISTER NO.	96307401005
COLLEGE OF STUDY	NOORUL ISLAM COLLEGE OF ENGINEERING	MONTH & YEAR OF LAST APPEARANCE	MAY 2009
PROGRAMME & BRANCH	M.E. - Applied Electronics	REGULATIONS	2007

SEM. NO.	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING
01	MA1616A	Applied Mathematics for Electronics Engineers	100	050	060	JAN 2008
01	AN1622	Neural Networks and Applications	100	050	066	JAN 2008
01	AN1605	Electronics Design Lab - I	100	050	099	JAN 2008
01	AN1604	Advanced Microprocessors and Micro Controllers	100	050	058	JAN 2008
01	AN1603	VLSI Design Techniques	100	050	067	JAN 2008
01	AN1601	Advanced Digital Signal Processing	100	050	080	MAY 2008
01	AN1602	Advanced Digital System Design	100	050	063	MAY 2008
02	AN1651	Analysis and Design of Analog Integrated Circuits	100	050	058	MAY 2008
02	AN1652	Computer Architecture and Parallel Processing	100	050	068	MAY 2008
02	AN1653	Digital Control Engineering	100	050	072	MAY 2008
02	AN1654	Embedded Systems	100	050	070	MAY 2008
02	AN1655	Electronic Design Lab II	100	050	098	MAY 2008
02	AN1628	Internetworking Multimedia	100	050	063	MAY 2008
02	AN1621	Digital Image Processing	100	050	064	MAY 2008
03	AN1630	High Performance Communication Networks	100	050	068	DEC 2008
03	AN1623	Robotics	100	050	079	DEC 2008
03	AN1625	ASIC Design	100	050	062	DEC 2008
04	AN1751	Project Work	600	300	564	MAY 2009
* End of Statement *						
Classifications : FIRST CLASS						
Total Marks : 1759 / 2300						
Percentage (rounded to nearest integer) : 76						

This document is issued without any correction
Medium of instruction : English

Tirunelveli - 627 007
Date : 07-Oct-2009

SIGNATURE OF THE STUDENT



For
CONTROLLER OF EXAMINATIONS