



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No.: **C 373452** BACHELOR OF TECHNOLOGY -- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No.:
Name: **585549695255**
K KANNAIAH

Name of the College: **N E C, GUDUR**
Month & Year of Final Exam: **May, 2008**

Hall Ticket No.: **05F15A0409** Year of Admission **2005-2006**

Class Awarded: **FIRST CLASS**

S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL
		20	80	100				25	50	75

I YEAR

DIRECT ADMISSION INTO II-YEAR UNDER LATERAL ENTRY SCHEME

I SEMESTER					II YEAR					II SEMESTER					
1	MATHEMATICS - II	13	38	51	4	1	MATHEMATICS - III	13	41	54	1	MATHEMATICS - III	13	41	54
2	ELECTRONIC CIRCUIT ANALYSIS	14	37	51	4	2	MANAGERIAL ECONOMICS & FINANCIAL A	15	44	59	2	MANAGERIAL ECONOMICS & FINANCIAL A	15	44	59
3	PROBABILITY THEORY & STOCHASTIC PRO	12	28	40	4	3	ELECTRICAL TECHNOLOGY	16	28	44	3	ELECTRICAL TECHNOLOGY	16	28	44
4	SIGNALS & SYSTEMS	13	49	62	4	4	CONTROL SYSTEMS	16	29	45	4	CONTROL SYSTEMS	16	29	45
5	SWITCHING THEORY & LOGIC DESIGN	15	47	62	4	5	EM WAVES & TRANSMISSION LINES	13	47	60	5	EM WAVES & TRANSMISSION LINES	13	47	60
6	PULSE & DIGITAL CIRCUITS	15	31	46	4	6	COMMUNICATION THEORY	16	31	47	6	COMMUNICATION THEORY	16	31	47
7	ELECTRONIC CIRCUITS (LAB)	20	44	64	2	7	NETWORKS & ELECTRICAL ENGG. (LAB)	21	37	58	7	NETWORKS & ELECTRICAL ENGG. (LAB)	21	37	58
8	PULSE & DIGITAL CIRCUITS (LAB)	20	42	62	2	8	ANALOG COMMUNICATIONS (LAB)	22	40	62	8	ANALOG COMMUNICATIONS (LAB)	22	40	62

I SEMESTER					III YEAR					II SEMESTER					
1	MANAGEMENT SCIENCE	16	53	69	4	1	COMMUNICATION SYSTEMS	19	40	59	1	COMMUNICATION SYSTEMS	19	40	59
2	COMPUTER ORGANIZATION	19	42	61	4	2	ELECTRONIC MEAS. & INSTRUMENTATION	13	55	68	2	ELECTRONIC MEAS. & INSTRUMENTATION	13	55	68
3	DIGITAL IC APPLICATIONS	16	40	56	4	3	DIGITAL SIGNAL PROCESSING	12	41	53	3	DIGITAL SIGNAL PROCESSING	12	41	53
4	ANTENNAS & WAVE PROPAGATION	16	37	53	4	4	MICROWAVE ENGINEERING	16	37	53	4	MICROWAVE ENGINEERING	16	37	53
5	LINEAR IC APPLICATIONS	16	30	46	4	5	VLSI DESIGN	17	41	58	5	VLSI DESIGN	17	41	58
6	DIGITAL COMMUNICATIONS	14	33	47	4	6	MICROPROCESSORS AND INTERFACING	15	43	58	6	MICROPROCESSORS AND INTERFACING	15	43	58
7	LINEAR IC APPLICATIONS (LAB)	19	40	59	2	7	MICROPROCESSORS (LAB)	22	42	64	7	MICROPROCESSORS (LAB)	22	42	64
8	DIGITAL COMMUNICATIONS (LAB)	22	47	69	2	8	ELECTRONIC CAD (LAB)	22	30	52	8	ELECTRONIC CAD (LAB)	22	30	52

I SEMESTER					IV YEAR					II SEMESTER					
1	OPERATING SYSTEMS	18	41	59	4	1	CELLULAR & MOBILE COMM.	19	47	66	1	CELLULAR & MOBILE COMM.	19	47	66
2	COMPUTER NETWORKS	17	34	51	4	2	RADAR ENGINEERING	20	53	73	2	RADAR ENGINEERING	20	53	73
3	TV ENGINEERING	14	50	64	4	3	PROJECT & SEMINAR #	37	131	168	3	PROJECT & SEMINAR #	37	131	168
4	OPTICAL COMMUNICATIONS	16	40	56	4										
5	SATELLITE COMMUNICATIONS	17	42	59	4										
6	DIGITAL IMAGE PROCESSING	16	44	60	4										
7	DIGITAL SIGNAL PROCESSING LAB	18	36	54	2										
8	MICROWAVE & OPTICAL COMM. LAB	23	40	63	2										

Number of Credits registered for: **156** (# Project Internal = 40, External = 160)
Aggregate Marks Secured for best: **156 CREDITS TOTAL 2565 OUT OF 4150 (61.81 %)**

Date of Declaration of Result: **May 16, 2008**
(See overleaf for instructions) (*Courses registered but not counted for calculation of aggregate)

B. Day Kumar
CONTROLLER OF EXAMINATIONS



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY - (A.P.)
COLLEGE OF ENGINEERING, ANANTAPUR
 (Autonomous)

S.No. 3912195

MEMORANDUM OF MARKS

NAME: K. KANNAIAH	HALL TICKET NO: 10004D0623
EXAMINATION M.TECH. (SSS)-I YEAR REGULAR EXAMS MONTH & YEAR OF EXAM. JULY 2010	
BRANCH DS & CE	

S.No.	SUBJECT CODE	SUBJECT TITLE	Internal Marks	End Exam.	Total Marks	RESULT	CREDITS
1	MEC11	DIGITAL SYSTEMS DESIGN	35	47	82	P	
2	MEC12	ADVANCED DATA COMMUNICATION	28	52	80	P	
3	MEC13	EMBEDDED SYSTEMS CONCEPTS	31	38	69	P	
4	MEC14	NEURAL NETWORKS & APPLICATIONS	27	47	74	P	
5	MEC15	DIGITAL SYSTEMS DESIGN LAB	34	42	76	P	
SUBJECTS REGISTERED			5	APPEARED:	5	PASSED:	5
TOTAL			155	226	381		

AGGREGATE MARKS ***** Three Eight One *****

DATE: 22-07-2010 VERIFIED BY

Basra
PRINCIPAL

INSTRUCTIONS:	MAXIMUM MARKS		MINIMUM FOR PASS	
	Internal	End Exam.	End Exam	Total of Int. & End
THEORY/DRAWING/DESIGN SUBJECTS	40	60	24	50
FACTUAL SUBJECTS	40	60	24	50

Electives P: Pass PR: Project Work F: Fail
 NOTE: The application for the recounting of marks with prescribed fee should be submitted within 15 days from the date of publication of results.