



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No.: **C 373452** BACHELOR OF TECHNOLOGY - ELECTRONICS & COMMUNICATION ENGINEERING

Serial No.: **585549695255**
Name: **K KANNAIAH**

Name of the College: **N E C. GUDUR**
Month & Year of Final Exam: **May, 2008**

Hall Ticket No.: **05F15A0409**

Year of Admission: **2005-2006**

Class Awarded: **FIRST CLASS**

COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL
THEORY	20	80	100			LAB	25	50	75

I YEAR

DIRECT ADMISSION INTO II-YEAR UNDER LATERAL ENTRY SCHEME

I SEMESTER

II YEAR

II SEMESTER

1 MATHEMATICS - II	13	38	51	4	1 MATHEMATICS - III	13	41	54
2 ELECTRONIC CIRCUIT ANALYSIS	14	37	51	4	2 MANAGERIAL ECONOMICS & FINANCIAL A	15	44	59
3 PROBABILITY THEORY & STOCHASTIC PRO	12	28	40	4	3 ELECTRICAL TECHNOLOGY	16	28	44
4 SIGNALS & SYSTEMS	13	49	62	4	4 CONTROL SYSTEMS	16	29	45
5 SWITCHING THEORY & LOGIC DESIGN	15	47	62	4	5 EM WAVES & TRANSMISSION LINES	13	47	60
6 PULSE & DIGITAL CIRCUITS	15	31	46	4	6 COMMUNICATION THEORY	16	31	47
7 ELECTRONIC CIRCUITS (LAB)	20	44	64	2	7 NETWORKS & ELECTRICAL ENGG. (LAB)	21	37	58
8 PULSE & DIGITAL CIRCUITS (LAB)	20	42	62	2	8 ANALOG COMMUNICATIONS (LAB)	22	40	62

I SEMESTER

III YEAR

II SEMESTER

1 MANAGEMENT SCIENCE	16	53	69	4	1 COMMUNICATION SYSTEMS	19	40	59
2 COMPUTER ORGANIZATION	19	42	61	4	2 ELECTRONIC MEAS. & INSTRUMENTATION	13	55	68
3 DIGITAL IC APPLICATIONS	16	40	56	4	3 DIGITAL SIGNAL PROCESSING	12	41	53
4 ANTENNAS & WAVE PROPAGATION	16	37	53	4	4 MICROWAVE ENGINEERING	16	37	53
5 LINEAR IC APPLICATIONS	16	30	46	4	5 VLSI DESIGN	17	41	58
6 DIGITAL COMMUNICATIONS	14	33	47	4	6 MICROPROCESSORS AND INTERFACING	15	43	58
7 LINEAR IC APPLICATIONS (LAB)	19	40	59	2	7 MICROPROCESSORS (LAB)	22	42	64
8 DIGITAL COMMUNICATIONS (LAB)	22	47	69	2	8 ELECTRONIC CAD (LAB)	22	30	52

I SEMESTER

IV YEAR

II SEMESTER

1 OPERATING SYSTEMS	18	41	59	4	1 CELLULAR & MOBILE COMM.	19	47	66
2 COMPUTER NETWORKS	17	34	51	4	2 RADAR ENGINEERING	20	53	73
3 TV ENGINEERING	14	50	64	4	3 PROJECT & SEMINAR #	37	131	168
4 OPTICAL COMMUNICATIONS	16	40	56	4				
5 SATELLITE COMMUNICATIONS	17	42	59	4				
6 DIGITAL IMAGE PROCESSING	16	44	60	4				
7 DIGITAL SIGNAL PROCESSING LAB	18	36	54	2				
8 MICROWAVE & OPTICAL COMM. LAB	23	40	63	2				

Number of Credits registered for: **156**

(# Project Internal = 40, External = 160)

Aggregate Marks Secured for best:

156 CREDITS TOTAL 2565 OUT OF 4150 (61.81 %)

Date of Declaration of Result

(See overleaf for instructions) **May 16, 2008**

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS

B. Day Kumar



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY - (A.P.)
COLLEGE OF ENGINEERING, ANANTAPUR
 (Autonomous)

S.No. 3242132

MEMORANDUM OF MARKS

NAME: K. KANNAIAH

HALL TICKET NO:

10004D0623

EXAMINATION M.TECH. (SSS) - III YEAR REGULAR EXAM. MONTH & YEAR OF EXAM. JULY 2012

BRANCH D S C E

No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam.	Total Marks	RESULT	CREDITS			
1	MEC31	DESIGN OF FAULT TOLERANT SYSTEMS	36	40	76	P				
2	MEC32	SYSTEM PROGRAMMING	34	36	70	P				
3	MEC33	NETWORK SECURITY & CRYPTOGRAPHY	37	48	85	P				
4	MEC34	CPLD & FPGA ARCHITECTURE & APPLICATIONS	38	49	87	P				
SUBJECTS REGISTERED 4			APPEARED: 4	PASSED: 4	TOTAL	145	173	318		

AGGREGATE (IN WORDS)

**** Three One Eight ****



PRINCIPAL

INSTRUCTIONS:

	MAXIMUM		MARKS	MINIMUM FOR PASS	
	Internal	End Exam.	Total of Int. & End	End Exam	Total of Int. & End
THEORY/DRAWING/DESIGN SUBJECTS	40	60	100	24	50
PRACTICAL SUBJECTS	40	60	100	24	50

E: Electives

P: Pass

PR: Project Work

F: Fail

NOTE: The application for the recounting of marks with prescribed fee should be submitted within 15 days from the date of publication of results.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY - (A.P.)
COLLEGE OF ENGINEERING, ANANTAPUR
(Autonomous)

S.No. 3892193

MEMORANDUM OF MARKS

NAME: K. KANNAIAH

HALL TICKET NO: 10004D0623

EXAMINATION	M.TECH. (SSS)- II YEAR REGULAR EXAM	MONTH & YEAR OF EXAM.	JULY 2011
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BRANCH D S & C E

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam.	Total Marks	RESULT	CREDIT
1	MEC21	MICROCOMPUTER SYSTEM DESIGN	33	38	71	P	
2	MEC22	ADVANCED COMPUTER ARCHITECTURE	31	45	76	P	
3	MEC23	HIGH SPEED NETWORKS	26	48	74	P	
4	MEC24	SPEECH PROCESSING	30	43	73	P	
5	MEC25	SIGNAL PROCESSING & MICROPROCESSOR LAB	35	50	85	P	
SUBJECTS REGISTERED 5			APPEARED:	5	PASSED:	5	TOTAL
			155	224	379		

AGGREGATE (IN WORDS)

**** Three Seven Nine *****

03-08-2011

VERIFIED BY

PRINCIPAL

INSTRUCTIONS:

MAXIMUM	MARKS
Internal End Exam.	Total of Int. & End

MINIMUM FOR PASS	
End Exam	Total of Int. & End

THEORY/DRAWING/DESIGN SUBJECTS

40 60 100

24 50

PRACTICAL SUBJECTS

40 60 100

24 50

EL: Electives

P: Pass

PR: Project Work

F: Fail

NOTE : The application for the recounting of marks with prescribed fee should be submitted within 15 days from the date of publication of results.



MEMORANDUM OF MARKS

HALL TICKET NO:

10004D0623

EXAMINATION M. TECH. (SSS)-I YEAR REGULAR EXAMS MONTH & YEAR OF EXAM. JULY 2010

BRANCH DS & CE

33 GREGG AUTO
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INSTRUCTIONS:

THEORY/DRAWING/DESIGN SUBJECTS

FACTICAL SUBJECTS

MAXIMUM
Internal End Exam.

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MINIMUM FOR PASS	
End Exam	Total of Int. & End

40 60

100

24

50

40 60

100

24

50

- Electives

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