

# UNIVERSITY OF KERALA

Serial No : 08 08 2790  
 Ref No. EG : 08 37 2790 A



Thiruvananthapuram  
 Date : 19/11/2008

**Additional Marklist  
 issued on 06-07-2009.**

Name of Programme : B.Tech. Electronics & Communication Engineering  
 Semester : 4  
 Month & Year of Exam : Jul 2008  
 Name of Student : **PRAJEESH R**

Exam Code : **412 03407**  
 Candidate Code : **04 411 012**

### Statement of Marks

Subject Code	Subjects	Credits	Marks Awarded		
			SM	UM	Total
03403	Analog Communication	3	46	42	<b>88</b>
	Semester - 1 & 2				6.7
	Semester - 3				6.5
	Semester - 4				6.9
	Semester - 5				6.4
	Semester - 6				6.3
	Semester - 7				6.1
	Semester - 8				6.8
Result : Passed			CGPA : <b>6.6</b> (Six * Six)		

Prepared By : Sheep

Bulaha. M. T

Checked By : Nirmala. M

Section Officer : Sheep

Controller of Examinations

\* Pass requirements & papers to be cleared if any shown overleaf



श्रद्धावान् लभते ज्ञानम्

# AMRITA VISHWA VIDYAPEETHAM UNIVERSITY

Established under Section 3 of the UGC Act 1956

AMRITA SCHOOL OF ENGINEERING, AMRITAPURI

MASTER OF TECHNOLOGY

IN

VLSI DESIGN

Grade Sheet: 2010-2012

Date of Birth: 23/05/1986

Name: Mr. Prajeesh R

RollNo: AM.EN.P2VLD10012

Code	Subject	Cr. Gr.	Month & Year of Passing	Code	Subject	Cr. Gr.	Month & Year of Passing
<b>First Semester</b>				<b>Third Semester</b>			
MA607	Probability Theory and Linear Algebra	3.0	C+ Dec, 2010	MA613	Orthogonal Functions, Optimization and Graph Theory	3.0	C Dec, 2011
VL601	CMOS Digital Integrated Circuits	4.0	C Dec, 2010	VL617	Computer Aided Design of VLSI Circuits	4.0	C+ Dec, 2011
VL602	Digital Hardware Modeling	3.0	C+ Dec, 2010	VL798	Minor Project	4.0	B Dec, 2011
VL611	Solid State Devices Modeling and Simulation	3.0	A Dec, 2010	RW703	Adaptive Digital Signal Processing using FPGA	4.0	C+ Dec, 2011
VL612	Digital Design	4.0	C+ Dec, 2010	VL721	Advanced Analog Design	3.0	C Dec, 2011
VL613	Digital Hardware Modeling Laboratory	1.0	B+ Dec, 2010	<b>Fourth Semester</b>			
HL301	Cultural Education	0.0	P Dec, 2010	VL799	Dissertation	10.0	C Jul, 2012
<b>Second Semester</b>							
VL603	Digital Signal Processing and Processors	4.0	B May, 2011				
VL614	Analysis and Design of Analog and Mixed VLSI Circuits	4.0	C May, 2011				
VL615	Testing of VLSI Circuits	3.0	B May, 2011				
VL616	VLSI Design Laboratory	1.0	A+ May, 2011				
VL701	Low Power VLSI Circuits	3.0	B+ May, 2011				
VL708	VLSI Fabrication Technology	3.0	C+ May, 2011				
VL710	RF IC Devices and Modelling	3.0	B+ May, 2011				

Summary: Grade Point Average (GPA) on a 10 Point Scale

Semester	1	2	3	4
Semester GPA	7.39	7.86	8.89	8.00

The cumulative grade point average of the candidate (CGPA) is 7.19

Date Issued On: August 6, 2012

*[Signature]*  
Deputy Controller of Examinations  
Amritapuri Campus

*[Signature]*  
Controller of Examinations

क्रमांक  
S.No. 2001

**केन्द्रीय माध्यमिक शिक्षा बोर्ड**

**CENTRAL BOARD OF SECONDARY EDUCATION**

**अंक विवरणिका MARKS STATEMENT**

सेकण्डरी स्कूल परीक्षा, २००९

**ALL INDIA SECONDARY SCHOOL EXAMINATION, 2001**

नाम Name **PRAJEEESH R** अनुक्रमांक Roll No. **4128457**  
 माता का नाम Mother's Name **VASANTHA**  
 पिता का नाम Father's Name **RAMANAN N**  
 जन्म तिथि Date of Birth **23RD MAY NINETEEN HUNDRED EIGHTY SIX**  
 विद्यालय School **6676 SREE BUDDHA CEN SCHOOL EDAKULANGARA KOLLAM KL.**

विषय कोड SUB. CODE	विषय SUBJECT	प्राप्तांक MARKS OBTAINED				स्थितीय ग्रेड POSITIONAL GRADE
		लि. TH	प्रे. PR	योग TOTAL	योग शब्दों में TOTAL IN WORDS	
001	ENGLISH COURSE-A	066	XXX	066	SIXTY SIX	B2
012	MALAYALAM	087	XXX	087	EIGHTY SEVEN	B1
041	MATHEMATICS	072	XXX	072	SEVENTY TWO	A2
086	SCIENCE WITH PRAC.	060	024	084	EIGHTY FOUR	A2
087	SOCIAL SCIENCE	076	XXX	076	SEVENTY SIX	A2

आद्यक्षरों का अर्थ : Abbreviations

AB : विषय में अनुपस्थित Absent in the Subject

परिणाम Result

**PASS**

दिल्ली Delhi

दिनांक Dated **28-05-2001**

परीक्षा नियंत्रक

Controller of Examinations