

**BOARD OF SECONDARY EDUCATION
ANDHRA PRADESH**



SECONDARY SCHOOL CERTIFICATE

AA 390740

22/05433/246762

CERTIFIED THAT MAJJARI RAVEENDRA
S/O M VENKATASUBBAIAH Bearing Roll No 0486304
belongs to CHAITANYA VIDYA MANDIR H S / BADVEL
has appeared and PASSED at the SSC EXAMINATION held in MARCH-2002
in **FIRST** Division with **TELUGU** as the medium of instruction.

DATE OF BIRTH OF THE CANDIDATE	DAY	MONTH	YEAR
C2/06/1987	ZERO TWO	JUN	ONE NINE EIGHT SEVEN

THE CANDIDATE SECURED THE FOLLOWING PERCENTAGE OF MARKS

SUBJECT	Marks Secured (in figures)	Marks Secured (in words)
FIRST LANGUAGE : TELUGU	79	SEVEN NINE
THIRD LANGUAGE : ENGLISH	58	FIVE EIGHT
MATHEMATICS :	73	SEVEN THREE
GENERAL SCIENCE :	65	SIX FIVE
SOCIAL STUDIES :	57	FIVE SEVEN
TOTAL :	332	(THREE THREE TWO)
SECOND LANGUAGE : HINDI	75	SEVEN FIVE
GRAND TOTAL :	407	(FOUR ZERO SEVEN)

Marks of Identification : 1. A MOLE UNDER THE RIGHT EAR
2. A MOLE ON THE RIGHT RING FINGER

G. K. Ramesh
HEAD MASTER, 4.4.2

Chaitanya Vidya Mandiram High School

Badvel-516 227.
with School Stamp

Date of issue : 30-04-2002

[Signature]
SECRETARY

BOARD OF SECONDARY EDUCATION
A P, HYDERABAD

Any corrections in the certificate will not be entertained after one year from the date of issue.
Any unauthorised correction in the certificate will result in cancellation of certificate.
The marks with an asterisk indicates the old marks secured in previous appearances.



390740



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No.: **C 396980** BACHELOR OF TECHNOLOGY - ELECTRICAL & ELECTRONICS ENGINEERING
Serial No.: **556554611287**
Name: **MAJJARI RAVEENDRA**

Name of the College: **SVCET, CHITTOOR**
Month & Year of Final Exam: **May, 2008**

Hall Ticket No.: **04781A0233**

Year of Admission: **2004 - 05**

Class Awarded: ***** SECOND CLASS *****



S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL MARKS	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL MARKS	CREDITS
	THEORY	20	80	100			THEORY	20	80	100	
	LAB	25	50	75			LAB	25	50	75	

I YEAR

1 ENGLISH	12	29	41	6	2 MATHEMATICS - I	14	38	52	6
3 SOLID STATE PHYSICS	16	37	53	4	4 INFORMATION TECH. & NUME. METHODS	15	28	43	6
5 C & DATA STRUCTURES	12	32	44	6	6 NETWORK THEORY	12	38	50	6
7 ELECTRONICS DEVICES & CIRCUITS	15	38	53	6	8 ENGINEERING DRAWING PRACTICE	22	36	58	4
9 COMPUTER PROGRAMMING (LAB)	18	20	38	8	10 ELECTRONICS DEVICES & CKTS (LAB)	23	39	62	4

I SEMESTER

II YEAR

II SEMESTER

1 MATHEMATICS - II	14	44	58	4	1 MANAGERIAL ECONOMICS & FINANCIAL AN	10	30	40	4
2 HYDRAULICS & HYDRAULIC MACHINERY	12	46	58	4	2 MATHEMATICS - III	12	51	63	4
3 PULSE & DIGITAL CIRCUITS	12	29	41	4	3 LINEAR & DIGITAL IC APPLICATIONS	16	34	50	4
4 SWITCHING THEORY & LOGIC DESIGN	17	28	45	4	4 POWER SYSTEMS - I	14	28	42	4
5 ELECTROMAGNETIC FIELDS	13	45	58	4	5 ELECTROMECHANICS - II	14	31	45	4
6 ELECTRO MECHANICS - I	14	28	42	4	6 CONTROL SYSTEMS	12	28	40	4
7 HYDRAULICS & HYDRAULIC MACHINERY (20	37	57	2	7 IC & PULSE & DIGITAL CIRCUITS (LAB)	20	34	54	2
8 NETWORKS (LAB)	22	38	60	2	8 ELECTROMECHANICS (LAB) - I	22	40	62	2

I SEMESTER

III YEAR

II SEMESTER

1 COMPUTER ORGANIZATION	12	28	40	4	1 ANALYSIS OF LINEAR SYSTEMS	16	41	57	4
2 ELECTRICAL MEASUREMENTS	18	37	55	4	2 MICROPROCESSORS AND INTERFACING	8	32	40	4
3 POWER SYSTEMS-II	13	28	41	4	3 MODELLING OF POWER SYSTEM COMPONENT	13	30	43	4
4 POWER ELECTRONICS	14	40	54	4	4 INSTRUMENTATION	16	39	55	4
5 ELECTROMECHANICS-III	15	28	43	4	5 POWER SYSTEMS - III	12	28	40	4
6 MANAGEMENT SCIENCE	12	28	40	4	6 UTILIZATION OF ELECTRICAL ENERGY	6	39	45	4
7 ELECTROMECHANICS (LAB) - II	23	36	59	2	7 ELECTRICAL MEASUREMENTS LAB	21	36	57	2
8 CONTROL SYSTEMS (LAB)	23	40	63	2	8 POWER ELECTRONICS LAB	22	38	60	2

I SEMESTER

IV YEAR

II SEMESTER

1 DIGITAL SIGNAL PROCESSING	8	41	49	4	1 H V D C TRANSMISSION	16	34	50	4
2 POWER SEMICONDUCTOR DRIVES	16	28	44	4	2 DATABASE MANAGEMENT SYSTEMS	13	28	41	4
3 COMPUTER METHODS IN POWER SYS.	12	32	44	4	3 PROJECT & SEMINAR #	37	152	189	8
4 POWER SYSTEM OPERATION & CONTROL	12	35	47	4					
5 H V ENGINEERING	7	33	40	4					
6 ELECTRICAL DISTRIBUTION SYS.	9	31	40	4					
7 MICROPROCESSORS LAB	20	42	62	2					
8 SIMULATION OF ELEC. SYS. LAB	22	37	59	2					

Number of Credits registered for : **212**
Aggregate Marks Secured for best : **212**

(# Project Internal = 40, External = 160)
2766 OUT OF 5075 (54.50%)

Date of Declaration of Result **May 16, 2008**
(See overleaf for instructions)

(*Courses registered but not counted for calculation of aggregate)

K. Jagan
CONTROLLER OF EXAMINATIONS

St. No. **E175997**

06/1458/E175997



Board of Intermediate Education, A. P.

Vidya Bhavan, Nampally, Hyderabad - 500 001



INTERMEDIATE PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that **MAJJARI RAVEENDRA** son
of **M VENKATASUBBAIAH** bearing
Registered No. **4135230** has appeared at the Intermediate Public
Examination held in **MARCH-2004** and passed in **FIRST DIVISION**
with **TELUGU** as the Medium of Instruction.

The subjects in which he was examined and the marks awarded are as follows :

Subject	I Year		II Year	
	Maximum Marks	Marks Secured	Maximum Marks	Marks Secured
Part - 1 : ENGLISH	100	045	100	047
Part - 2 : SANSKRIT	100	089	100	096
Part - 3 : Optional Subjects				
MATHEMATICS - A	075	061	075	050
MATHEMATICS - B	075	066	075	037
PHYSICS	060	042	060	042
CHEMISTRY	060	048	060	049
PHYSICS PRACTICAL			030	023
CHEMISTRY PRACTICAL			030	025
Total Marks In Figures	720 →			
In words	*SEVEN*TWO*ZERO*			
Date	09-05-2004	R.No.		

N. Srinivas
20/5
PRINCIPAL

Vijayawada

Nalanda Jr. College of Science & Arts

Signature of the Principal and College Seal
RING ROAD, GUNTUR-522007.

[Signature]
Controller of Examinations

4135230

CC 119372

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR



ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

H.T.No: 09711D4911

Majjari Raveendra

No. M Venkatasubbaiah

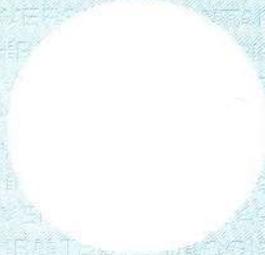
*having fulfilled the academic requirements and passed the examination held during July, 2011 in *** First Class With Distinction ****

has this day been admitted by the Executive Council to the Degree of

Master of Technology
(ELECTRICAL POWER ENGINEERING)

Monday, 4 July, 2016.

Given under the Seal of the University



[Signature]
Director of Evaluation

[Signature]
Registrar

[Signature]
Vice Chancellor