UNIVERSITY OF MADRAS

B. E. DEGREE EXAMINATION **[FIRST YEAR]**

STATEMENT OF MARKS 2K\ 0226528

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S	8012166	1433	54985

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	APR. 2001	KTK 30-07-2001

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	TOTAL	RESULT
2	ENGLISH	NSA	036	016	052	P
2	ENGG. MATHEMATICS	NSB	066	017	083	P
2	APPLIED PHYSICS	NSC	035	016	051	P
2	APPLIED CHEMISTRY	NSD	035	017	052	P
2	BASIC CIVIL & MECHANICAL ENGINEERING	NSE	047	018	065	P
2	BASIC ELECTRICAL & ELECTRONICS ENGG	NSG	045	018	063	P
2	ENGINEERING DRAWING & GRAPHICS	NSH	048	018	066	P
2	PHYSICAL SCIENCES LAB	NS1	069	016	085	P
2	COMPUTER PROGRAMMING LAB	NS2	062	018	080	P
2	WORKSHOP PRACTICE	NS3	065	017	082	P

* * * END OF STATEMENT * * *

UE - University Exam Marks
IA - Internal Assessment Marks
Passing Minimum : 35 Marks in UE and 50 Marks in Aggregate
P - PASS   F - FAIL   A - ABSENT
*** - Not obtained Minimum marks in UE
Maximum Marks : UE - 80, IA - 20

PROF. S. KARUNANIDHI
Controller of Examinations

UNIVERSITY BUILDINGS,
CHENNAI - 600 005

Change(s)/Overscript(s) should bear the attestation of the Competent authority of the University with official seal, else the Certificate is invalid

UNIVERSITY OF MADRAS

B.E. DEGREE EXAMINATION [THIRD SEMESTER]

STATEMENT OF MARKS · 2K\ 0252821

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S 06-07-83	8012166	1433	48221

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	OCT '2001	MPK 11-02-2002

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	TOT	RESULT
3	APPLIED MATHEMATICS	CE301	054	017	071	P
3	ELECTRICAL ENGINEERING	EC302	053	018	071	P
3	ELECTROMAGNETIC THEORY	EC304	045	017	062	P
3	SIGNALS & SYSTEMS	EC305	029	018	047	F
3	DIGITAL SYSTEMS & COMPUTER ARCHITECTURE	EC306	041	016	057	P
3	ELECTRON DEVICES	EE303	043	018	061	P
3	CIRCUIT THEORY	EE307	066	019	085	P
3	ELECTRON DEVICES & DIGITAL LAB	EC308	067	019	086	P
3	ELECTRICAL ENGINEERING LAB	EC309	061	018	079	P

* * * END OF STATEMENT * * *
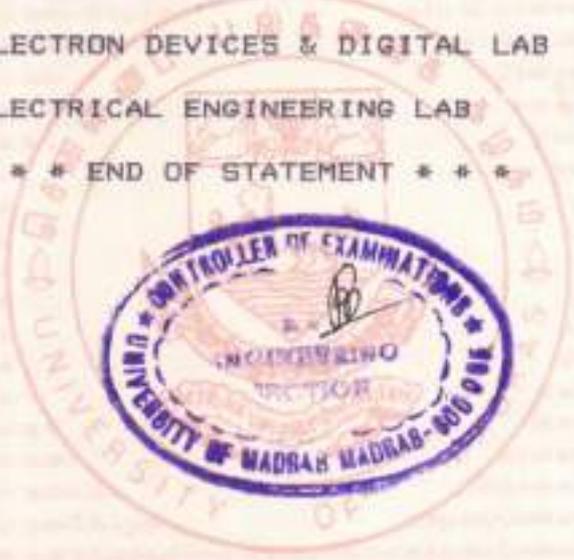
UE - University Exam Marks
IA - Internal Assessment Marks
Passing Minimum: 35 Marks in UE and 50 Marks in Aggregate
P - PASS F - FAIL A - ABSENT
'aaa' Not Obtained Minimum marks in UE
Maximum Marks: UE - 80, IA - 20

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UNIVERSITY OF MADRAS

B. E. DEGREE EXAMINATION [FOURTH SEMESTER]

STATEMENT OF MARKS   2K\ 0024518

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S 06-07-83	8012166	1433	44471

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	APR. 2002	JVK 03-07-2002

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	TOT	RESULT
3	SIGNALS & SYSTEMS	EC305	AAA	018	AAA	F
4	TRANSMISSION LINES & WAVE GUIDES	EC402	AAA	017	AAA	F
4	NETWORK THEORY & SYNTHESIS	EC403	043	018	061	P
4	ELECTRONIC CIRCUITS - I	EC404	051	017	068	P
4	ANALOG COMMUNICATION	EC405	016	017	033	F
4	OBJECT ORIENTED PROGRAMMING IN C++	EC406	036	017	053	P
4	HIGHER MATHEMATICS	EE401	053	015	068	P
4	SOFTWARE ENGINEERING	EE407	050	019	069	P
4	ELECTRONIC CIRCUITS LAB I	EC408	069	017	086	P
4	OBJECT ORIENTED PROGRAMMING C++LAB	EC409	060	019	079	P

* * * END OF STATEMENT * * *

UE - University Exam Marks
IA - Internal Assessment Marks
Passing Minimum: 35 Marks in UE and 50 Marks in Aggregate
P - PASS   F - FAIL   A - ABSENT
'***' Not Obtained Minimum marks in UE
Maximum Marks: UE - 80,   IA - 20

PROF. S. KARUNANIDHI
Controller of Examinations

Change(s)/Overscript(s) should bear the attestation of the Competent authority of the University with official seal, else the Certificate is invalid

சென்னைப் பல்கலைக்கழகம்
UNIVERSITY OF MADRAS

B. E. DEGREE EXAMINATION [FIFTH SEMESTER]

STATEMENT OF MARKS   2001/0524969

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S 06-07-83	8012166	1433	28735

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	OCT. 2002	IMK 30-12-2002

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	TOT	RESULT
3	SIGNALS & SYSTEMS	EC305	036	018	054	P
4	TRANSMISSION LINES & WAVE GUIDES	EC402	038	017	055	P
4	ANALOG COMMUNICATION	EC405	044	017	061	P
5	ELECTRONIC CIRCUITS II	EC501	043	017	060	P
5	LINEAR & DIGITAL INTEGRATED CIRCUITS DESIGN	EC502	043	019	062	P
5	MICROPROCESSOR & APPLICATIONS	EC503	037	017	054	P
5	PRINCIPLES OF COMMUNICATION THEORY	EC504	052	018	070	P
5	DIGITAL COMMUNICATION	EC505	037	018	055	P
5	ANTENNAS & WAVE PROPAGATION	EC506	036	017	053	P
5	DIGITAL SIGNAL PROCESSING	EC507	045	018	063	P
5	ELECRONICS CIRCUITS LAB II	EC508	060	017	077	P
5	SYSTEM PROGRAMMING LAB	EC509	051	019	070	P

* * * END OF STATEMENT * * *

UE - University Exam Marks
IA - Internal Assessment Marks
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Maximum Marks: UE - 80,   IA - 20

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Controller of Examinations

UNIVERSITY BUILDINGS,
CHENNAI - 600 005

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S 06-07-83	8012166	1433	31912

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	APR. 2003	KMI 23-06-2003

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	TOT	RESULT
6	MICROWAVE ENGINEERING	EC601	050	017	067	P
6	COMMUNICATION SYSTEMS	EC602	053	020	073	P
6	INSTRUMENTATION DEVICES & SYSTEMS	EC603	053	018	071	P
6	VLSI DESIGN & CIRCUITS	EC604	060	017	077	P
6	OPTICAL COMMUNICATION	EC605	057	019	076	P
6	DATABASE MANAGEMENT AND DESIGN	EC606	056	018	074	P
6	COMMUNICATION LAB I	EC607	076	020	096	P
6	MICROPROCESSOR LAB	EC608	068	019	087	P

* * * END OF STATEMENT * * *

UE - University Exam Marks
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P - PASS F - FAIL A - ABSENT
'***' Not Obtained Minimum marks in UE
Maximum Marks: UE - 80, IA - 20

PROF. S. KARUNANIDHI
Controller of Examinations

NAME OF CANDIDATE	REGISTER NO. / ENROLMENT NO.	CENTRE NO.	SERIAL NO.
JEYAMARY S 06-07-83	5012166	1433	21182

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	OCT. 2003	SPT 26-12-2003

SEM.	NAME OF SUBJECT	SUBJECT CODE	UE	IA	TOT	RESULT
7	ADVANCED COMMUNICATION ENGINEERING	EC701	060	019	079	P
7	LINEAR & DIGITAL CONTROL SYSTEM	EC703	056	019	075	P
7	SATELLITE COMMUNICATION	EC704	050	019	067	P
7	ADVANCED MICROPROCESSORS AND MICROCONTROLLERS	EC734	049	016	065	P
7	TELEVISION ENGINEERING	EC762	048	019	067	P
7	PRINCIPLES OF MANAGEMENT & ORGANISATIONAL BEHAVIOUR	EE702	055	019	074	P
7	COMMUNICATION LAB II	EC707	068	018	086	P
7	CONTROL ENGINEERING LAB	EC708	072	019	091	P

* * * END OF STATEMENT * * *

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P - PASS   F - FAIL   A - ABSENT
'***' Not Obtained Minimum marks in UE
Maximum Marks: UE - 80,   IA - 20

UNIVERSITY BUILDINGS,
CHENNAI - 600 005

Dr. V. BALU, Ph. D.
Controller of Examinations

NAME OF CANDIDATE	REGISTER NO./ ENROLMENT NO.	CENTRENO	SERIAL NO
JEYAMARY S 06-07-83	8012166	1433	17175

BRANCH NAME	MONTH & YEAR	PUBLICATION CODE AND DATE
ELECTRONICS & COMM. ENGG.	APR. 2004	TKB 01-07-2004

SEM.	NAME OF SUBJECT	SUBJECT CODE	MARKS SECURED UE	IA	XXXX TO XXXX MAX TOT	RESULT
8	TELECOMMUNICATION SWITCHING NETWORKS	EC801	067	018	085	P
8	COMPUTER COMMUNICATION	EC802	054	017	071	P
8	CELLULAR MOBILE COMMUNICATION	EC803	057	019	076	P
8	DIGITAL IMAGE PROCESSING	EC841	061	020	081	P
8	RADARS AND NAVIGATIONAL AIDS	EC851	070	018	088	P
8	PROJECT WORK & VIVA VOCE	EC806	045	036	081	P
8	COMPREHENSIVE VIVA	EC807	066	---	066	P

* * * END OF STATEMENT * * *

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Maximum Marks: UE - 80,   IA - 20

PROF. N. BALASUBRAMANIAN, Ph. D.,

ANNA UNIVERSITY OF TECHNOLOGY TIRUNELVELI

TIRUNELVELI - 627 007

M.E. DEGREE EXAMINATIONS

CONSOLIDATED STATEMENT OF GRADES

Folio No: DPG0901639

NAME OF THE CANDIDATE	JEYAMARY S	
REGISTER NO.	96809401010	
COLLEGE OF STUDY	SARDAR RAJA COLLEGE OF ENGINEERING	
MONTH & YEAR OF LAST APPEARANCE	APRIL 2011	
PROGRAMME & BRANCH	M.E. - Applied Electronics	
REGULATIONS	2009	

SEM. NO.	SUBJECT CODE	SUBJECT TITLE	CREDITS	GRADE	MONTH & YEAR OF PASSING
01	AP911	Advanced Digital Signal Processing	3	D	DEC 2009
01	AP912	Advanced Digital System Design	3	D	DEC 2009
01	AP913	Advanced Microprocessors and Micro Controllers	3	D	DEC 2009
01	MA908	Applied Mathematics for Electronics Engineers	4	E	JUN 2010
01	VL912	VLSI Design Techniques	3	C	DEC 2009
01	AP953	ROBOTICS	3	D	DEC 2009
01	AP916	Electronics Design Lab I	2	S	DEC 2009
02	AP921	Analysis and Design of Analog Integrated Circuits	3	B	JUN 2010
02	AP922	Computer Architecture and Parallel Processing	3	B	JUN 2010
02	AP923	Digital Control Engineering	3	B	JUN 2010
02	AP924	Embedded Systems	3	C	JUN 2010
02	AP951	Digital Image Processing	3	A	JUN 2010
02	AP952	Neural Network And Its Applications	3	C	JUN 2010
02	AP926	Electronics Design Lab II	2	S	JUN 2010
03	AP958	RF System Design	3	D	DEC 2010
03	CP912	High Performance Computer Networks	3	C	DEC 2010
03	VL952	Low Power VLSI Design	3	D	DEC 2010
03	AP931	Project Work (Phase I)	6	A	DEC 2010
04	AP941	Project Work (Phase II)	12	S	APR 2011

*** End of Statement ***

Classification: FIRST CLASS

Semester	I	II	III	IV	V	VI	VII	VIII	IX	X
Grade Point Average (GPA)	6.333	8.050	7.400	10.000						
Total Credits Earned	68	Cumulative Grade Point Average (CGPA)					7.72	Medium of Instruction: ENGLISH		

* Previously Anna University Tirunelveli

Tirunelveli - 627 007
Date : 11 October 2011

SIGNATURE OF THE STUDENT

CONTROLLER OF EXAMINATIONS

Range	91 - 100	81 - 90	71 - 80	61 - 70	57 ▮ 60	50 ▮ 56
Grade	S	A	B	C	D	E
Points	10	9	8	7	6	5