



ANNA UNIVERSITY TIRUNELVELI



DEGREE EXAMINATIONS CONSOLIDATED STATEMENT OF MARKS

Folio No. : F08000187

NAME OF THE CANDIDATE	GOPU T	REGISTER NO.	95508419001
COLLEGE OF STUDY	FRANCIS XAVIER ENGINEERING COLLEGE	MONTH & YEAR OF LAST APPEARANCE	JUN 2010
PROGRAMME & BRANCH	M.E. - VLSI Design	REGULATIONS	2007

SEM. NO.	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING
01	AN1602	Advanced Digital System Design	100	50	065	DEC 2008
01	AN1603	VLSI Design Techniques	100	50	065	DEC 2008
01	MA1616A	Applied Mathematics for Electronics Engineers	100	50	058	DEC 2008
01	VL1601	DSP Integrated Circuits	100	50	068	DEC 2008
01	VL1602	Solid State Device Modeling and Simulation	100	50	058	DEC 2008
01	VL1603	VLSI Design Laboratory I	100	50	079	DEC 2008
01	VL1624	CMOS VLSI Design	100	50	059	DEC 2008
02	AN1625	ASIC Design	100	50	062	MAY 2009
02	AN1629	Electromagnetic Interference and Compatibility in S	100	50	060	MAY 2009
02	AN1651	Analysis and Design of Analog Integrated Circuits	100	50	061	MAY 2009
02	AN1652	Computer Architecture and Parallel Processing	100	50	059	DEC 2009
02	AN1654	Embedded Systems	100	50	060	MAY 2009
02	VL1651	Computer Aided Design of VLSI Circuits	100	50	063	MAY 2009
02	VL1652	VLSI Design Lab II	100	50	086	MAY 2009
03	VL1621	Testing of VLSI Circuits	100	50	086	DEC 2009
03	VL1622	Low Power VLSI Design	100	50	084	DEC 2009
03	VL1627	VLSI Technology	100	50	059	DEC 2009
04	VL1751	Project Work (Phase II)	600	300	519	JUN 2010

*** End of Statement ***

Classifications : FIRST CLASS

Total Marks : 1651 / 2300

Percentage (rounded to nearest integer) : 72

This document is issued without any correction
Medium of instruction : English

Tirunelveli - 627 007
Date : 04-Oct-2010

T.Gopu

SIGNATURE OF THE STUDENT



M. [Signature]

CONTROLLER OF EXAMINATIONS : /c