



KALASALINGAM

ACADEMY OF RESEARCH AND EDUCATION

(DEEMED TO BE UNIVERSITY)

(Under Section 3 of UGC Act 1956)
Srivilliputtur (Via), Virudhunagar (Dt.) Tamil Nadu

Sl. No : PG 20244132



M.Tech. Degree Examinations

CONSOLIDATED GRADE CARD

Name of the Student: CHIRALA CHAITANYA LAXMI PRASAD

Register No : 9923152003

Branch: VLSI Design

Batch: 2023-2025

COURSE CODE	NAME OF THE COURSE	COURSE CATEGORY	CREDITS	LETTER GRADE	MONTH & YEAR OF PASSING
CGPA COURSES					
222VLSI5101	Solid State Device Modelling	PC	4.0	C	Nov/Dec 2023
222VLSI5102	Testing of VLSI Circuits	PC	4.0	D	Nov/Dec 2023
222VLSI5103	Digital IC Design	PC	4.0	C	Nov/Dec 2023
222VLSI5104	Low Power VLSI Design	PC	4.0	B	Nov/Dec 2023
222VLSI5205	Solid State Device Modelling Laboratory	PC	2.0	A	Nov/Dec 2023
222VLSI5206	Digital IC Design Laboratory	PC	2.0	A	Nov/Dec 2023
221MAT5103	Applied Mathematics	SC	3.0	E	Nov/Dec 2023
222VLSI5107	Analog IC Design	PC	4.0	D	Apr/May 2024
222VLSI5108	RTL Design and Verification with system verilog	PC	4.0	B	Apr/May 2024
222VLSI5109	VLSI Interconnects and its Design Techniques	PC	4.0	D	Apr/May 2024
222VLSI5110	Machine Learning in Computer Aided VLSI Design	PC	4.0	B	Apr/May 2024
222VLSI5211	Analog IC Design Laboratory	PC	2.0	A	Apr/May 2024
222VLSI5212	RTL Design and Verification with SystemVerilog Laboratory	PC	2.0	A	Apr/May 2024
221ISE5101	Research Methodology	SC	2.0	E	Apr/May 2024
224VLSI7246	Semester Long Internship	EE	15.0	S	Nov/Dec 2024
223VLSI7201	Capstone Project	EC	20.0	A	Apr/May 2025

CREDIT SUMMARY

TOTAL TO BE EARNED	EARNED	CGPA	CLASS AWARDED
80.0	80.0	8.14	First Class

Place: Krishnankoil

Date: 12/06/2025



Signature

Controller of Examinations

Please see overleaf for details