SRM UNIVERSITY

Established U/S 3 of UGC Act 1956

Folio No. **C130787**

0099710

M.Tech. DEGREE EXAMINATION

CONSOLIDATED GRADE CARD

NAME OF THE CANDIDATE	PRASANA P [19-May-1989]	REGISTER NUMBER	1581110007
BRANCH / SPECIALISATION	VLSI DESIGN	MONTH & YEAR OF LASTAPPEARANCE	MAY 2013

Year/ Sem.	Course Code	Course Title	Credit	Grade	Att. Code	Month & Year of Passing
1	MA0515	GRAPH THEORY AND OPTIMIZATION TECHNIQUES	3	C-	8	DEC 2011
1	VL0501	PROGRAMMING IN HDL	4	B-	9	DEC 2011
1	VL0502	DIGITAL SIGNAL PROCESSING STRUCTURES FOR VLSI	4	C+	9	DEC 2011
1	VL0507	DIGITAL VLSI DESIGN	4	C+	H	DEC 2011
1	VL0562	NANO ELECTRONICS	3	B-	H	DEC 2011
2	VL0503	DSP ARCHITECTURES AND APPLICATIONS	4	D	9	MAY 2012
2	VL0504	CMOS ANALOG CIRCUIT DESIGN	4	B-	9	MAY 2012
2	VL0505	TESTING OF VLSI CIRCUITS	4	C+	9	MAY 2012
2	VL0509	ASIC DESIGN	3	C	9	MAY 2012
2	VL0551A	LOW POWER VLSI DESIGN	3	C+	8	MAY 2012
3	CS0601	WIRELESS COMMUNICATION AND NETWORKS	3	C+	9	NOV 2012
3	VL0554	VLSI DESIGN AUTOMATION	3	C-	9	NOV 2012
3	EM0566	FPGA BASED DESIGN	3	C+	9	NOV 2012
3	VL0603	SEMINAR	1	C+	9	NOV 2012
3	VL0601	PROJECT WORK - PHASE - I	6	D	-	NOV 2012
4	VL0602	PROJECT WORK - PHASE - II	18	C	-	MAY 2013

*** End of Statement ***

CGPA : 6.557

SRM Nagar
Kattankulathur - 603 203
Kancheepuram (Dt), Tamil Nadu, India.

Date :17-Jun-2013

This sheet bears no correction

CONTROLLER OF EXAMINATIONS

GRADING

Letter Grade	A+	A	A−	B+	B	B−	C+	C	C−	D	E	U	W	I
Grade Points	10	9.5	9.0	8.5	8.0	7.5	7.0	6.5	6.0	5.0	4.0	0	0	0

U – Failure due to insufficient marks

W – Failure due to insufficient attendance

I – Incomplete due to absent

ATTENDANCE CODE (Att. Code)

Attendance Percentage	95% and above	85 to 94%	75 to 84%	Below 75%
Code	H	9	8	L

CALCULATION OF CGPA

The Cumulative Grade Point Average (CGPA) = $\dfrac{\Sigma(C \times GP)}{\Sigma C}$

Where, C = Credit of the course
GP = Grade Points obtained for the course

CGPA is calculated considering all the courses taken from first semester onwards.

Read by :	K.S. Ruy
Verified by :	

ANNA UNIVERSITY CHENNAI, CHENNAI - 600 025

B.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF MARKS

Field	Value
NAME OF THE CANDIDATE	PRASANA-P
COLLEGE OF STUDY	206: Maamallan Institute of Technology
PROGRAMME & BRANCH	B.E. Electronics and Communication Engineering
REGISTER NO.	20606106042
MONTH & YEAR OF LAST APPEARANCE	April 2010
REGULATIONS	2004
Folio No.	B954741
	P11809945031?

SEM	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING
01*	CY1X01	Engineering Chemistry	100	50	067	APR 2007
01*	EC1X01	Electron Devices	100	50	058	APR 2007
01*	EC1X02	Circuit Analysis	100	50	062	APR 2007
01*	GE1X01	Engineering Graphics	100	50	065	APR 2007
01*	GE1X02	Computer Programming	100	50	055	APR 2007
01*	HS1X01	Technical English	100	50	069	APR 2007
01*	MA1X01	Engineering Mathematics - I	100	50	069	APR 2007
01*	PH1X01	Engineering Physics	100	50	082	APR 2007
01*	GE1X03	Engineering Practices Laboratory	100	50	094	APR 2007
01*	PC1X01	Physics and Chemistry Laboratory	100	50	094	APR 2007
03	CS1151	Data Structures	100	50	055	NOV 2007
03	CY1201	Environmental Science and Engineering	100	50	066	NOV 2007
03	EC1201	Digital Electronics	100	50	073	NOV 2007
03	EC1203	Electronic Circuits - I	100	50	069	NOV 2007
03	EE1211	Electrical Machines	100	50	068	NOV 2007
03	MA1201	Mathematics - III	100	50	077	NOV 2007
03	CS1152	Data Structure Lab	100	50	088	NOV 2007
03	EC1204	Electronic Devices and Circuits Lab I	100	50	093	NOV 2007
04	EE1261	Electrical Machines Lab	100	50	085	APR 2008
04	EC1251	Electronic Circuits II	100	50	068	APR 2008
04	EC1252	Signals and Systems	100	50	059	APR 2008
04	EC1253	Electromagnetic Fields	100	50	073	APR 2008
04	EC1254	Linear Integrated Circuits	100	50	062	APR 2008
04	EC1255	Measurements and Instrumentation	100	50	067	APR 2008
04	MA1254	Random Processes	100	50	059	APR 2008
04	EC1256	Electronics circuits II and simulation lab	100	50	095	APR 2008
04	EC1257	Linear Integrated Circuit Lab	100	50	093	APR 2008
04	EC1258	Digital Electronics lab	100	50	089	APR 2008
05	EC1301	Communication Theory	100	50	064	NOV 2008
05	EC1302	Digital Signal Processing	100	50	061	NOV 2008
05	EC1303	Microprocessors and Its Applications	100	50	056	NOV 2008
05	EC1304	Control Systems	100	50	072	NOV 2008
05	EC1305	Transmission Lines and Waveguides	100	50	066	NOV 2008
05	MA1251	Numerical Methods	100	50	073	NOV 2008
05	EC1306	Digital Signal Processing Lab	100	50	086	NOV 2008
05	EC1307	Microprocessor and Application Lab	100	50	088	NOV 2008
05	CS1251	Computer Architecture	100	50	058	APR 2009
06	CS1302	Computer Networks	100	50	063	APR 2009
06	EC1351	Digital Communication	100	50	060	APR 2009
06	EC1352	Antenna and Wave Propagation	100	50	056	APR 2009
06	MG1351	Principles of Management	100	50	052	APR 2009
06	EC1006	Medical Electronics	100	50	065	APR 2009
06	EC1353	Communication System Lab	100	50	087	APR 2009
06	EC1354	Networks Lab	100	50	092	APR 2009
06	EC1355	Electronic System Design Lab	100	50	087	APR 2009
06	GE1352	Communication Skills Laboratory	100	50	074	APR 2009
07	EC1009	Digital Image Processing	100	50	050	NOV 2009
07	EC1401	VLSI Design	100	50	050	APR 2010
07	EC1402	Optical Communication	100	50	050	NOV 2009
07	EC1403	Microwave Engineering	100	50	054	NOV 2009
07	EC1003	Computer Hardware and Interfacing	100	50	056	NOV 2009
07	MG1401	Total Quality Management	100	50	058	NOV 2009
07	EC1404	VLSI Lab	100	50	056	NOV 2009
07	EC1405	Optical and Microwave Lab	100	50	082	NOV 2009
08	EC1451	Mobile Communication	100	50	064	APR 2010
08	EC1013	Wireless networks	100	50	057	APR 2010
08	EC1014	Telecommunication Switching and Networks	100	50	054	APR 2010
08	EC1452	Project Work	200	100	165	APR 2010

Classification: FIRST CLASS

Total Marks (from 3rd to 8th semester): 3400 / 4900

Percentage (rounded to nearest integer): 69

* Annual Pattern is followed in the First Year of study and Semester Pattern is followed from Second Year onwards.

End of Statement

Medium of Instruction : ENGLISH

Chennai - 600 025.

Date : 01/07/2010

Controller of Examinations