



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Eighth Semester (Electrical & Electronics Engineering)



F^J₁₂ 091129

Name of the Student : SHAMEEM P C

Father's / Mother's Name : ABDULLA P C

Name of the College : RAJIV GANDHI INSTITUTE OF TECHNOLOGY, BANGALORE

Seat No. : 1RG09EE038

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Month & Year of Passing
	Code	Title	Max	Min	Obtained	Max	Min	Obtained	Max	Min	Obtained	
1	10EE81	Electrical Design, Estimating & Costing	100	35	59	25	-	24	125	50	83	Jul 14
2	10EE82	Power System Operation and Control	100	35	61	25	-	23	125	50	84	Jul 14
3	10EE836	Renewable Energy Sources	100	35	65	25	-	21	125	50	86	Jul 14
4	10EE844	Electrical Distribution Systems	100	35	75	25	-	20	125	50	95	Jul 14
5	10EEP85	Project Work	100	40	93	100	50	98	200	90	191	Jul 14
6	10EES86	Seminar	-	-	-	50	20	49	50	20	49	Jul 14

GRAND TOTAL 750 310 588 *

*First Attempt Total (in words) : FIVE HUNDRED AND EIGHTY EIGHT ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

Results of V to VIII Semesters :

Semesters	V	VI	VII	VIII	V TO VIII
Marks at First Attempt	485	491	607	588	2171
Total Marks (Max.)	900	900	900	750	3450
Result	SC	SC	FC	FCD	FC



[Signature]

Registrar (Evaluation)

First Attempt Total (V to VIII Sem) in words : TWO THOUSAND ONE HUNDRED AND SEVENTY ONE ONLY

Result of the Degree : FIRST CLASS

Date : AUG 22, 2014



POST GRADUATION PROGRAMME UNDER CREDIT SEMESTER SYSTEM

FINAL GRADE SHEET

Date : 05.11.2016

NAME OF PROGRAMME: MASTER OF TECHNOLOGY IN DIGITAL SIGNAL PROCESSING (ELECTRONICS ENGINEERING)

NAME OF CANDIDATE: SHAMEEM P C **REGISTER NO.:** CTAOCDP010

MONTH & YEAR: JUNE, 2016

The following is the cumulative grades awarded to the candidate at the Final Semester Degree Examination (CUCSS), JUNE 2016

Course Code	Course Title	Credits	Letter Grade	Credit Point	Date Of Exam
SEMESTER I					
DSP10 101	LINEAR ALGEBRA FOR SIGNAL PROCESSING	4	D	24.00	1/2015
DSP10 102	DSP ALGORITHMS AND ARCHITECTURE	4	C	28.00	1/2016
DSP10 103	RANDOM PROCESSES AND APPLICATIONS	4	D	24.00	1/2016
DSP10 104	MULTIRATE SIGNAL PROCESSING	4	D	24.00	1/2016
DSP10 105C	IMAGE PROCESSING	4	D	24.00	1/2015
DSP10 106P	DSP SYSTEMS LAB	2	D	12.00	1/2015
DSP10 107P	SEMINAR	2	B	16.00	1/2015
Semester-I		SGPA-I : 6.33			
SEMESTER II					
DSP10 201	WAVELET THEORY	4	D	24.00	6/2015
DSP10 202	ADAPTIVE SIGNAL PROCESSING	4	D	24.00	6/2015
DSP10 203	ESTIMATION AND DETECTION THEORY	4	C	28.00	6/2015
DSP10 204A	SIGNAL COMPRESSION TECHNIQUES	4	D	24.00	6/2015
DSP10 205D	GRAPH THEORY	4	D	24.00	6/2015
DSP10 206P	SIGNAL PROCESSING LAB	2	C	14.00	6/2015
DSP10 207P	SEMINAR	2	B	16.00	6/2015
Semester-II		SGPA-II : 6.42			
SEMESTER III					
DSP 10 301B	BIOMEDICAL SIGNAL PROCESSING	4	C	28.00	12/2015
DSP 10 302A	VLSI STRUCTURE FOR DSP	4	D	24.00	12/2015
DSP 10 303P	INDUSTRIAL TRAINING	1	A	9.00	12/2015
DSP 10 304P	MASTER RESEARCH PROJECT(PHASE-I)	6	A	54.00	12/2015
Semester-III		SGPA-III : 7.67			
SEMESTER IV					
DSP10 404P	MASTER RESEARCH PROJECT (PHASE-II)	12	B	96.00	6/2016
Semester-IV		SGPA-IV : 8.0			
Programme Total Credits Acquired : 75 (Core: 55, Elective: 20, Dissertation: 0) CGPA : 6.89					



Prepared by

Checked by

Section Officer

AR/DR/JR (Exams)

[Signature]
Controller of Examinations