Alagappa Chettiar College of Engineering and Technology, Karaikudi - 630 004

(A Government Autonomous Institution Affiliated to Anna University)

Sl. No. 006855

GRADE SHEET

FOLIO NO. 09743

NAME OF THE CANDIDATE	BAKIALAKSHMI. D.				REGISTER NO.	0934501
DATE OF BIRTH	10-04-1978	GENDER	FEMALE	MONTH & YEAR OF EXAMINATION ---	DATE OF PUBLICATION	---
FACULTY OF STUDY	Information and Communication Engineering				REGULATIONS	2009
PROGRAMME	B.E.(PT) Electronics and Communication Engineering					

SEM. NO.	SUBJECT CODE	NAME OF THE SUBJECT	CREDITS	LETTER GRADE	GRADE POINT	RESULT
1	PTMA101	Mathematics-1	4	D	6	P-Dec09
1	PTPS102	Physical Sciences	4	C	7	P-Dec09
1	PTEC103	Digital Computer Principles	4	C	7	P-Dec09
1	PTEC104	Electronic Devices	4	D	6	P-Dec09
1	PTEE106	Measurements and Instrumentation	4	C	7	P-Dec09
2	PTMA201	Mathematics-II	4	C	7	P-May10
2	PTCH202	Environmental Science and Engineering	4	B	8	P-May10
2	PTEC203	Electronic Circuits I	4	D	6	P-May10
2	PTEC204	Electrical Machines	4	C	7	P-May10
2	PTEC205	Electromagnetic Fields and Waves	4	C	7	P-May10
3	PTEC301	Signals and Systems	4	B	8	P-May12
3	PTEC302	Electronic Circuits II	4	C	7	P-Dec10
3	PTEC303	Linear Integrated Circuits	4	C	7	P-Dec10
3	PTEC304	Transmission Lines and Wave Guides	4	C	7	P-Dec10
3	PTEC305	Control Systems	4	D	6	P-Dec10
4	PTEC401	Communication Theory and Systems	4	C	7	P-May11
4	PTEC402	Microprocessor and its Applications	4	D	6	P-May11
4	PTEC403	Digital Signal Processing	4	C	7	P-Dec11
4	PTEC404	Computer Networks	4	D	6	P-May11
4	PTEC405	Microprocessor and Control Systems Laboratory	3	B	8	P-May11
5	PTEC501	Digital Communication	3	D	6	P-Dec11
5	PTEC502	Operating System	3	D	6	P-Dec11
5	PTEC503	RF and Microwave Engineering	3	D	6	P-Dec11
5	PTEC037	Professional Ethics in Engineering	3	C	7	P-Dec11
5	PTEC505	Communication System and Network Laboratory	3	A	9	P-Dec11
6	PTEC601	VLSI Design	3	D	6	P-May12
6	PTEC602	Optical Communication and Networking	3	C	7	P-May12
6	PTEC603	Antennas and Wave Propagation	3	D	6	P-May12
6	PTEC033	Wireless & Mobile Communication	3	C	7	P-May12
6	PTEC605	VLSI Design Laboratory	3	S	10	P-May12
7	PTEC014	Satellite Communication	3	C	7	P-Dec12
7	PTEC701	Total Quality Management	3	C	7	P-Dec12
7	PTEC020	Television and Video Engineering	3	C	7	P-Dec12
7	PTEC034	Medical Electronics	3	D	6	P-Dec12
7	PTEC705	Project Work	3	S	10	P-Dec12

****End of Statement***

Semester	I	II	III	IV	V	VI	VII	VIII
Credits Registered								
Credits Earned								
Grade Points Earned								
Grade Point Average (GPA)								
Cumulative Credits Earned								

Cumulative Grade Point Average (CGPA) : 6.944

Classification : FIRST CLASS

Cumulative Grade Point Average (CGPA)

Medium of Instruction : ENGLISH

RA - Reappearance is Required   W - Withdrawal   AB - Absent for End Semester Examination

Karaikudi
Date : 21-07-13

SIGNATURE OF THE CANDIDATE

CONTROLLER OF EXAMINATIONS

ANNA UNIVERSITY, CHENNAI - 25
M.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES

B092024749543D   Folio No. AUY11716732

NAME OF THE CANDIDATE	BAKIALAKSHMI D				REGISTER NO.		912413419002
DATE OF BIRTH	10-APR-78	GENDER	FEMALE	MONTH & YEAR OF LAST APPEARANCE		JUN 2015	
COLLEGE OF STUDY	SHANMUGANATHAN ENGINEERING COLLEGE				REGULATIONS		2013
PROGRAMME & SPECIALIZATION	M.E. VLSI Design						

SEM. NO.	COURSE CODE	COURSE TITLE	CREDITS	LETTER GRADE	GRADE POINT	MONTH & YEAR OF PASSING
01	MA7157	Applied Mathematics for Electronics Engineers	4	C	7	JAN 2014
01	VL7101	VLSI Signal Processing	3	D	6	JAN 2014
01	VL7102	VLSI Design Techniques	3	C	7	JAN 2014
01	VL7103	Solid State Device Modeling and Simulation	3	C	7	JAN 2014
01	AP7202	ASIC and FPGA Design	3	D	6	JAN 2014
01	VL7002	Security solutions in VLSI	3	D	6	JAN 2014
01	VL7111	VLSI Design Laboratory I	2	S	10	JAN 2014
02	AP7201	Analysis and Design of Analog Integrated Circuits	3	C	7	NOV 2014
02	VL7201	CAD for VLSI Circuits	3	D	6	JUN 2014
02	VL7202	Low Power VLSI Design	3	D	6	JUN 2014
02	AP7016	System on Chip Design	3	D	6	NOV 2014
02	CU7001	Real Time Embedded Systems	3	B	8	NOV 2014
02	VL7005	Physical Design of VLSI Circuits	3	C	7	NOV 2014
02	VL7211	VLSI Design Laboratory II	2	S	10	JUN 2014
03	VL7301	Testing of VLSI Circuits	3	C	7	JUN 2015
03	VL7011	Signal Integrity for High Speed Devices	3	C	7	NOV 2014
03	VL7014	IP Based VLSI Design	3	D	6	NOV 2014
03	VL7311	Project Work (Phase I)	6	S	10	NOV 2014
04	VL7411	Project Work (Phase II)	12	S	10	JUN 2015

* * * End of Statement * * *

Cumulative Grade Point Average : 7.71

Classification : FIRST CLASS

Medium of Instruction : ENGLISH

Chennai - 25
Date 20/07/2015

SIGNATURE OF THE STUDENT

CONTROLLER OF EXAMINATIONS