KALASALINGAM
ACADEMY OF RESEARCH AND EDUCATION
(DEEMED TO BE UNIVERSITY)
Under sec. 3 of UGC Act 1956. Accredited by NAAC with "A" Grade

Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:30-06-2020

Department	ELECTRONICS AND COMMUNICATION ENGINEERING	Application No.	20200175
Area of Research	EMBEDDED SYSTEMS	Research Mode	PART TIME

Name :YERRABOINA SREENIVASULU

Date of Birth / Age :02-01-1966 / 54 Years

Gender :MALE

Category :BC

e-Mail ID :yerraboina@yahoo.com

Mobile :08978389238

Father's/Husband's Name	YERRABOINA SUBBAIAH	Father's/Husband's Occupation	CULTIVATION
Family Income	800000	Residental Type	RURAL
Birth Place	BHAVAPURAM	Mother Tongue	TELUGU
Religion	HINDU	Martial Status	MARRIED
Aadhaar No.	243556355797	PAN No.	BPGPS6322G
Physically Challenged	NO	Type of Disability	-

Address for Communication:
H.NO: 1-10-171/2
TEMPLE-ALWAL
SECUNDERABAD
SECUNDERABAD DISTRICT
TELANGANA
INDIA
Pin-500010

Permenant Address:
2
BHAVAPURAM (VILLAGE)
GIDDALURE
PRAKASAM DISTRICT
ANDHRA PRADESH
INDIA
Pin-523373

Qualification

Degree	Discipline	College/university	Year Passed	AVG/CGPA	Class	Mode
M.TECH	EC	SKEC,JNTUH	2010	69.3	I	REGULAR
B.TECH	ECE	SVHCET, ANU	1989	56.7	II	REGULAR
INTERMEDIATE	MPC	G.J.COLLEGE	1984	58.2	II	REGULAR
SSC	COMPOSITE MATHS	G.J.COLLEGE	1982	63.33	I	REGULAR

Experience

Organization	Designation	Experience From	Experience TO	Work Nature
SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY	ASSOC.PROF	2018-02-23	2020-06-30	TEACHING
GVR&S	PROF.	2015-03-02	2018-02-21	TEACHING
CITM	ASSOC.PROF	2014-05-23	2015-02-28	TEACHING
SITS	PROF	0012-09-01	2013-12-16	TEACHING

Payment Details

Transaction ID	Reference	Date of transaction	Amount	Status

| 20200175_200630135048 | SHMP8947171745 | 30-06-2020 | 600 | SUCCESS |

EXPLORATION OF CRYPTOGRAPHIC ALGORITHMS ON EMBEDDED RISC ARCHITECTURE

The protection of sensitive information against unauthorized access or fraudulent changes has been of prime concern throughout the centuries. Modern communication techniques, using computers connected through networks (wired / wireless), make all date even more vulnerable for these threats. Cryptography is an essential process to assure confidentiality over wired/wireless communication channels, these channels are an open medium to intruders in which they can intercept and alter the contents of any transmitted information. Cryptography is the foundation of all information security which deals secrecy and authentication of data. Implementation of secrecy and authentication features for data and sensor data applications on wired/wireless embedded networks becomes crucial, because of stringent resource constraints (SOC processor, Memory, complexity of computational algorithm, protocols, data integrity. QOS, area, power, speed and etc..) to implement on various embedded reconfigurable architectures. To enhance the system performance including these features (secrecy and authentication), the present researchers have lot of scope to further investigation of various security algorithms to implements on modern tinny resource constraints secured embedded systems.

The cryptographic algorithms are categorized in to a block cipher is an encryption / decryption scheme in which a block of data is used to produce the cipher text and a stream cipher is one that encrypts a digital data as a stream of data. The well known block cipher algorithms are TDES, AES and the stream ciphers are RC$, SEAL. The basic functions of any security algorithm consist of transposition, substitution, rotation and confusion. To design or implement above set functions the researchers proposed and proposing various computation procedures. TDES algorithm mainly includes two basic functions transposition and rotation. The AES algorithm uses with the three different key lengths are "AES-128", "AES-192", and "AES-256". The basic transformation functions used in AES algorithm are **sub-bytes, shift-rows, mix-column** and **add-round key**   transformations. Block ciphers uses fixed length key and data. RC4 is a famous stream cipher includes basic functions like key initialization, key setup and key streaming. Stream cipher uses variable key and data lengths.

Reset data communication protocols implementation consists of transferring of data (sensor, text and image) on various embedded networks (wired/wireless) become crucial, because of several constraints like network design, buffering, integrity, QOS and etc. upto now most of the researchers investigated the performance of various protocols implementation on embedded wire and wireless communication networks. They did lot of research on data integrity (error correction and detection) and QOS etc. adding the security (secrecy and authentication) features for existing research work would definitely encourages the present researchers and system implementation engineers upto great extent.

Now a day's implementation of various security algorithms (block and stream ciphers) on embedded reconfigurable RISC architecture, for sensor and image data applications become a challenge job for current researchers, because of resource constraints like area, power, speed etc.. The research work focuses on effective implementation of various security algorithms (Block Ciphers : AES and TDES and Stream Cipher : RC4 and SEAL) for embedded wire and wireless sensor data networks, our research work may extract the desired security algorithms from existing algorithms or proposes the design of new algorithms for resource constraints tinny embedded system applications, the work includes study and investigate the feasibility of various security algorithms, those suits for image data security applications.

To carry out the proposed research work by adapting various industries driven embedded EDA tools, embedded design, simulation & implementation methodologies. Source of the following familiar basic tools, IDE's & ISE's.

Embedded software Tools : Cross compilers, Emulators, Simulators & etc..

Hardware Tools : ORCAD (schematic & PCB) design, simulation & validation.

Languages : Embedded – C, MATLAB : Language & tools box.

Processors : 8 – bit Amtel, 32 – bit embedded ARM RISC Architecture.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

RF 352769
19435539302

420007024726

College Code: C8 (S K B C, KARPALLY)

Mr. Vavadeina Seenivasulu

S/o. Mr. Vavadeina Subbaiah

having fulfilled the academic requirements and passed the examination

held during January - 2010 in First Class

has this day been admitted by the Executive Council to the Degree of

Master of Technology

Electronics & Communication Engineering

Given under the Seal of the University

H.T.No: 07C81D7014

Date: 28-04-2011

DIRECTOR OF EVALUATION   REGISTRAR   VICE-CHANCELLOR

Regd. No. : 85EC435

ACHARYA NAGARJUNA UNIVERSITY
ఆచార్య నాగార్జున విశ్వవిద్యాలయము

FACULTY OF ENGINEERING
ఫాకల్టీ ఆఫ్ ఇంజనీరింగ్

This is to certify that
ధృవీకరణ

Yerraboina Sreenivasulu

Mr/Ms
శ్రీ/శ్రీమతి/కుమారి

Son/Daughter of _____ ---
కుమారుడు/కుమార్తె

has been duly admitted to the Degree of **Bachelor of Technology**
బ్యాచులర్ ఆఫ్ టెక్నాలజి పట్టాప్రదానానికి అర్హతపొందినందున

of this University he/she having been declared to have passed the examination prescribed therefor as follows :

ఈ విశ్వవిద్యాలయం నుంచి కింద సూచించిన విధంగా ఆతడు/ఆమె పట్టాకు నిర్దేశించిన పరీక్షలో ఉత్తీర్ణత పొందినట్లు ప్రకటించబడింది:

Subjects అభ్యసించిన అంశాలు	Month & Year of Passing ఉత్తీర్ణత పొందిన నెల, సంవత్సరం	Class Awarded పొందిన శ్రేణి
Electronics & Communications	June 1989	Second Class

Given under the Seal of the University
విశ్వవిద్యాలయ అధికార ముద్రతో జారీచేయబడినది.

Nagarjuna Nagar, Andhra Pradesh, India
నాగార్జున నగరం, ఆంధ్రప్రదేశ్, భారతదేశము.

Date 2 FEB 2018
తేది

Controller of Examinations
పరీక్షల నిర్వహణాధికారి

BOARD OF SECONDARY EDUCATION
ANDHRA PRADESH

520804 | **SECONDARY SCHOOL CERTIFICATE** | C/G074782

Certified that **YERRABOINA SREENIVASULU** bearing

R. No. **151455** Son/Daughter of XXXXX **YERRABOINA SUBBAIAH**

and belonging to **GOVT JR COLLEGE KOMAROLE** appeared

at the SSC EXAMINATION held in **MAR,1982** and PASSED the Examination in

FIRST division with **TELUGU** as the medium of instruction.

The Date of Birth of the Candidate is

DATE OF BIRTH	DAY		MONTH	YEAR			
01/02/1966	ZERO	ONE	FEB	ONE	NINE	SIX	SIX

The candidate Secured the following Percentage of marks

FIRST LANG.	MARKS	THIRD LANG.	MARKS	MATHEMATICS	MARKS
TELUGU	68	ENGLISH	71	COMPOSITE	57
GENERAL SCIENCE	74	SOCIAL STUDIES	72	TOTAL (in Figs.)	342

TOTAL (In Words)	THREE FOUR TWO

SECOND LANGUAGE (	HINDI	)	46

Marks of Indentification

1. A mole between eye brows
2. A mole in the right palm

V. Venkateswarlu

Head of Institution

PRINCIPAL
Government Junior College
KOMAROLE, Prakasam

Date of issue

HYDERABAD

Addl /Jt Secretary

Board of Secondary Education

The marks with the asterisk indicate the marks secured in the Previous Examinations.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

HYDERABAD - 500 072; ANDHRA PRADESH, INDIA

MEMORANDUM OF MARKS

MEMONO.› B 1"4 9 9200

S.No.   5313301

EXAMINATION:: M. Tech.   I S ear s l er• Re g u l ar

BRANCH : Electronics   g Comnun i c:a1 1 on

NAME YERRADINNA SREENI VASULU

HALL TICKET NO: 07C81D7014

MONTH & YEAR OF EXAM.: I ja   ch   ?G£JB

INSTITUTION: S K EN   K AR PALLY

SNo.	SUBJECT CODE	SUBJECT TITLE	internal Marks	End Exam	Total Marks	Result	Credits
1	A7001	DIGITAL. DATA ZOt4MUNICATIDNS	36	33	69	P	
2	A7002	STATISTICAL SIGNAL PROCESSING	31	35	66	P	
3	A7003	ADVANCED DIGITAL SIGNAL PROCESSING	35	25	60	P	
4	A7004	VLSI ARCH. & DESIGN METHODOLOGIES	35	33	68	P	
5	A7005	CODING THEORY & PRACTICE	37	43	80	P	
6	A7006	SPREAD SPECTRUM TECHNIQUES	39	29	68	P	
7	A7008	COMMUNICATION SYSTEMS SIMULATION (LAB)	40	59	99		

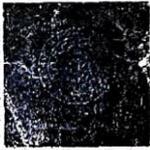SUBJECTS REGISTERED: APPEARED: PASSED:

TOTAL | 253 | 257 | 510

"": /\GGREGATE (IN WORDS)

**** Five One Zero *****

DATE:- 27-04-2008   VERIFIED BY

CONTROLLER OF EXAMINATIONS

-. INSTRUCTIONS:

	MAXIMUM MARKS			MINIlv\UM FOR PASS	
	Internal	End Exam.	Total of Int. & End	End Exam	Total of Int. & End
'TIHEORY/DRAWING./DE SIGN SUBJEGTS	40	60	100	'?4	50
" P PfACTICAL SUBJECTS	40	60	100	24	50

P:PASS   F: FAIL   AB ABSENT

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

MEMORANDUM OF MARKS

MEMO NO.: IM 00147789

S.No.: 582849

EXAMINATION: M.Fch. II semester suppl.

HILL TICKET NO.: e7c‹a 7014

BRANCH: Electronics & Communications

MONTH & YEAR OF EXAM: March, 2009

" /E. YERRABO IN A SREENIVASULU

INSTITUTION: SKEM, KARPALLY

S. No.	SUBJECT CODE	SUBJECT TITLE	INTERNAL MARKS	END EXAM	TOTAL MARKS	RESULT	CREDITS
1	B70O1	ENBEDDED & REQL TIME SYSTEMS	34	24	58	P	

SUBJECTS REGISTERED :	APPEARED :	PASSED :	TOTAL :			
1	1	1		S4	24	58

AGGREGATE (IN WORDS)

**** Zero Five Eight *****

DATE: 16-06-2009

VERIFIED BY

CONTROLLER OF EXAMINATIONS

INSTRUCTIONS :

	MAXIMUM MARKS			MINIMUM FOR PASS	
	Internal	End Exam.	Total of Int. & End	End Exam	Total of Int. & End
THEORY/DRAWING/DESIGN SUBJECTS	40	60	100	24	50
PRACTICAL SUBJECTS	40	60	100	24	50

P: PASS   F: FAIL   AB: ABSENT

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SPURTHI EDUCATIONAL SOCIETY'S

SREEKAVITHA ENGINEERING COLLEGE

(Approved by AICTE, New Delhi & Affiliated to JNTU, Hyderabad)

(ACCREDITED BY NBA)

KAREPALLI, KHAMMAM - 507 122 (Dt.) A.P.

Transfer Certificate

Sl.No. **85**   Admn.No. 07C81D7014

1.	Name of College (Place and District)	:	SREEKAVITHA ENGINEERING COLLEGE Karepalli-507 122, Khammam Dist, A.P.
2.	Name of the student	:	YERRABOINA SREENIVASULU
3.	Name of the Father	:	Y SUBBAIAH
4.	Nationality & Religion	:	INDIAN / HINDU
5.	Date of birth 01/02/1966 (in words) as entered in the Admission Register	:	FIRST- FEBRUARY NINETEEN SIXTY SIX
6.	Whether the candidate belongs to reserved category (i.e. BC,SC or ST) if so, state caste and group.	:	BC-D
7.	Class in which the student was studying at the time of leaving the institution	:	PASSED M.TECH (ECE) & P.G. COURSE
8.	Whether the student qualified for Promotion to higher class	:	-YES-
9.	Date of Admission into the Institution	:	12-10-2007
10.	Date on which the Student leaves or has left the collage	:	23/01/2010
11.	Whether the Student has paid all the fee or dues to the Institution.	:	-YES-
12.	Whether the pupil was in receipt of any scholarship (Nature of the Scholarship to be specified)	:	-NO-
13	Reasons for applying for the Transfer Certificate	:	FURTHER STUDIES
14.	Personal marks of Identification	1):	A MOLE ON THE RIGHT PALM
		2):	-
15.	Date of application for Transfer Certificate	:	07-03-2010
16.	Date of issue of Transfer Certificate	:	07-03-2010
17.	Conduct	:	Good

Date: 07-03-2010

PREPARED BY   SUPERINTENDENT/A.O.

Principal

PRINCIPAL

SREEKAVITHA ENGINEERING COLLEGE

KAREPALLY, KHAMMAM DT. 507

SPURTHI EDUCATIONAL SOCIETY`S

SREE KAVITHA ENGINEERING COLLEGE

(Approved by AICTE, New Delhi & Affiliated to JNTU, Hyderabad)

KAREPALLI, KHAMMAM - 507 122 (Dt.) A.P.

Admn. No. 972

Date : 07|03|2010

STUDY AND CONDUCT CERTIFICATE

This is to certify that Mr./Kum __YERRABOINA SREENIVASULU__

S/o., ~~D/o.~~, __Y SUBBAIAH__ was / is a bonafide student of this

college from __2007-08__ to __2008-09__ and studied /

studying in __M.Tech (ECE)__ Branch with H.T. No. __07C81D7014__.

During this period his/her conduct was found to be Satisfactory/not

Satisfactory.

His/Her date of birth as per our records is __01|02|1966__ .

Prepared by

Superintendent

Principal

PRINCIPAL
SREEKAVITHA ENGINEERING COLL
KAREPALLY, KHAMMAM DT. 507 1

SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY

SNIST
Sreenidhi Institute of Science and Technology
Because, life is all about taking the right decision

AUTONOMOUS

www.sreenidhi.edu.in

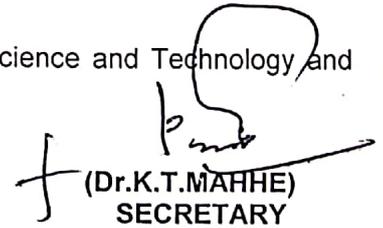No.SNIST/ADMN/TS- / 2018   Date: 22-02-2018

APPOINTMENT ORDER

Sub:- Establishment – SNIST – Appointment as Assocaite Professor Orders issued – Reg.

Ref:- Your application for the above post.

****

In pursuance of the recommendations of Selection Committee constituted by SNIST, you are hereby appointed as Associate Professor in the Department of Electronics and Communication Engineering. Following are the governing conditions to your appointment order :

1. You will be paid a basic salary of Rs.12000/- P.M. in the pay scale of Rs. 12000-420-18300 as per D.A. merged scale the basic pay is Rs.18000/-.

2. You will be paid D.A. of Rs.27180/-, HRA of Rs.2700/-, CCA of Rs.240/- and other allowances of Rs.51,880/-. Thus, you will be paid Gross Salary of Rs.1,00,000/- PM.

3. You cannot leave the college service during an ongoing semester since it leads to dislocation in class work and disruption in service to the students.

4. If you, however, wish to leave the college service, you may do so during the end of the ongoing semester by giving one month notice period. Otherwise, you will have to serve three months notice or pay amount in lieu thereof.

5. Your service conditions will be governed by the Rules and Regulations of the Institute, as amended, from time to time.

6. Your appointment is subject to ratification by duly constituted by Slection Committee and approved by by JNTUH. If you are selected, your appointment is valid up to the end of the academic year 2017-18 which is extendable on mutual agreement.

7. Your appointment can be terminated any time with three months notice during ongoing semester, otherwise one month notice during the end of the semester on either side.

8. You have to submit attested photocopies of your original certificates of qualifications and experience for record in our office.

9. You will have to deposit your original certificates of all your qualifications with the institute since it is experienced by the institute that some teachers have left the institution without giving any notice and without clearing the dues in library or elsewhere. This irregularity disrupts the academic and administrative functioning of the institution. In case you are not agreeable to this, you are required to deposit one-month salary with the institute, which is recoverable from your salary in four monthly installments from the date of your joining the institute. Entire deposit shall be paid back to you if you leave the college with requisite notice and after clearing all the dues payable.

10. You are to give all your data in the format of AICTE / JNTUH which is enclosed herewith for our record.

11. You are required to report to duty to Principal, Sreenidhi Institute of Science and Technology and submit the joining report through the Head of the Department.

To
Dr Y. Sreenivasulu.

(Dr.K.T.MAHHE)
SECRETARY

ACKNOWLEDGEMENT OF APPOINTMENT ORDER

I, the undersigned, hereby acknowledge the appointment order dated 22/02/2018 and further submit that I understood the conditions mentioned in the appointment order and would abide by the same.

Signature

To,
Dr Y. Sreenivasulu.
Cc:
1. Principal
2. Concerned HOD
3. Accounts Officer
4. Personal File

आयकर विभाग
INCOME TAX DEPARTMENT

भारत सरकार
GOVT. OF INDIA

YERRABOINA SREENIVASULU
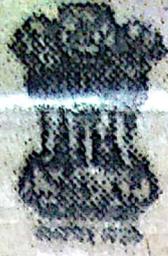
YERRABOINA SUBBAIAH
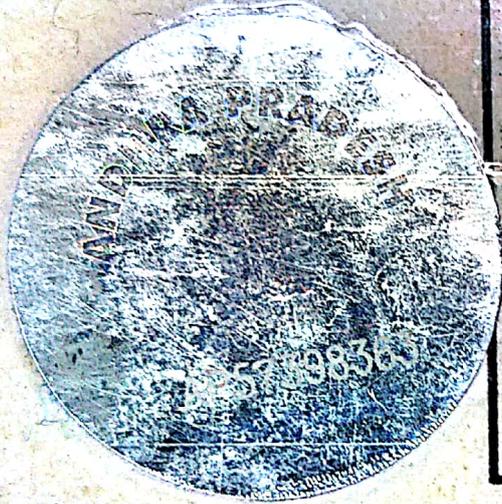
01/02/1966

Permanent Account Number
BPJPS6322G

Signature

ELECTION COMMISSION OF INDIA

భారత ఎస్నికల సంఘము
గుర్తింపుకార్డు

ELECTION COMMISSION OF INDIA
IDENTITY CARD

ZDR2044030

ఓటరు పేరు : శ్రీనివాసులు యర్రబోయ్న

Elector's Name : Sreenivasulu Yerraboina

తండ్రి పేరు : సుబ్బయ్య యర్రబోయ్న

Father's Name :Subbaiah Yerraboina

లింగము / Sex : పు / M

పుట్టిన తేది / Date of Birth   01/02/1966

1-10-10/1

టెంపుల అల్వాల్ , అల్వాల్
సికింద్రాబాద్ , మల్కాజ్గిరి,
రంగారెడ్డి,500010

Address:
1-10-10/1
Temple Alwal , Alwal
Secunderabad , Malkajgiri
Rangareddy,500010

Date: 23/02/2014

ప్రతిరూప సంతకము
ఓటరు రిజిస్ట్రేషన్ అధికారి
44... మల్కాజ్గిరి శాసనసభ నియోజక వర్గం

Facsimile Signature of

Electoral Registration Officer
44. - Malkajgiri Assembly Constituency

చిరువామాలో మార్పు ఉన్నట్లయితే మారిన చిరునామాతో
మీ పేరు జాబితాలో చేర్చుటకై మరియు అదే నంబరుతో కార్డు
పొందుటకై సంబంధిత ఫారంలో ఈ కార్డు నంబరు తెలుపవలెను.

In case of change in address, mention this Card No. in
the relevent form for including your name in the Roll at
the changed address and to obtain the card with same
342 / 886

ANNEXURE-I

CERTIFICATE FROM THE ORGANISATION WHERE THE CANDIDATE IS EMPLOYED

Certified that Mr./Ms./Mrs. YERRABOINA · SREENIVASULU is

employed as Assoc. Prof _____ (Designation) in the

Electronics and communication Engg (Department/Division Name)

of Sreenidhi institute of Science and Technology

(Institution/Industry Name).

We have no objection in forwarding his/her application for the Ph.D. Research Programme.
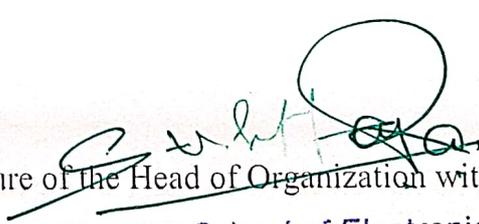
FOR FULL TIME:

The candidate will be sanctioned leave for the duration of the research programme and will be relieved from duty from _____ to _____ to undertake the full time research work in the University.

✓FOR PART TIME:

The candidate will be permitted to undertake part time study in the University/College and will be allowed to be present for discussions with the supervisor, attending course works, conduct of experiments and participations in seminars and related presentations. Further the required facilities at our organization will also be provided to the candidate for doing research.

Date: 27/6/2020

Signature of the Head of Organization with office seal

Director, School of Electronics,
Sreenidhi Institute of Science & Technology,
Yamnampet, Ghatkesar, Hyderabad - 501 301.