



Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:30-06-2020

Department	ELECTRONICS AND COMMUNICATION ENGINEERING	Application No.	20200183
Area of Research	COMMUNICATION	Research Mode	PART TIME

Name :KARKUVEL V
Date of Birth / Age :18-03-1993 / 27 Years
Gender :MALE
Category :BC
e-Mail ID :karkuvel18393@gmail.com
Mobile :9092185282



Father's/Husband's Name	VAGULAPARANAN N	Father's/Husband's Occupation	FARMER
Family Income	140000	Residential Type	RURAL
Birth Place	ALWARTHIRUNAGARI	Mother Tongue	TAMIL
Religion	HINDU	Martial Status	SINGLE
Aadhaar No.	232526248407	PAN No.	DAAPK4211C
Physically Challenged	NO	Type of Disability	-

Address for Communication:
 61
 AKILANDAMMAN KOVIL STREET
 ALWARTHIRUNAGARI
 TUTICORIN DISTRICT
 TAMILNADU
 INDIA
 Pin-628612

Permenant Address:
 61
 AKIILANDAMMAN KOVIL STREET
 ALWARTHIRUNAGARI
 TUTICORIN DISTRICT
 TAMILNADU
 INDIA
 Pin-628612

Qualification						
Degree	Discipline	College/university	Year Passed	AVG/CGPA	Class	Mode
M.E	VLSI	DR.SIVANTHI ADITANAR COLLEGE OF ENGINEERING	2018	7.31	FIRST	REGULAR
B.E	ELECTRONICS AND COMMUNICATION ENGINEERING	DR.SIVANTHI ADITANAR COLLEGE OF ENGINEERING	2015	6.15	SECOND	REGULAR

Experience				
Organization	Designation	Experience From	Experience TO	Work Nature
SRI RAMANA INSITITUTE OF POLYTECHNIC COLLEGE	LECTURER	2018-06-18	2020-06-30	LECTURER

Payment Details				
Transaction ID	Reference	Date of transaction	Amount	Status
20200183_200630160449	SUR28947677058	30-06-2020	600	SUCCESS

DESIGN OF A NOVEL ENCODING MECHANISM USING SOLS TECHNIQUE FOR DEDICATED SHORT RANGE COMMUNICATION

ABSTRACT

Due to the popularity of portable electronic products, low power system has attracted more attention in recent years. Dynamic power consumption remains to be the biggest contributor to the total power consumption of a hardware design. One of the major sources for dynamic power consumption is the system's clock signal, which is responsible for thirty to seventy percentage of the total dynamic power consumption. Several techniques to reduce the dynamic power were developed, among which clock gating is predominant. To reduce unwanted switching of clock signals, clock gating technique is used. Various clock gating techniques are available, of which Look-Ahead Clock Gating (LACG) plays a dominant role to reduce power consumption.

This project presents a novel Linear Feedback Shift Register (LFSR) with LACG technique for power reduction in sequential circuits. LACG computes the clock enabling signals of each flip-flop one cycle ahead of time, based on the present cycle data of those flip-flops on which it depends. Simulation results using CADENCE shows that, the LFSR with the LACG technique exhibit resourceful power reduction compared to the non-gated clock LFSR and data-driven clock gating LFSR.

To improve the power reduction in the LFSR with LACG technique, the concept of flip-flop grouping is performed in this project which is based on the toggling probability. The novel grouped LFSR with LACG technique can be applied for various applications like Pseudo Random Number Generators (PRNGs), Built In Self Test (BIST), Signature analysis and Spread spectrum communication systems for power reduction. In this project, the proposed technique is applied to the application circuit called BIST for reducing the dynamic power consumption.

Anna University



Reg.No. 950516419002/RG

The Syndicate of the Anna University hereby makes known that
KARKUVEL V *has been admitted to the* **DEGREE OF MASTER OF ENGINEERING** *in* **VLSI DESIGN** *under the* **Faculty of Information and Communication Engineering**, *having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in* **FIRST CLASS** *at the Examination held in* **JUNE 2018** .

Given under the Seal of the University

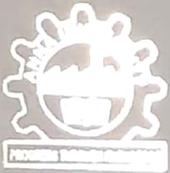


Chennai 600 025
India
December 2018

Controller of Examinations

Registrar

Vice-Chancellor



ANNA UNIVERSITY, CHENNAI - 25
M.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES



K114191249924V

Folio No. AUS1564016

NAME OF THE CANDIDATE		KARKUVEL V			REGISTER NO.	950516419002
DATE OF BIRTH	18-MAR-93	GENDER	MALE	MONTH & YEAR OF LAST APPEARANCE	JUN 2018	
COLLEGE OF STUDY	DR.SIVANTHI ADITANAR COLLEGE OF ENGINEERING			REGULATIONS	2013	
PROGRAMME & SPECIALIZATION	M.E. VLSI Design					

SEM NO	COURSE CODE	COURSE TITLE	CREDITS	LETTER GRADE	GRADE POINT	MONTH & YEAR OF PASSING
01	MA7157	Applied Mathematics for Electronics Engineers	4	D	6	NOV 2016
01	VL7101	VLSI Signal Processing	3	E	5	NOV 2016
01	VL7102	VLSI Design Techniques	3	D	6	NOV 2016
01	VL7103	Solid State Device Modeling and Simulation	3	C	7	JUN 2017
01	AP7008	DSP Integrated Circuits	3	E	5	NOV 2016
01	VL7002	Security solutions in VLSI	3	E	5	NOV 2016
01	VL7111	VLSI Design Laboratory I	2	S	10	NOV 2016
02	AP7201	Analysis and Design of Analog Integrated Circuits	3	D	6	JUN 2017
02	VL7201	CAD for VLSI Circuits	3	C	7	NOV 2017
02	VL7202	Low Power VLSI Design	3	C	7	JUN 2017
02	CU7001	Real Time Embedded Systems	3	D	6	JUN 2017
02	VL7005	Physical Design of VLSI Circuits	3	C	7	NOV 2017
02	VL7010	Submicron VLSI Design	3	D	6	JUN 2017
02	VL7211	VLSI Design Laboratory II	2	A	9	JUN 2017
03	VL7301	Testing of VLSI Circuits	3	D	6	NOV 2017
03	AP7301	Electromagnetic Interference and Compatibility	3	C	7	JUN 2018
03	VL7014	IP Based VLSI Design	3	C	7	JUN 2018
03	VL7311	Project Work (Phase I)	6	A	9	NOV 2017
04	VL7411	Project Work (Phase II)	12	S	10	JUN 2018

*** End of Statement ***

Cumulative Grade Point Average : 7.31

Classification : FIRST CLASS



Medium of Instruction : ENGLISH

Chennai - 600 025.

Date : 14/09/2018

SIGNATURE OF THE STUDENT



CONTROLLER OF EXAMINATIONS

Dr.SIVANTHI ADITANAR COLLEGE OF ENGINEERING
TIRUCHENDUR - 628 215

(an ISO 9001 : 2000 certified Institution & accredited by NBA, AICTE)



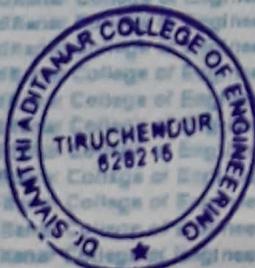
TRANSFER CERTIFICATE

Admission No. PVD 87

T.C.No. 11
1822009

1. Name of the Student ... KARKUVEL V
2. Gender ... MALE
3. Date of Birth (in words) as entered in the Admission Register ... 18-03-1993
Eighteenth March NineteenNintyThree
4. Religion and Community ... BC HINDU.YADAVAR
5. Nationality ... Indian
6. Name of the Parent / Guardian ... VAGULAPARANAN
7. Date in which he/she was admitted to the College ... 01-09-2016
8. Class in which he/she was studying at the time of leaving the Institution ... ME - Final Year
9. Branch studied by him/her ... VLSI Design
10. Mode of study ... Full Time / Part Time
10. Whether qualified for Promotion to higher class ... Refer the Mark Statement
12. Date on which he/she actually left ... 10-04-2018
13. Whether there is any break of study ... No
14. Date of application for Transfer Certificate ... 11-06-2018
15. Conduct and Charcter ... Very Good

Date: 12-06-2018



[Signature]
PRINCIPAL

Dr. SIVANTHI ADITANAR COLLEGE
OF ENGINEERING
TIRUCHENDUR



SRI RAMANA

Institute of
Polytechnic College

moolaikaraipatti - 627354. Mobile : 94427 31275

IDENTITY CARD



V. Karkuvel
Lecturer in EEE



18-03-1993



S/o. Vagulaparanan
61, Akilandamman Kovil Street,
Alwarthirunagari - 628612.



90921 85282

Principal

आयकर विभाग
INCOME TAX DEPARTMENT



भारत सरकार
GOVT. OF INDIA

KARKUVEL V

VAGULAPARANAN

18/03/1993

Permanent Account Number

DAAPK4211C

Signature



09042013



இந்திய தேர்தல் ஆணையம்
வாக்காளர் அடையாள அட்டை
ELECTION COMMISSION OF INDIA
IDENTITY CARD

UHX0613224



வாக்காளர் பெயர் : கற்குவேல்

Elector's Name : Karkuvel

தந்தை பெயர் : வகுளாபரணன்

Father's Name : Vagulaparanan

பாலினம் / Sex : ஆண் / Male

பிறந்த தேதி / Date of Birth: 18/03/1993

UHX0613224

முகவரி: 61 / 44-61

அகிலாண்டம்மன் கேயில் தெரு
ஆழ்வார்திருநகரி-628612
ஸ்ரீவைகுண்டம் வட்டம்

Address : 61 / 44-61
Ahilandaman Kovil Street
Alwarthirunagari -628912
Srivaikuntam Taluk

நாள் / Date : 10/01/2013

வாக்காளர் பதிவு அதிகாரியின்
கையொப்ப முத்திரை

216 - ஸ்ரீவைகுண்டம்
சட்டமன்ற தொகுதி

Facsimile Signature of
Electoral Registration Officer
216 - Srivaikuntam
Assembly Constituency

முகவரியில் மாற்றம் ஏற்பட்டால் வாக்காளர்
பட்டியலில் புதிய முகவரியில் உங்கள் பெயரைச்
சேர்ப்பதற்கான உரிய படிவத்தில் இந்த
அட்டையின் எண்ணைக் குறிப்பிட்டு, இதே
எண்ணுள்ள அட்டையை பெறலாம்

In case of change of address, mention this card
No. in the relevant form for including your name
in the roll at the changed address and to obtain
the card with same number.

127/1354

R.Dis.....198...../2007

GCP-26-2-6,00,000 Cps-20-06-2006 (P4-13)

Dated...21...8...2007

BC



சான்றிதழ் எண் :
Certificate No. 7762019 / 2007

மாவட்டக் குறியீடு எண் : 19
District Code

வட்டக் குறியீடு எண் : 03
Taluk Code

கிராமக் குறியீடு எண் : 003
Village Code

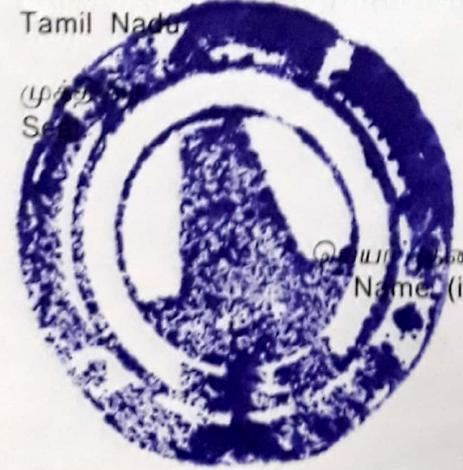
சாதிச் சான்றிதழ் COMMUNITY CERTIFICATE

தூத்துக்குடி மாவட்டம் கிருச்சிந்தூர் வருவாய் கோட்டம்
கிருச்சிந்தூர் அம்பலவார்த்துநகரி கிராமம்/நகரம்,
திரு/திருமதி வகுளா பரணை மனைவி/மகன்/மகள்
கந்தவேல் கிருதுயாதவர் வருப்பைச் சார்ந்தவர்.
அரசு ஆணை நிலை எண் 28, பிற்பட்ட மற்றும் மிகவும் பிற்பட்ட பிரிவின்
நலத் துறை, நாள் 19-7-1994 வரிசை எண் 139 படி பிற்பட்ட பிரிவினைச்
சார்ந்தவர் எனச் சான்றளிக்கப்படுகிறது.

This is to certify that Son / Daughter of
Thiru..... of
Village / Town..... Taluk..... Division
District of the State of Tamil Nadu belongs to
..... Community, which is recognised as a
Backward Class as per Government Order (Ms.) No. 28 Backward Classes
and Most Backward Classes Welfare, dated 19th July 1994
vide Serial No.....

2. திரு/திருமதி / செல்வன் / செல்வி கந்தவேல்
என்பவரும் அவருடைய குடும்பத்தினரும் தமிழ்நாட்டில் தூத்துக்குடி
மாவட்டத்தில் கிருச்சிந்தூர் வட்டத்தில் அம்பலவார்த்துநகரி
கிராமத்தில் / நகரத்தில் வசித்து வருகிறார்கள் எனச் சான்றளிக்கப்படுகிறது.

2. It is certified that Thiru/Tmt./Selvan/Selvi..... and
his/her family ordinarily reside(s) at Village/Town
..... Taluk District of
Tamil Nadu



கையொப்பம் :
Signature :
நாள் :
Date : 21.8.07
பதவி (in Capital Letters) :
Name (in Capital Letters) :
பதவி (in Capital Letters) :
Designation :
20.8.2007



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Principal