



Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:08-07-2020

Department	ELECTRICAL AND ELECTRONICS ENGINEERING	Application No.	20200215
Area of Research	POWER SYSTEM	Research Mode	PART TIME

Name :V RAJESH KUMAR
Date of Birth / Age :20-05-1987 / 33 Years
Gender :MALE
Category :MBC
e-Mail ID :vrk5197148@gmail.com
Mobile :8667861911



Father's/Husband's Name	K.VIKIRAMTHITHAN	Father's/Husband's Occupation	RETD DRIVER
Family Income	110000	Residential Type	RURAL
Birth Place	SIVAGIRI	Mother Tongue	TAMIL
Religion	HINDU	Martial Status	MARRIED
Aadhaar No.	721414382909	PAN No.	BOZPRF7086
Physically Challenged	NO	Type of Disability	-

Address for Communication: 121, SANTHAI PETTAI STREET,SIVAGIRI SIVAGIRI TENKASI DISTRICT TAMIL NADU INDIA Pin-627757	Permenant Address: 121 SANTHAI PETTAI STREET,SIVAGIRI SIVAGIRI TENKASI DISTRICT TAMIL NADU INDIA Pin-627757
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Qualification						
Degree	Discipline	College/university	Year Passed	AVG/CGPA	Class	Mode
M.E.,	PED	PSR ENGINEERING COLLEGE	2018	7.23 CGPA	FIRST	REGULAR
B.E.,	EEE	I F E T COLLEGE OF ENGINEERING	2009	62%	FIRST	REGULAR

Experience				
Organization	Designation	Experience From	Experience TO	Work Nature

Payment Details				
Transaction ID	Reference	Date of transaction	Amount	Status
20200215_200708154007	SHD48976033407	08-07-2020	600	SUCCESS

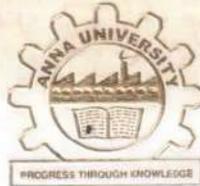
MACHINE LEARNING TECHNIQUE BASED FAULT CLASSIFICATION AND SITE LOCATION ON TRANSMISSION LINES

ABSTRACT:

The protection and maintenance of an energy transmission system during fault condition is indispensable to make sure of an efficient and reliable power supply to consumers. Forecasting of faults (classification and site) with high precision increases the operational strength and reliability of the power system and helps to avoid huge power outage. This paper proposes a machine learning technique based fault classification and location identification on the transmission line. A Machine learning algorithm has ability to “learn” from the info without explicitly programmed and may independently adapt when exposed to new data. Previously artificial neural network technique is employed for the detection, classification of fault in transmission cable which fails to locate and it needs huge training data. Whereas the proposed technique doesn't require such large data, to predict the fault site and classification

Anna University Chennai

Reg. No.40805105028/RG



The Syndicate of the Anna University Chennai hereby makes known that **RAJESH KUMAR V** has been admitted to the **DEGREE OF BACHELOR OF ENGINEERING** in **ELECTRICAL AND ELECTRONICS ENGINEERING** under the *Faculty of Electrical Engineering*, having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in **FIRST CLASS** at the Examination held in **APRIL 2009**.

Given under the Seal of the University



Chennai 600 025
India
February 2010
9136110501601

Controller of Examinations

Registrar

Vice-Chancellor

9136110501601

Anna University



Reg.No. 1626002/RG



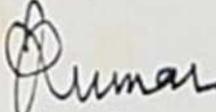
The Syndicate of the Anna University hereby makes known that
RAJESH KUMAR V *has been admitted to the* **DEGREE OF MASTER OF ENGINEERING** *in* **POWER ELECTRONICS AND DRIVES** *under the Faculty of Electrical Engineering, having completed the prescribed programme of study through* **P.S.R. ENGINEERING COLLEGE, SIVAKASI** *an autonomous college affiliated to this University and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in* **FIRST CLASS** *at the Examination held in* **APRIL 2018.**

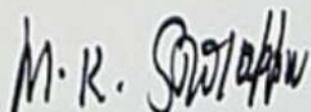
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Chennai 600 025
India
December 2018
P.P.K. 11/11/18


Controller of Examinations


Registrar


Vice-Chancellor



ANNA UNIVERSITY CHENNAI, CHENNAI - 600 025

Folio No. 1242732

B.E. DEGREE EXAMINATIONS

RI120043031V

CONSOLIDATED STATEMENT OF MARKS

NAME OF THE CANDIDATE		RAJESH KUMAR V				REGISTER NO.	40305105028						
COLLEGE OF STUDY		403 LEET College of Engineering				MONTH & YEAR OF LAST APPEARANCE	April 2009						
PROGRAMME & BRANCH		B.E. Electrical and Electronics Engineering				REGULATIONS	2004						
SEM.	SUBJECT CODE	SUBJECT TITLE	MAX	M/N	MARKS SECURED	MONTH & YEAR OF PASSING	SEM.	SUBJECT CODE	SUBJECT TITLE	MAX	M/N	MARKS SECURED	MONTH & YEAR OF PASSING
01	CY1101	Chemistry-I	100	50	056	NOV 2005	03	EE1314	Integrated Circuits Laboratory	100	50	068	NOV 2007
01	GE1101	Engineering Graphics	100	50	056	NOV 2005	05	EE1303	Power Electronics Laboratory	100	50	031	NOV 2007
01	GE1102	Fundamentals of Computing	100	50	053	NOV 2005	05	CE1332	Communication Skills Laboratory	100	50	066	NOV 2007
01	EE1101	English-I	100	50	063	NOV 2005	06	EE1301	Digital Signal Processing	100	50	060	NOV 2008
01	MA1101	Mathematics-I	100	50	050	APR 2007	06	EE1302	Microprocessor and Microcontroller	100	50	057	APR 2008
01	EE1101	Physics-I	100	50	038	NOV 2005	06	EE1331	Solid State Drives	100	50	050	APR 2008
01	CY1102	Chemistry Laboratory	100	50	071	NOV 2005	06	EE1332	Power System Analysis	100	50	050	APR 2008
01	GE1103	Engineering Practices Lab	100	50	090	NOV 2005	06	EE1331	Measurements and Instrumentation	100	50	050	APR 2008
01	GE1104	Computer Practice-I	100	50	063	NOV 2005	06	MG1331	Principles of Management	100	50	054	APR 2008
01	EE1102	Physics Laboratory	100	50	084	NOV 2005	06	EE1333	Microprocessor and Microcontroller Laboratory	100	50	066	APR 2008
02	CE1101	Fluid and Solid Mechanics	100	50	071	APR 2006	06	EE1332	Measurements and Instrumentation Laboratory	100	50	071	APR 2008
02	CY1151	Chemistry-II	100	50	051	APR 2006	07	EE1401	Power System Operation and Control	100	50	057	NOV 2008
02	EE1151	Electric Circuit Analysis	100	50	060	NOV 2006	07	EE1402	High Voltage Engineering	100	50	068	NOV 2008
02	CE1151	Engineering Mechanics	100	50	056	APR 2006	07	EE1403	Design of Electrical Apparatus	100	50	055	NOV 2008
02	EE1151	English - II	100	50	054	APR 2006	07	MG1401	Total Quality Management	100	50	058	NOV 2008
02	MA1151	Mathematics - II	100	50	099	APR 2006	07	CS1034	Computer Architecture	100	50	063	NOV 2008
02	EE1151	Physics-II	100	50	050	APR 2006	07	EE1001	Special Electrical Machines	100	50	078	NOV 2008
02	EE1152	Electric Circuits Lab	100	50	079	APR 2006	07	EE1404	Power System Simulation Laboratory	100	50	028	NOV 2008
02	CE1152	Computer Practice II	100	50	082	APR 2006	08	EE1451	Electric Energy Generation, Utilization and Conservation	100	50	069	APR 2009
03	CS1211	Data Structures and Algorithms	100	50	053	NOV 2007	08	EE1003	Power Systems Transients	100	50	067	APR 2009
03	CY1201	Environmental Science and Engineering	100	50	054	NOV 2006	08	IC1403	Neural Network and Fuzzy Logic Control	100	50	058	APR 2009
03	EE1211	Electronic Devices	100	50	030	NOV 2006	08	EE1452	Project Work	200	100	136	APR 2009
03	EE1201	Electromagnetic Theory	100	50	051	NOV 2006	<p align="center">***End of Statement***</p> <p align="center">Classification : FIRST CLASS</p> <p align="center">Total Marks (from 3rd to 8th semester): 2998 / 4500</p> <p align="center">Percentage (rounded to nearest integer): 67</p> 						
03	EE1202	Electrical Machines - I	100	50	016	NOV 2006							
03	MA1201	Mathematics - III	100	50	055	APR 2007							
03	ME1211	Applied Thermodynamics	100	50	030	NOV 2006							
03	CS1212	Data Structures and Algorithms Laboratory	100	50	076	NOV 2006							
03	EE1203	Electrical Machines Laboratory - I	100	50	063	NOV 2006							
04	CS1261	Object Oriented Programming	100	50	051	APR 2008							
04	EE1206	Electronic Circuits	100	50	054	NOV 2007							
04	EE1251	Electrical Machines - II	100	50	051	NOV 2008							
04	EE1252	Transmission and Distribution	100	50	050	NOV 2008							
04	IC1251	Control Systems	100	50	050	NOV 2007							
04	MA1251	Numerical Methods	100	50	050	APR 2009							
04	EE1252	Electronic Devices and Circuits Laboratory	100	50	075	APR 2007							
04	EE1304	Electrical Machines Laboratory II	100	50	067	APR 2007							
04	IC1252	Control Systems Laboratory	100	50	064	APR 2007							
05	EE1311	Communication Engineering	100	50	056	NOV 2007							
05	EE1312	Digital Logic Circuits	100	50	050	NOV 2007							
05	EE1313	Linear Integrated Circuits	100	50	050	NOV 2007							
05	EE1301	Power Electronics	100	50	051	NOV 2007							
05	EE1302	Protection and Switchgear	100	50	068	NOV 2008							
05	GE1301	Professional Ethics and Human Values	100	50	073	NOV 2007							
05	CS1262	Object Oriented Programming Laboratory (exercise on Application of C++)	100	50	088	NOV 2007							

Medium of Instruction : ENGLISH

Chennai - 600 025.

V. S. Kumar
Professor of Examinations



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Folio No. 1242732

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05	EE1313	Linear Integrated Circuits	100	50	050	NOV 2007							
05	EE1301	Power Electronics	100	50	051	NOV 2007							
05	EE1302	Protection and Switchgear	100	50	068	NOV 2008							
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Medium of Instruction : ENGLISH

Chennai - 600 025.

Professor of Examinations

आयकर विभाग
INCOME TAX DEPARTMENT



भारत सरकार
GOVT. OF INDIA

RAJESHKUMAR

VIKKIRAMATHITHAN

20/05/1987

Permanent Account Number

BOZPR7608F

Signature



आयकर विभाग
INCOME TAX DEPARTMENT



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भारत सरकार
GOVT. OF INDIA

RAJESHKUMAR

VIKKIRAMATHITHAN

20/05/1987

Permanent Account Number

BOZPR7608F

Signature



ANNEXURE-I

CERTIFICATE FROM THE ORGANISATION WHERE THE
CANDIDATE IS EMPLOYED

Certified that Mr./~~Ms.~~/Mrs. V.RAJESH KUMAR is employed as
(Designation)

Asst.Prof in the (Department/Division Name) EEE of (Insti

SHRIDEVI INSTITUTE OF ENGINEERING AND TECHNOLOGY, TUMKUR

We have no objection in forwarding his/~~her~~ application for the Ph. D Research Programme

FOR FULL TIME :

The candidate will be sanctioned leave for the duration of the research programme and will be relieved from duty from _____ to _____ to undertake the full time research work in the University.

FOR PART TIME :

The candidate will be permitted to undertake part time study in the University/College and will be allowed to be present for discussions with the supervisor, attending course works, conduct of experiments and participations in seminars and related presentations. Further the required facilities at our organization will also be provided to the candidate for doing research.

Date :08-07-2020

Signature of the Head of the Department with Seal


Head of the Department
Electrical & Electronics Engineering
Shridevi Institute of Engineering & Technology
TUMKUR-572108.