KALASALINGAM
ACADEMY OF RESEARCH AND EDUCATION
(DEEMED TO BE UNIVERSITY)
Under sec. 3 of UGC Act 1956. Accredited by NAAC with "A" Grade

Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:16-12-2020

Department	ELECTRONICS AND COMMUNICATION ENGINEERING	Application No.	202020109
Area of Research	WIRELESS SENSOR NETWORK	Research Mode	PART TIME

Name :LORATE SHINY M

Date of Birth / Age :25-07-1988 / 32 Years

Gender :FEMALE

Category :OBC

e-Mail ID :lorateshiny.cse@sairamce.edu.in

Mobile :9940535422
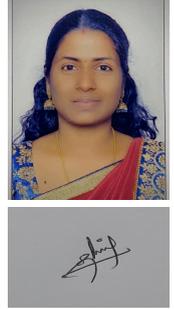
Father's/Husband's Name	J.NISHANTH JOSEPH	Father's/Husband's Occupation	ENGINEER
Family Income	4 LPA	Residental Type	RURAL
Birth Place	KANYAKUMARI,TAMILNADU	Mother Tongue	TAMIL
Religion	CHRISTIAN	Martial Status	MARRIED
Aadhaar No.	2282 4440 1597	PAN No.	AMMPL9819J
Physically Challenged	NO	Type of Disability	-

Address for Communication:
7K
BLOCK B, FLAT NO. 7K, VAHE IMPERIAL GARDENS APARTMENTS
GUNJUR
BANGALORE DISTRICT
KARNATAKA
INDIA
Pin-560087

Permenant Address:
9/100
PALAYAM, MEYCODE POST
THINGAL NAGAR
KANYAKUMARI DISTRICT
TAMILNADU
INDIA
Pin-629 804

Qualification

Degree	Discipline	College/university	Year Passed	AVG/CGPA	Class	Mode
M.E	EMBEDDED SYSTEM TECHNOLOGIES	ANNA UNIVERSITY	2012	7.94	FIRST	REGULAR
B.E	ELECTRONICS AND COMMUNICATION ENGINEERING	ANNA UNIVERSITY	2009	69%	FIRST	REGULAR
HSC	BIO-MATHS	ST. FRANCIS XAVIER HIGHER SECONDARY SCHOOL, MANGUZHY, KANYAKUMARI.	2005	86%	FIRST	REGULAR
SSLC	BIO-MATHS	ST. FRANCIS XAVIER HIGHER SECONDARY SCHOOL, MANGUZHY, KANYAKUMARI.	2003	90%	FIRST	REGULAR

Experience

Organization	Designation	Experience From	Experience TO	Work Nature

SRI SAIRAM COLLEGE OF ENGINEERING, BANGALORE.	ASSISTANT PROFESSOR	2012-07-21	2020-12-16	TEACHING
MNSK COLLEGE OF ENGINEERING, PUDUKKOTTAI.	ASSISTANT PROFESSOR	2011-01-06	2012-05-25	TEACHING

Payment Details				
Transaction ID	Reference	Date of transaction	Amount	Status
202020109_201222235055	VHMP9585590568	22-12-2020	600	SUCCESS

PROPOSED TITLE:

VAMPIRE ATTACK DETECTION AND SECURE ROUTING IN WIRELESS AD HOC SENSOR NETWORK

OBJECTIVES:

- Detect Vampire attack
- Provides Security against vampire attacks.
- Ensure Quality of Service.
  - ✓ Minimum delay in data delivery
  - ✓ High packet delivery ratio
  - ✓ High malicious detection rate
  - ✓ High network lifetime

SCOPE OF THE RESEARCH WORK
1. Trust based secure transmission routing
   - Detect vampire attack using KID-SASR
2. Secure routing scheme to mitigate the attack
   - Detect anti-nodes from safe nodes.
   - Provide security in routing using BSCSRP

PREVIOUS WORK:

- Focused on denial of communication at the routing or medium access control level
- Previous studied Dos attacks are Reduction of Quality (ROQ) and Routing Infrastructure Attacks.
- Previous work are confined to MAC or application layer(not in Routing Protocol Layer)
- In the existing methods low throughput and the packet delay happens during communication.
- Some protocol doesn't offer secure routing and high in energy consumption.
- Existing scheme protects against only in hostile attacks.
- No other techniques presented with security, trustworthiness against vampire attacks and low energy consumption.
- The main aim of the introduced method is to provide good quality of service and security in WSN

RESEARCH PROBLEM
- Various experts have published their report on identifying vampire attacks in WSN.
- Attackers aim to broadcast little data, making long source paths to reach the destination.
- Results in waste of energy by consuming its battery power e.t.c.

PROPOSED WORK

1. SASR Algorithm for Optimal Route Selection
   I. Problem formulation for secure routing.
   II. Routing design using SASR
2. A secure routing scheme to mitigate the attack in WASN

Anna University Chennai

Reg. No.20205106033/RG

The Syndicate of the Anna University Chennai hereby makes known that **LORATE SHINY M** *has been admitted to the* **DEGREE OF BACHELOR OF ENGINEERING** *in* **ELECTRONICS AND COMMUNICATION ENGINEERING** *under the* *Faculty of Information and Communication Engineering,* *having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in* **FIRST CLASS** *at the Examination held in* **APRIL 2009.**

Given under the Seal of the University

Chennai 600 025
India
February 2010   *Controller of Examinations*   *Registrar*   *Vice-Chancellor*

Anna University

The Syndicate of the Anna University hereby makes known that

LORATE SHINY M has been admitted to the **DEGREE OF MASTER OF ENGINEERING** in **EMBEDDED SYSTEM TECHNOLOGIES** under the **Faculty of Electrical Engineering,** having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in **FIRST CLASS** at the Examination held in **JUNE 2012**.
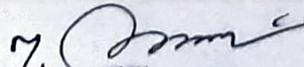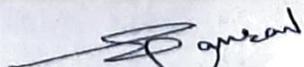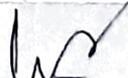
Given under the Seal of the University

Chennai 600 025
India
May 2014

Controller of Examinations   Registrar   Vice-Chancellor

NAME OF THE CANDIDATE	LORATE SHINY M	REGISTER NO.	20205106033
COLLEGE OF STUDY	202: D.M.I. College of Engineering	MONTH & YEAR OF LAST APPEARANCE	April 2009
PROGRAMME & BRANCH	B.E. Electronics and Communication Engineering	REGULATIONS	2004

SEM.	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING	SEM.	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING
01	CY1101	Chemistry-I	100	50	063	NOV 2005	06	CS1302	Computer Networks	100	50	058	APR 2008
01	GE1101	Engineering Graphics	100	50	081	NOV 2005	06	EC1351	Digital Communication	100	50	057	APR 2008
01	GE1102	Fundamentals of Computing	100	50	067	NOV 2005	06	EC1352	Antenna and Wave Propagation	100	50	065	APR 2008
01	HS1101	English-I	100	50	055	NOV 2005	06	MG1351	Principles of Management	100	50	074	APR 2008
01	MA1101	Mathematics-I	100	50	062	NOV 2005	06	EC1006	Medical Electronics	100	50	057	APR 2008
01	PH1101	Physics-I	100	50	065	NOV 2005	06	EC1353	Communication System Lab	100	50	085	APR 2008
01	CY1102	Chemistry Laboratory	100	50	090	NOV 2005	06	EC1354	Networks Lab	100	50	089	APR 2008
01	GE1103	Engineering Practices Lab	100	50	096	NOV 2005	06	EC1355	Electronic System Design Lab	100	50	078	APR 2008
01	GE1104	Computer Practice-I	100	50	083	NOV 2005	06	GE1352	Communication Skills Laboratory	100	50	074	NOV 2008
01	PH1102	Physics Laboratory	100	50	082	NOV 2005	07	EC1401	VLSI Design	100	50	066	NOV 2008
02	CY1201	Environmental Science and Engineering	100	50	054	APR 2006	07	EC1402	Optical Communication	100	50	059	NOV 2008
02	EC1151	Circuit Analysis	100	50	060	APR 2006	07	EC1403	Microwave Engineering	100	50	065	NOV 2008
02	GE1151	Engineering Mechanics	100	50	069	APR 2006	07	MG1401	Total Quality Management	100	50	070	NOV 2008
02	HS1151	English - II	100	50	057	APR 2006	07	EC1007	Operating systems	100	50	056	NOV 2008
02	MA1151	Mathematics - II	100	50	072	APR 2006	07	EC1011	Television and Video Engineering	100	50	059	NOV 2008
02	PH1154	Physics - II	100	50	055	APR 2006	07	EC1404	VLSI Lab	100	50	088	NOV 2008
02	EC1152	Electric Circuits Lab	100	50	082	APR 2006	07	EC1405	Optical and Microwave Lab	100	50	080	NOV 2008
02	GE1152	Computer Practice-II	100	50	086	APR 2006	08	EC1451	Mobile Communication	100	50	060	APR 2009
03	CS1151	Data Structures	100	50	059	NOV 2006	08	EC1013	Wireless networks	100	50	066	APR 2009
03	EC1201	Digital Electronics	100	50	059	NOV 2006	08	EC1015	Satellite Communication	100	50	060	APR 2009
03	EC1202	Electron Devices	100	50	059	NOV 2006	08	EC1452	Project Work	200	100	152	APR 2009
03	EC1203	Electronic Circuits - I	100	50	063	NOV 2006							
03	EE1211	Electrical Machines	100	50	056	NOV 2006			***End of Statement***				
03	MA1201	Mathematics - III	100	50	062	NOV 2006			Classification : FIRST CLASS				
03	CS1152	Data Structure Lab	100	50	076	NOV 2006			Total Marks (from 3rd to 8th semester): 3372 / 4900				
03	EC1204	Electronic Devices and Circuits Lab I	100	50	084	NOV 2006			Percentage (rounded to nearest integer) : 69				
03	EE1261	Electrical Machines Lab	100	50	084	NOV 2006							
04	EC1251	Electronic Circuits II	100	50	055	APR 2007							
04	EC1252	Signals and Systems	100	50	052	APR 2007							
04	EC1253	Electromagnetic Fields	100	50	077	APR 2007							
04	EC1254	Linear Integrated Circuits	100	50	057	APR 2007							
04	EC1255	Measurements and Instrumentation	100	50	067	APR 2007							
04	MA1254	Random Processes	100	50	059	APR 2007							
04	EC1256	Electronics circuits II and simulation Lab	100	50	081	APR 2007							
04	EC1257	Linear Integrated Circuit Lab	100	50	068	APR 2007							
04	EC1258	Digital Electronics Lab	100	50	096	APR 2007							
05	EC1301	Communication Theory	100	50	075	NOV 2007							
05	EC1302	Digital Signal Processing	100	50	056	NOV 2007							
05	EC1303	Microprocessors and Its Applications	100	50	065	NOV 2007							
05	EC1304	Control Systems	100	50	057	NOV 2007							
05	EC1305	Transmission Lines and Waveguides	100	50	056	NOV 2007							
05	GE1301	Professional Ethics and Human Values	100	50	070	NOV 2007							
05	EC1306	Digital Signal Processing Lab	100	50	091	NOV 2007							
05	EC1307	Microprocessor and Application Lab	100	50	067	NOV 2007							
06	CS1251	Computer Architecture	100	50	073	APR 2008							

Medium of Instruction : ENGLISH

ANNA UNIVERSITY, CHENNAI - 25
M.E.
DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES

L120084721443M   **Folio No.** TRB107501

NAME OF THE CANDIDATE	LORATE SHINY M			REGISTER NO.	92009523007
DATE OF BIRTH	25-JUL-88	GENDER	FEMALE	MONTH & YEAR OF LAST APPEARANCE	JUN 2012
COLLEGE OF STUDY	SUDHARSAN ENGINEERING COLLEGE			REGULATIONS	2008
PROGRAMME & SPECIALIZATION	M.E. EMBEDDED SYSTEM TECHNOLOGIES (Part Time)				

SEM. NO.	COURSE CODE	COURSE TITLE	CREDITS	LETTER GRADE	GRADE POINT	MONTH & YEAR OF PASSING
01	AN5102	Advanced Digital System Design	3	B	8	NOV 2009
01	ES5101	Micro Controller System Design and Applications	3	C	7	NOV 2009
01	ES5102	Software Technology for Embedded Systems	3	C	7	NOV 2009
01	ES5103	Real Time Systems	3	C	7	APR 2010
01	MA5145	Applied Mathematics for Embedded System	4	B	8	NOV 2010
01	ES5008	VHDL	3	B	8	NOV 2009
02	ES5151	Real Time Operating System	3	C	7	APR 2010
02	ES5152	Embedded Networking	3	C	7	APR 2010
02	ES5153	VLSI Architecture and Design Methodologies	4	B	8	APR 2010
02	ES5154	Design of Embedded Systems	3	C	7	APR 2010
02	ES5006	Wireless and Mobile Communication	3	C	7	APR 2010
02	ES5009	Data Communication and Networks	3	B	8	APR 2010
02	ES5155	Embedded System Laboratory	2	A	9	APR 2010
03	AN5005	ASIC Design	3	C	7	NOV 2010
03	ES5007	Advanced Embedded Systems	3	B	8	NOV 2010
03	ES5012	Embedded Communication Software Design	3	B	8	NOV 2010
04	ES5251	Project Work	18	A	9	JUN 2012

* * * End of Statement * * *

Cumulative Grade Point Average : 7.94

Classification : FIRST CLASS

Medium of Instruction : **ENGLISH**

Chennai - 25.

Date : 18/02/2014   **SIGNATURE OF THE STUDENT**   **CONTROLLER OF EXAMINATIONS**

TRANSFER CERTIFICATE

Sudharsan Engineering College

SATHIYAMANGALAM – 622 501, KOLATHUR TALUK,

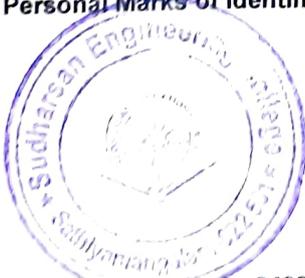PUDUKKOTTAI DISTRICT. TAMIL NADU, INDIA.

(Approved by AICTE, New Delhi and Affiliated to

Anna University, Tiruchirappalli

TC No: 2767/2009 - 2011	Register No: 92009523005	Branch: M.E., Embedded Systems

1. Name of the Student : **LORATE SHINY M**

2. Name of the Father / Mother : **MARIA YESTHAK V**

3. Date of Birth : **25.07.1988 (Twenty Fifth July Nineteen Eighty Eight)**

4. Sex : **Female**

5. Nationality : **Indian**

6. Religion / Caste : **Christian / Nadar**

7. Date of Admission : **09.09.2009**

8. Year in which the student was studying
   at the time of leaving the College : **Final (II)Year**

9. Date of leaving the College : **30.05.2011**

10. Reason for leaving the College : **Course Completed**

11. Whether qualified for promotion
    to the higher class : **Refer Statement of Marks**

12. Whether the student has paid all the
    fees / dues to the college : **Yes**

13. Student's Conduct and Character : **Good**

14. Personal Marks of Identification : ⎯⎯

PRINCIPAL,
SUDHARSAN ENGINEERING COLLEGE
SATHIYAMANGALAM - 622 501,
PUDUKKOTTAI DISTRICT
Email: principal@sec.ac.in

SEC. Telephone: 04339 - 240830   Tele Fax: 04339 - 240205

MNSK
COLLEGE OF ENGINEERING

Vallathirakkottai (Post), Pudukkottai - 622 305.

Dr. V. VALASAMUDRAM, Ph.D
Principal

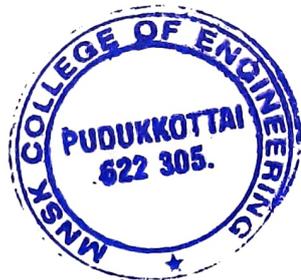29.08.2012
Date : ....

Lr.No.96/MNSK/2012

SERVICE CERTIFICATE

This is to certify that **Mrs. LORATE SHINY**, is working as **Lecturer** in **Electronics and Communication Engineering** department in our MNSK College of Engineering, Pudukkottai – 622 305 period from 03.01.2011 to 31.05.2012.

PRINCIPAL
Mnsk College Of Engineering
Vallathirakkottai - 622305
Pudukkottai .D.t

आयकर विभाग भारत सरकार
INCOME TAX DEPARTMENT **GOVT. OF INDIA**

LORATESHINY M

MARIAYESTHAK

25/07/1988

Permanent Account Number

AMMPL9819J

Signature

In case this card is lost / found, kindly inform / return to :
Income Tax PAN Services Unit, UTIITSL
Plot No. 3, Sector 11, CBD Belapur,
Navi Mumbai - 400 614.

इस कार्ड के खोने/पाने पर कृपया सूचित करें/लौटाएं :
आयकर पैन सेवा यूनीट, यूटीआईआईटीएसएल
प्लाट नं: ३, सेक्टर ११, सी.बी.डी. बेलापुर,
नवी मुंबई-४०० ६१४.

Reg no: 738/05

R.Dis................../200   dt.

BC

சான்றிதழ் எண் :
Certificate No.   :

6222299

மாவட்டக் குறியீடு எண்ன :
District Code   :

வட்டக் குறியீடு எண்ண
Taluk Code   :

கிராமக் குறியீடு எண்ன
Village Code   :

2	0
0	3
0	2	5

சாதிச் சான்றிதழ்
COMMUNITY CERTIFICATE

கன்னியாகுமரி மாவட்டம் கல்கேர வட்டம்
பெட்ஸ்கோடிஸ்ஏரை கிராமம்/நகரம், திரு / திருமதி / செல்வி/
செல்வன் M. லொபேஸ் ஹானி தகப்பனார்/கணவர்
பெயர் (லேட்) மதிய எஸ்தாக்

வகுப்பைச் சார்ந்தவர், அரசு ஆணை நிலை எண் 28, பிறபட்ட மற்றும்
மிகவும் பிறபட்ட பிரிவின் நலத் துறை, நாள் 19-7-1994 வரிசை எண் 82 படி,
பிறபட்ட பிரிவினைச் சார்ந்தவர் எனச் சான்றளிக்கப்படுகிறது.

This is to certify that..Son / Daughter of
Thiru...of
........................Village / Town,...Taluk,.................
.... District of the State of Tamil Nadu belongs to
....Community which is recognised as
a Backward Class as per Government Order (Ms.) No. 28, Backward Classes
and Most Backward Classes Welfare, dated 19th July 1994
vide Serial No.........................

2. திரு-/ திருமதி / செல்வன் / செல்வி M. லொபேஸ் ஹானி
என்பவரும் அவருடைய குடும்பத்தினரும் தமிழ்நாட்டில் கன்னியாகுமரி
மாவட்டத்தில் கல்கேர வட்டத்தில் பெட்ஸ்கோடிஸ்ஏரை
கிராமத்தில் / நகரத்தில் வசித்து வருகிறார்கள் எனச் சான்றளிக்கப்படுகிறது.

2. It is certified that Thiru/Tmt./Selvan/Selvi....................................... and
his/her family ordinarily reside(s) at ....Village/
Town ....Taluk ....District of
Tamil Nadu.

முத்திரை.
Seal.

கையொப்பம் :
Signature :   (signature)
                 24.3.05
நாள் :
Date

(தலை எழுத்துக்களில்)
Name (In Capital Letters) :   **RAJA MOHANAN THAMPY**

பதவி
Designation   **HEAD QUARTERS
DEPUTY TAHSILDAR
KALKULAM**
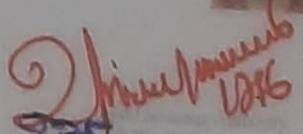
ANNEXURE-I

CERTIFICATE FROM THE ORGANISATION WHERE THE CANDIDATE IS EMPLOYED

Certified that Mr./Ms./Mrs. ___M. dORATE SHINY___ is

employed as ___Assistant Professor___ (Designation) in the

___Computer Science and Engineering___ (Department/Division Name)

of ___Sri Sairam College of Engineering___

(Institution/Industry Name).

We have no objection in forwarding his/her application for the Ph.D. Research Programme.

FOR FULL TIME:

The candidate will be sanctioned leave for the duration of the research programme and will be relieved from duty from _____ to _____ to undertake the full time research work in the University.

✓FOR PART TIME:

The candidate will be permitted to undertake part time study in the University/College and will be allowed to be present for discussions with the supervisor, attending course works, conduct of experiments and participations in seminars and related presentations. Further the required facilities at our organization will also be provided to the candidate for doing research.

Date: 22)12/2020,
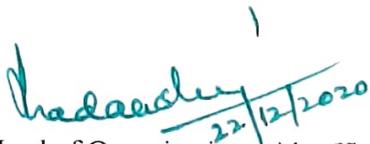
Signature of the Head of Organization with office seal

PRINCIPAL
Sri Sairam College Of Engineering
Sai Leo Nagar, Guddanahalli Post,
Anekal, Bengaluru - 562 106