



Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

## APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:09-07-2021

|                  |  |                 |           |
|------------------|--|-----------------|-----------|
| Department       | ELECTRICAL AND ELECTRONICS ENGINEERING | Application No. | 202110172 |
| Area of Research | POWER SYSTEM                           | Research Mode   | PART TIME |

**Name** :MAJJARI RAVEENDRA

**Date of Birth / Age** :02-06-1987 / 34 Years

**Gender** :MALE

**Category** :OBC

**e-Mail ID** :ravindramajjari@gmail.com

**Mobile** :09490825826



*Ravi*

|  |                      |  |            |
|--|----------------------|--|------------|
| <b>Father's/Husband's Name</b>   | M VENKATASUBBAIAH    | <b>Father's/Husband's Occupation</b>   | FARMER     |
| <b>Family Income</b>   | 50000                | <b>Residential Type</b>  | RURAL      |
| <b>Birth Place</b>   | BADVEL ANDHRAPRADESH | <b>Mother Tongue</b>   | TELUGU     |
| <b>Religion</b>  | HINDU                | <b>Martial Status</b>  | SINGLE     |
| <b>Aadhaar No.</b>   | 394161490702         | <b>PAN No.</b>   | AWWPM9262A |
| <b>Physically Challenged</b>   | NO                   | <b>Type of Disability</b>  | -          |
| <b>Address for Communication:</b><br>10/4/58<br>RAGHUNADHAPURAM VILLAGE<br>BADVEL<br>BADVEL DISTRICT<br>ANDHRAPRADESH<br>INDIA<br>Pin-516227 |                      | <b>Permenant Address:</b><br>10/4/58<br>RAGHUNADHAPURAM VILLAGE<br>BADVEL<br>BADVEL DISTRICT<br>ANDHRAPRADESH<br>INDIA<br>Pin-516227 |            |

| Qualification |            |                    |             |          |            |         |
|---------------|------------|--------------------|-------------|----------|------------|---------|
| Degree        | Discipline | College/university | Year Passed | AVG/CGPA | Class      | Mode    |
| M.TECH        | EPE        | JNTUA              | 2011        | 70.15    | FIRST/DIST | REGULAR |
| B.TECH        | EEE        | JNTUA              | 2008        | 54.50    | SECOND     | REGULAR |

| Experience                           |                     |                 |               |             |
|--------------------------------------|---------------------|-----------------|---------------|-------------|
| Organization                         | Designation         | Experience From | Experience TO | Work Nature |
| ANDHRA ENGINEERING COLLEGE           | ASSISTANT PROFESSOR | 2011-07-01      | 2016-06-30    | REGGULAR    |
| MERITS ENGINEERING COLLEGE UDAYAGIRI | ASSISTANT PROFESSOR | 2016-08-01      | 1970-01-01    | REGGULAR    |

| Payment Details        |                |                     |        |         |
|------------------------|----------------|---------------------|--------|---------|
| Transaction ID         | Reference      | Date of transaction | Amount | Status  |
| 202110172_210709150827 | VHMP0101634836 | 09-07-2021          | 600    | SUCCESS |

## **Title of the Proposed Research**

### **Research on Power Electronics and Control: Grid-Interface for Renewable, Storage and Green Micro-Grids**

Related Technology Fields: The primary area of the proposed research is in the category of “Power Electronics, Power Systems, and Transmission of Electricity”. However, the impact of this research will be in all the research areas mentioned in this RFP “Renewable Energy for Minnesota’s Future:”

1. Wind Generation
2. Photovoltaic Generation (utility-scale and community-solar)
3. Electric Energy Storage in the form of battery-stacks
4. Hydropower, Biofuels and Thermal Electric, all of which will utilize variable-speed electric generators to optimize their efficiencies at various output power levels
5. Interface of green micro-grids, with significant renewables within them, with each other and with the main grid

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# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY



HYDERABAD, ANDHRA PRADESH, INDIA

College Code: 78 (S V C E T, CHITTOOR)

*Mr. Majjari Raveendra*

*S/o Mr. M Venkata Subbaiah*

having fulfilled the academic requirements and passed the examination held during *May - 2008* in *\*\* Second Class \*\**

has this day been admitted by the Executive Council to the Degree of

*Bachelor of Technology*  
*Electrical & Electronics Engineering*

Given under the Seal of the University

H. T. No: 04781A0233

Date: 27-11-2009



*K. Ram*

Controller of Examinations

*G. Srinivas*

Director of Evaluation

*E. Sree Babu Reddy*

Registrar

CC 119372

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR



ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

H.T.No: 09711D4911

*Majjari Raveendra*

*No. M Venkatasubbaiah*

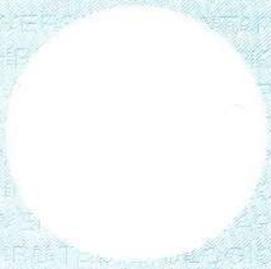
*having fulfilled the academic requirements and passed the examination held during July, 2011 in \*\*\* First Class With Distinction \*\*\**

*has this day been admitted by the Executive Council to the Degree of*

*Master of Technology*  
**(ELECTRICAL POWER ENGINEERING)**

Monday, 4 July, 2016.

Given under the Seal of the University



*[Signature]*  
Director of Evaluation

*[Signature]*  
Registrar

*[Signature]*  
Vice Chancellor

**BOARD OF SECONDARY EDUCATION  
ANDHRA PRADESH**



**SECONDARY SCHOOL CERTIFICATE**

**AA 390740**

**22/05433/246762**

CERTIFIED THAT MAJJARI RAVEENDRA  
S/O M VENKATASUBBAIAH Bearing Roll No 0486304  
belongs to CHAITANYA VIDYA MANDIR H S / BADVEL  
has appeared and PASSED at the SSC EXAMINATION held in MARCH-2002  
in **FIRST** Division with **TELUGU** as the medium of instruction.

| DATE OF BIRTH OF THE CANDIDATE | DAY      | MONTH | YEAR                 |
|--------------------------------|----------|-------|----------------------|
| C2/06/1987                     | ZERO TWO | JUN   | ONE NINE EIGHT SEVEN |

THE CANDIDATE SECURED THE FOLLOWING PERCENTAGE OF MARKS

| SUBJECT                  | Marks Secured (in figures) | Marks Secured (in words) |
|--------------------------|----------------------------|--------------------------|
| FIRST LANGUAGE : TELUGU  | 79                         | SEVEN NINE               |
| THIRD LANGUAGE : ENGLISH | 58                         | FIVE EIGHT               |
| MATHEMATICS :            | 73                         | SEVEN THREE              |
| GENERAL SCIENCE :        | 65                         | SIX FIVE                 |
| SOCIAL STUDIES :         | 57                         | FIVE SEVEN               |
| TOTAL :                  | 332                        | (THREE THREE TWO)        |
| SECOND LANGUAGE : HINDI  | 75                         | SEVEN FIVE               |
| GRAND TOTAL :            | 407                        | (FOUR ZERO SEVEN)        |

Marks of Identification : 1. A MOCK UNDER THE RIGHT EAR  
2. A MOCK ON THE RIGHT RING FINGER

*G. K. Ramesh*  
HEAD MASTER, 4.4.2

Chaitanya Vidya Mandiram High School

BADVEL-516 227.  
with School Stamp

Date of issue : 30-04-2002

*[Signature]*  
SECRETARY

BOARD OF SECONDARY EDUCATION  
A P, HYDERABAD

Any corrections in the certificate will not be entertained after one year from the date of issue.  
Any unauthorised correction in the certificate will result in cancellation of certificate.  
The marks with an asterisk indicates the old marks secured in previous appearances.



**390740**



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY**  
HYDERABAD, ANDHRA PRADESH, INDIA

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

CMM. No.: **C 396980** BACHELOR OF TECHNOLOGY - ELECTRICAL & ELECTRONICS ENGINEERING  
Serial No.: **556554611287**  
Name: **MAJJARI RAVEENDRA**

Name of the College: **SVCET, CHITTOOR**  
Month & Year of Final Exam: **May, 2008**

Hall Ticket No.: **04781A0233**

Year of Admission: **2004 - 05**

Class Awarded: **\*\*\* SECOND CLASS \*\*\***



| S.No. | COURSE TITLE | INT. MARKS | EXT. MARKS | TOTAL MARKS | CREDITS | S.No. | COURSE TITLE | INT. MARKS | EXT. MARKS | TOTAL MARKS | CREDITS |
|-------|--------------|------------|------------|-------------|---------|-------|--------------|------------|------------|-------------|---------|
|       | THEORY       | 20         | 80         | 100         |         |       | THEORY       | 20         | 80         | 100         |         |
|       | LAB          | 25         | 50         | 75          |         |       | LAB          | 25         | 50         | 75          |         |

**I YEAR**

|                                  |    |    |    |   |                                     |    |    |    |   |
|----------------------------------|----|----|----|---|-------------------------------------|----|----|----|---|
| 1 ENGLISH                        | 12 | 29 | 41 | 6 | 2 MATHEMATICS - I                   | 14 | 38 | 52 | 6 |
| 3 SOLID STATE PHYSICS            | 16 | 37 | 53 | 4 | 4 INFORMATION TECH. & NUME. METHODS | 15 | 28 | 43 | 6 |
| 5 C & DATA STRUCTURES            | 12 | 32 | 44 | 6 | 6 NETWORK THEORY                    | 12 | 38 | 50 | 6 |
| 7 ELECTRONICS DEVICES & CIRCUITS | 15 | 38 | 53 | 6 | 8 ENGINEERING DRAWING PRACTICE      | 22 | 36 | 58 | 4 |
| 9 COMPUTER PROGRAMMING (LAB)     | 18 | 20 | 38 | 8 | 10 ELECTRONICS DEVICES & CKTS (LAB) | 23 | 39 | 62 | 4 |

**I SEMESTER**

**II YEAR**

**II SEMESTER**

|                                      |    |    |    |   |                                       |    |    |    |   |
|--------------------------------------|----|----|----|---|---------------------------------------|----|----|----|---|
| 1 MATHEMATICS - II                   | 14 | 44 | 58 | 4 | 1 MANAGERIAL ECONOMICS & FINANCIAL AN | 10 | 30 | 40 | 4 |
| 2 HYDRAULICS & HYDRAULIC MACHINERY   | 12 | 46 | 58 | 4 | 2 MATHEMATICS - III                   | 12 | 51 | 63 | 4 |
| 3 PULSE & DIGITAL CIRCUITS           | 12 | 29 | 41 | 4 | 3 LINEAR & DIGITAL IC APPLICATIONS    | 16 | 34 | 50 | 4 |
| 4 SWITCHING THEORY & LOGIC DESIGN    | 17 | 28 | 45 | 4 | 4 POWER SYSTEMS - I                   | 14 | 28 | 42 | 4 |
| 5 ELECTROMAGNETIC FIELDS             | 13 | 45 | 58 | 4 | 5 ELECTROMECHANICS - II               | 14 | 31 | 45 | 4 |
| 6 ELECTRO MECHANICS - I              | 14 | 28 | 42 | 4 | 6 CONTROL SYSTEMS                     | 12 | 28 | 40 | 4 |
| 7 HYDRAULICS & HYDRAULIC MACHINERY ( | 20 | 37 | 57 | 2 | 7 IC & PULSE & DIGITAL CIRCUITS (LAB) | 20 | 34 | 54 | 2 |
| 8 NETWORKS (LAB)                     | 22 | 38 | 60 | 2 | 8 ELECTROMECHANICS (LAB) - I          | 22 | 40 | 62 | 2 |

**I SEMESTER**

**III YEAR**

**II SEMESTER**

|                               |    |    |    |   |                                       |    |    |    |   |
|-------------------------------|----|----|----|---|---------------------------------------|----|----|----|---|
| 1 COMPUTER ORGANIZATION       | 12 | 28 | 40 | 4 | 1 ANALYSIS OF LINEAR SYSTEMS          | 16 | 41 | 57 | 4 |
| 2 ELECTRICAL MEASUREMENTS     | 18 | 37 | 55 | 4 | 2 MICROPROCESSORS AND INTERFACING     | 8  | 32 | 40 | 4 |
| 3 POWER SYSTEMS-II            | 13 | 28 | 41 | 4 | 3 MODELLING OF POWER SYSTEM COMPONENT | 13 | 30 | 43 | 4 |
| 4 POWER ELECTRONICS           | 14 | 40 | 54 | 4 | 4 INSTRUMENTATION                     | 16 | 39 | 55 | 4 |
| 5 ELECTROMECHANICS-III        | 15 | 28 | 43 | 4 | 5 POWER SYSTEMS - III                 | 12 | 28 | 40 | 4 |
| 6 MANAGEMENT SCIENCE          | 12 | 28 | 40 | 4 | 6 UTILIZATION OF ELECTRICAL ENERGY    | 6  | 39 | 45 | 4 |
| 7 ELECTROMECHANICS (LAB) - II | 23 | 36 | 59 | 2 | 7 ELECTRICAL MEASUREMENTS LAB         | 21 | 36 | 57 | 2 |
| 8 CONTROL SYSTEMS (LAB)       | 23 | 40 | 63 | 2 | 8 POWER ELECTRONICS LAB               | 22 | 38 | 60 | 2 |

**I SEMESTER**

**IV YEAR**

**II SEMESTER**

|                                    |    |    |    |   |                               |    |     |     |   |
|------------------------------------|----|----|----|---|-------------------------------|----|-----|-----|---|
| 1 DIGITAL SIGNAL PROCESSING        | 8  | 41 | 49 | 4 | 1 H V D C TRANSMISSION        | 16 | 34  | 50  | 4 |
| 2 POWER SEMICONDUCTOR DRIVES       | 16 | 28 | 44 | 4 | 2 DATABASE MANAGEMENT SYSTEMS | 13 | 28  | 41  | 4 |
| 3 COMPUTER METHODS IN POWER SYS.   | 12 | 32 | 44 | 4 | 3 PROJECT & SEMINAR #         | 37 | 152 | 189 | 8 |
| 4 POWER SYSTEM OPERATION & CONTROL | 12 | 35 | 47 | 4 |                               |    |     |     |   |
| 5 H V ENGINEERING                  | 7  | 33 | 40 | 4 |                               |    |     |     |   |
| 6 ELECTRICAL DISTRIBUTION SYS.     | 9  | 31 | 40 | 4 |                               |    |     |     |   |
| 7 MICROPROCESSORS LAB              | 20 | 42 | 62 | 2 |                               |    |     |     |   |
| 8 SIMULATION OF ELEC. SYS. LAB     | 22 | 37 | 59 | 2 |                               |    |     |     |   |

Number of Credits registered for : **212**  
Aggregate Marks Secured for best : **212**

(# Project Internal = 40, External = 160)  
**2766 OUT OF 5075 ( 54.50% )**

Date of Declaration of Result **May 16, 2008**  
(See overleaf for instructions)

(\*Courses registered but not counted for calculation of aggregate)

*K. Janaki*  
**CONTROLLER OF EXAMINATIONS**

St. No. **E175997**

06/1458/E175997



# Board of Intermediate Education, A. P.

Vidya Bhavan, Nampally, Hyderabad - 500 001



## INTERMEDIATE PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that **MAJJARI RAVEENDRA** son  
of **M VENKATASUBBAIAH** bearing  
Registered No. **4135230** has appeared at the Intermediate Public  
Examination held in **MARCH-2004** and passed in **FIRST DIVISION**  
with **TELUGU** as the Medium of Instruction.

The subjects in which he was examined and the marks awarded are as follows :

| Subject                         | I Year           |               | II Year       |               |
|---------------------------------|------------------|---------------|---------------|---------------|
|                                 | Maximum Marks    | Marks Secured | Maximum Marks | Marks Secured |
| Part - 1 : ENGLISH              | 100              | 045           | 100           | 047           |
| Part - 2 : SANSKRIT             | 100              | 089           | 100           | 096           |
| Part - 3 :<br>Optional Subjects |                  |               |               |               |
| MATHEMATICS - A                 | 075              | 061           | 075           | 050           |
| MATHEMATICS - B                 | 075              | 066           | 075           | 037           |
| PHYSICS                         | 060              | 042           | 060           | 042           |
| CHEMISTRY                       | 060              | 048           | 060           | 049           |
| PHYSICS PRACTICAL               |                  |               | 030           | 023           |
| CHEMISTRY PRACTICAL             |                  |               | 030           | 025           |
| Total Marks<br>In Figures       | 720 →            |               |               |               |
| In words                        | *SEVEN*TWO*ZERO* |               |               |               |
| Date                            | 09-05-2004       | R.No.         |               |               |

*N. Srinivasulu*  
20/5  
**PRINCIPAL**

Vijayawada

Nalanda Jr. College of Science &amp; Arts

RING ROAD, GUNTUR-522007.



*[Signature]*  
Controller of Examinations

4135230

CC 119372

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR



ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

H.T.No: 09711D4911

*Majjari Raveendra*

*No. M Venkatasubbaiah*

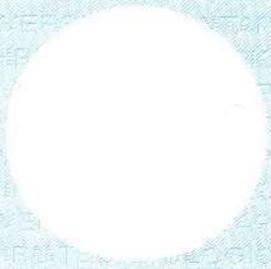
*having fulfilled the academic requirements and passed the examination held during July, 2011 in \*\*\* First Class With Distinction \*\*\**

*has this day been admitted by the Executive Council to the Degree of*

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Monday, 4 July, 2016.

Given under the Seal of the University



*[Signature]*  
Director of Evaluation

*[Signature]*  
Registrar

*[Signature]*  
Vice Chancellor



## ORIGINAL

### TRANSFER CERTIFICATE

Adm.No : 200904040019  
Regd. No. : 09711D4911

TC No. : 20210160/TC  
Date : 08-07-2021

1. Name of the Student : MAJJARI RAVEENDRA
2. Name of the Parent / Guardian : M VENKATA SUBBAIAH
3. Date of Birth : 02/06/1987  
(as per the Admission Register)
4. Nationality / Religion / Caste : Indian/Hindu/BC-D/UPPARA
5. Mother Tongue : TELUGU
6. Class, year and Branch in which the Student was first admitted : I M.TECH (EPE)
7. Date of Admission into the College : 12/OCT/2009
8. Whether the Student has completed the course : YES
9. Class in which the student is Studying At the time of leaving : II M.TECH -IV Sem
10. Whether qualified for promotion to the next Year : YES
11. Date on which the student has actually Left the College : 13-JULY-2011
12. Reason for leaving the College : COURSE COMPLETED
13. Whether the candidate had been awarded any punishment during the period of study : NO
14. General progress and conduct of the Student : GOOD



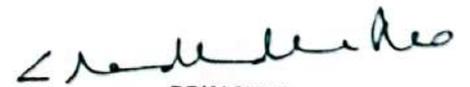
*(Signature)*  
Signature of the Head of the Institution  
with Seal

PRINCIPAL  
NARAYANA ENGINEERING COLLEGE  
NELLORE

Date: 13-06-2016.

## SERVICE & SALARY CERTIFICATE

This is to certify that **Mr. M.RAVEENDRA** has been working as **Asst. Professor** in the department of **ELECTRICAL & ELECTRONICS ENGINEERING**, from 20-06-2011 to 13-06-2016. His salary at the time of relieving is Rs: 19,000/- per month. The service and conduct during the period of his service in this institution is good.



PRINCIPAL

PRINCIPAL

Andhra Engineering College

3-76-2, ATMAKUR, Nellore,

Andhra Pradesh - 524 322, India.



# MEKAPATI RAJAMOHAN REDDY INSTITUTE OF TECHNOLOGY & SCIENCE

UDAYAGIRI - 524 226, SPSR Nellore Dist. (A.P.)

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Dt: 27-11-2018

## SERVICE CERTIFICATE

This is to certify that Mr. **M Raveendra** is working as **Assistant Professor** in the department of **Electrical & Electronics Engineering** from **1<sup>st</sup> August 2016** to **Till date**. During this period his service is good.

  
Principal  
PRINCIPAL  
MEKAPATI RAJAMOHAN REDDY  
INSTITUTE OF TECHNOLOGY AND SCIENCE  
UDAYAGIRI-524 226, Nellore Dist., A.P.

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Phone : 08620-229031, 229353, 229297, Fax : 08620-229297

E-mail : meritstech@gmail.com principalmerits@gmail.com Website : www.meritstech.ac.in

आयकर विभाग  
INCOME TAX DEPARTMENT



भारत सरकार  
GOVT. OF INDIA

MAJJARI RAVEENDRA

VENKATA SUBBAIAH MAJJARI

02/06/1987

Permanent Account Number

AWWPM9262A

M. Raveendra

Signature



09122008

Raveendra

భారత ప్రభుత్వం  
Government of India




మజ్జారి రవీంద్ర  
**MAJJARI RAVEENDRA**  
పుట్టిన తేదీ / DOB: 02/06/1987  
పురుషుడు / Male



3941 6149 0702

నా ఆధార్, నా గుర్తింపు

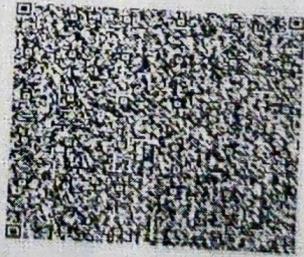
భారత విశిష్ట గుర్తింపు ప్రాధికార సంస్థ  
Unique Identification Authority of India



ఆధార్

చిరునామా: S/O మజ్జారి వెంకటసుబ్బయ్య, గం-4-58, రఘునాథపురం  
గుంథపల్లె, బద్వల, కడప, ఆంధ్ర ప్రదేశ్, 516227

Address: S/O Majjari Venkatasubbaiah, 10-4-58,  
RAGHUNADHAPURAM, Gunthapalle, Badvel,  
Cuddapah, Andhra Pradesh, 516227



3941 6149 0702

1947

help@uidai.gov.in

www.uidai.gov.in

*Raveey*



AP04 20397749

GOVERNMENT OF ANDHRA PRADESH  
REVENUE DEPARTMENT



COMMUNITY, NATIVITY & DATE OF BIRTH CERTIFICATE

1.This is to certify that the Sri/Srimathi/Kumari MAJJARI RAVEENDRA S/o / D/o / M/o / F/o / W/o / H/o / C/o Sri. Majjari Venkatasubbaiah of BADVEL (P) (U) village / Town of Badvel Mandal of Y.S.R. District of the State Andhra Pradesh belongs to Uppara-SI.No-31(BCD) Community / Caste which is recognized as Backward Class under I)G.O.Ms No.1793, Education, dt.23.09.1970 as amended from time to time .

2.It is certified that the Sri/Srimathi/Kumari MAJJARI RAVEENDRA is a native of RAGHUNADHAPURAM Locality/Landmark of BADVEL (P) (U) village / Town of Badvel Mandal of Y.S.R. District of the State Andhra Pradesh.

3.It is certified that the place of Birth Sri/Srimathi/Kumari MAJJARI RAVEENDRA is of BADVEL (P) (U) village / Town of Badvel Mandal of Y.S.R. District of the State Andhra Pradesh.

4.It is certified that the Date of Birth Sri/Srimathi/Kumari MAJJARI RAVEENDRA is 02 Day June Month 1987 Year (in words) Zero Two - June - One Nine Eight Seven as per the declaration given by his/her Father/Mother/Guardian and as entered in the School records where he/she studied.

Certified By

Name : K. MADHAVA KRISHNA REDDY  
Designation : TAHSILDAR  
Mandal : Badvel

Note : This is Digitally Signed Certificate, does not require physical signature. And this certificate can be verified at <http://www.ap.meeseva.gov.in/> by furnishing the application number mentioned in the Certificate.

Note : This is a Digitally Signed Certificate, does not require physical signature. And this certificate can be verified at [www.ap.meeseva.gov.in/](http://www.ap.meeseva.gov.in/) by furnishing the application number mentioned in the Certificate.

వరుస సంఖ్య Serial No.

జిల్లా కోడ్ District Code

1 1

షె.కు. S.C.

మండల కోడ్ Mandal Code

1 1

షె.తె. S.T.

షె.బి. B.C. D)

గ్రామం కోడ్ Village Code

0 0 6

ప్రొ.ప. పత్రం నెం. Certificate No. 000039

### కులం స్వస్థం, పుట్టిన తేదీ ధృవపత్రం COMMUNITY, NATIVITY AND DATE OF BIRTH CERTIFICATE

ఆంధ్రప్రదేశ్ రాష్ట్రంలోని కడప జిల్లా ..... మండలం, ..... గ్రామం/పట్టణంకు చెందిన శ్రీ..... గారి కుమారుడు/కుమార్తె/భార్య అయిన / శ్రీ / శ్రీమతి / కుమారి ఈ దిగువ పేర్కొన్న వాటి క్రింద షెడ్యూలు కులం / షెడ్యూలు తెగ/వైనకబడిన తరగతిగా గుర్తించబడిన కులానికి చెందిన వారని ధృవీకరించడమయినది.

This is to certify that Sri/Smt./Kum. Majjari Ravindera Son/Wife/Daughter of Sri H. abhanta Subbarao of Village/Town Rajumattapuzam H/o Anantapalle Mandal Badvel District Cuddapah of the State of Andhra Pradesh belongs to Uppala (B.C-D) Community which is recognised as Scheduled Caste/Scheduled Tribe/Backward Caste - Group [ ], Serial No. .... under:

- 1950 రాజ్యాంగ (షెడ్యూలు కులాలు) ఉత్తరువు.
- 1950, రాజ్యాంగ (షెడ్యూలు తెగలు) ఉత్తరువు
- 1950, షెడ్యూలు కులాలు, షెడ్యూలు తెగల జాబితా (నూర్పిడి) ఉత్తరువు
- 1956, షెడ్యూలు కులాలు, షెడ్యూలు తెగల (సవరణ) చట్టం.
- వెనుకబడిన తరగతుల కోసం ౬ వృద్ధపురుషుల సవరించిన విధంగా, 23-9-1970 తేదీగల ఎం.ఎస్.నెం. 1793 జి.ఓ.
- The Constitution ( Scheduled Castes) Order, 1950.
- The Constitution (Scheduled Tribes) Order, 1950.
- S.Cs., S.Ts. list (Modification) Order, 1950.
- The S.Cs. and S.Ts. (Amendment) Act. 1976.
- G.O.Ms.No.1798, Education Dept. dated: 23.9.1970 as amended from time to time, (B.Cs).

2. శ్రీ/శ్రీమతి/కుమారి..... గారి స్వస్థలం ఆంధ్రప్రదేశ్ లోని కడప జిల్లా, ..... మండలం, ..... గ్రామం / పట్టణం అని ధృవీకరించడమయినది.

2. It is Certified that Sri/Smt./Kum. M. Ravindera is a native of Rajumattapuzam H/o Anantapalle Village / Town, Badvel Mandal, Cuddapah District of Andhra Pradesh.

3. శ్రీ/శ్రీమతి/కుమారి ..... గారి పుట్టిన ప్రదేశం ఆంధ్రప్రదేశ్ లోని కడప జిల్లా, ..... మండలం ..... గ్రామం/ పట్టణం అని ధృవీకరించడమయినది.

3. It is Certified that Sri/Smt./Kum. M. Ravindera is Rajumattapuzam H/o Anantapalle Village / Town, Badvel Mandal, Cuddapah District of Andhra Pradesh.

4. ఆయన / ఆమె తండ్రి / తల్లి / సంరక్షకుడు ఇచ్చిన డిక్లరేషను ప్రకారంగాను, ఆయన / ఆమె అభ్యసించిన పాఠశాల రికార్డులలో నమోదు చేసిన విధంగాను శ్రీ/శ్రీమతి/కుమారి ..... గారి పుట్టిన తేదీ

|      |     |          |
|------|-----|----------|
| తేదీ | నెల | సంవత్సరం |
| 1    | 6   | 1987     |

(అక్షరాలలో)..... అని ధృవీకరించడమయినది.

4. It is Certified that the Date of Birth of Sri/Smt./Kum. .... is

|      |       |      |
|------|-------|------|
| Date | Month | Year |
| 02   | 06    | 1987 |

(In words) Second June nineteen eighty seven as per the declaration given by his/her father/mother/guardian and as entered in the school Records where he/she studied.

OFFICE SEAL

సంతకం / Signature  
 తేదీ / Date  
 పేరు వివర అక్షరాలలో / Name in Capital Letter  
 ఉద్యోగం / Designation

MANDAL REVENUE OFFICER

SADVEL 4/2/03

4/2/03