



Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:30-10-2023

| | | | |
|------------------|---|-----------------|-----------|
| Department | ELECTRONICS AND COMMUNICATION ENGINEERING | Application No. | 202312128 |
| Area of Research | DIGITAL IMAGE PROCESSING | Research Mode | PART TIME |

Name :SURESH M
Date of Birth / Age :07-03-1989 / 34 Years
Gender :MALE
Category :BC
e-Mail ID :psna.sam@gmail.com
Mobile :8073744044



msb

| | | | |
|---|--------------|---|------------|
| Father's/Husband's Name | S.MARIAPPAN | Father's/Husband's Occupation | SUPERVISOR |
| Family Income | 360000 | Residential Type | RURAL |
| Birth Place | PULIANGUDI | Mother Tongue | TAMIL |
| Religion | HINDU | Martial Status | MARRIED |
| Aadhaar No. | 765252208434 | PAN No. | CIGPS9382H |
| Physically Challenged | NO | Type of Disability | - |
| Address for Communication: #14, RAMOHALLI CROSS, RAJARAJESWARI COLLEGE OF ENGINEERING KUMBALAGODU BANGALORE DISTRICT KARNATAKA INDIA Pin-560074 | | Permenant Address: 30/2 BALA VINAYAGAR KOVIL SOUTH 3RD STREET, T.N.PUTHUKU KADAYANALLUR TENKASI DISTRICT TAMILNADU INDIA Pin-627855 | |

| Qualification | | | | | | |
|---------------|---------------------|-------------------------------------|-------------|----------|-------------|---------|
| Degree | Discipline | College/university | Year Passed | AVG/CGPA | Class | Mode |
| BE | ECE | BHARATH NIKETAN ENGINEERING COLLEGE | 2010 | 75 | FIRST CLASS | REGULAR |
| ME | APPLIED ELECTRONICS | PSNA COLLEGE OF ENGINEERING | 2013 | 7.99 | FIRST CLASS | REGULAR |

| Experience | | | | | |
|--------------------------------------|---------------------|-----------------|---------------|---------------|--|
| Organization | Designation | Experience From | Experience TO | Work Nature | |
| KODAIKANAL INSTITUTE OF ENGINEERING | ASSISTANT PROFESSOR | 2013-12-27 | 2015-06-27 | FULL TIME | |
| RAJARAJESWARI COLLEGE OF ENGINEERING | ASSISTANT PROFESSOR | 2015-07-21 | 2023-10-30 | STILL WORKING | |

| Payment Details | | | | |
|------------------------|----------------|---------------------|--------|---------|
| Transaction ID | Reference | Date of transaction | Amount | Status |
| 202312128_231107112128 | ZHD51517679619 | 07-11-2023 | 600 | SUCCESS |

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Abstract

Due to increase in growth of internet users of networks are increasing rapidly. Owners of the digital products are concerned about illegal copying of their products. Security and copyright protection are becoming important issues in multimedia applications and services. Digital watermarking is a technology used for copyright protection of digital media. Here ownership information data called watermark is embedded into the digital media without affecting its perceptual quality. In case of any dispute, the watermark data can be detected or extracted from the media and use as a proof of ownership. Digital video watermarking scheme based on Discrete Wavelet Transform is addressed in this paper. Design of this scheme using Matlab Simulink is proposed. Embedded watermark is robust against various attacks that can be carried out on the watermarked video.

Introduction:

Due to the advancement in technology, digital document data like videos, images, text messages are shared easily using mobile devices and can be copied without owner's permission. This leads to a new challenge of protecting the multimedia data. Watermarking is a technique of embedding copyright information into the host data to protect the intellectual right and the originality of data. Video watermarking technique is an extension of watermarking concept. The collection of consecutive still images is considered as digital video and the amount of watermark data inserted in video is called as payload. Video watermarking provide few attacks like frame dropping, frame swapping which applicable only to video and to be addressed to achieve the security in watermarking. In this paper section II discuss about Video watermarking, section III is the survey on video watermarking, section IV is the survey on video watermarking on mobile devices, section V is the Comparative analysis and conclusion is given in section.

Digital watermarking protects the illegal copying of multimedia. A watermark is secret information about origin, ownership, copy control, and so forth. This information is embedded in multimedia content, taking care of robustness and imperceptibly. The watermark is embedded and extracted as per requirement to represent the ownership and/or the identity of multimedia.

Main Aim of the Project :

Video Watermarking maximum occurrences of copyright violation and distribution happen for video media content. So Video Watermarking is one of the most accepted techniques among the various watermarking techniques currently in use.

Project goal/Overall objective:

Background:

1. General Properties of Video Watermarking.
2. The properties we discuss here play a very important role in the video watermarking process.
3. The embedded watermark should not change, affect or damage the quality of original data. A watermark should be imperceptible that humans cannot find the difference between the original and the watermarked data.
4. When a video is shared usually there will be some distortion. The watermark should be robust against all innocent and malicious attacks. Even if the video changes copyright data should not be affected.

5. The amount of data to be embedded in cover work is called Capacity. The number of watermark bits in host data is payload. The payload varies from one application to other.

6. The Watermark and original data should be accessible only to the authorized user. The hackers and unknown user must be unable to extract the watermark and the original data should not be affected by any attacks.

7. The cost to embed watermark into host data and to extract watermark should be reliable. It is very important to pick a suitable complexity watermarking algorithm to avoid high complexity problems like more software and hardware resources. Time taken by watermarking algorithm should be less to increase the efficiency.

8. The video watermarking technique mainly faces two types of attack

9. Innocent attack is unintentional and coincidence attack like smoothing and compressing image.

10. These are intentional attacks like deleting and desynchronizing attack.

Justification for the project :

Requirements for video Watermarking are as follows:

1. Video data is subject to increased attacks than any other media.
2. Video content is sensitive to distortions and Watermarking may degrade the quality.
3. Video compression algorithms are computationally rigorous.
4. Video require large bandwidth that is why it is mostly carried in compressed domain. So Watermarking algorithms also adaptable for compress area processing.

Problem Statement :

The signal embedding techniques employed are either adaptive or non-adaptive and moreover, redundancy must be involved. Inclusions of error detection and correction methods are also expected. The watermark detection or extraction methods in blind retrieval are more challenging.

Tools :

- a. Programming Section:
 - i. Matlab
 - ii. Simulink

- b. Documentation:
 - i. Microsoft Word + Power Point 2010

Resources:

The resources come from own experiences, media, such as books, brochures, journals, magazines, newspapers and books, and other electronic sources, such as the Internet and the World Wide Web. They may also come from interviews and collaborate with teachers and surveys. The project supervisor is the person who has the overall responsibility for the successful initiation, planning, execution, monitoring, controlling and closure of a project or research.

Conclusion :

Though there are several other techniques available for copyright protection and several other issues, digital water marking is the only preferred algorithm because of its basic advantages like being robust, fragile and imperceptible.

The research on current robust watermarking techniques concludes that the choice of location either in spatial domain or frequency domain where the watermark is embedded is a primary requirement. Further, it must use the human visual system to optimize the location and strength of embedding.

The signal embedding techniques employed are either adaptive or non-adaptive and moreover, redundancy must be involved.

Several proposals have been exploit the benefits of this algorithm for videopiracy detection, but the problem is still far off from a solution.



ANNA UNIVERSITY, CHENNAI - 25
M.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES

S099306319614M

Folio No.

AUCD328405



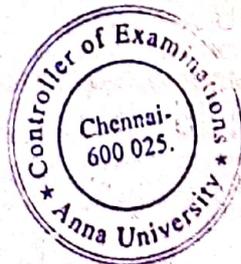
| | | | | | |
|----------------------------|--|--------|------|---------------------------------|--------------|
| NAME OF THE CANDIDATE | SURESH M | | | REGISTER NO. | 921311401023 |
| DATE OF BIRTH | 07-MAR-89 | GENDER | MALE | MONTH & YEAR OF LAST APPEARANCE | JUN 2013 |
| COLLEGE OF STUDY | PSNA COLLEGE OF ENGINEERING AND TECHNOLOGY | | | REGULATIONS | 2009 |
| PROGRAMME & SPECIALIZATION | M.E. Applied Electronics | | | | |

| SEM. NO. | COURSE CODE | COURSE TITLE | CREDITS | LETTER GRADE | GRADE POINT | MONTH & YEAR OF PASSING |
|----------|-------------|---|---------|--------------|-------------|-------------------------|
| 01 | AP9211 | Advanced Digital Signal Processing | 3 | A | 9 | JAN 2012 |
| 01 | AP9212 | Advanced Digital System Design | 3 | C | 7 | JAN 2012 |
| 01 | AP9213 | Advanced Microprocessors and Microcontrollers | 3 | A | 9 | JAN 2012 |
| 01 | MA9217 | Applied Mathematics for Electronics Engineers | 4 | D | 6 | JAN 2012 |
| 01 | VL9212 | VLSI Design Techniques | 3 | C | 7 | JAN 2012 |
| 01 | AP9251 | Digital Image Processing | 3 | C | 7 | JAN 2012 |
| 01 | AP9217 | Electronics Design Laboratory I | 2 | A | 9 | JAN 2012 |
| 02 | AP9221 | Analysis and Design of Analog Integrated Circuits | 3 | C | 7 | JUN 2012 |
| 02 | AP9222 | Computer Architecture and Parallel Processing | 3 | C | 7 | JUN 2012 |
| 02 | AP9223 | Digital Control Engineering | 3 | E | 5 | JUN 2012 |
| 02 | AP9224 | Embedded Systems | 3 | C | 7 | JUN 2012 |
| 02 | CP9212 | High Performance Computer Networks | 3 | D | 6 | JUN 2012 |
| 02 | VL9211 | DSP Integrated Circuits | 3 | B | 8 | JUN 2012 |
| 02 | AP9227 | Electronics Design Laboratory II | 2 | A | 9 | JUN 2012 |
| 03 | AP9252 | Neural Networks and Its Applications | 3 | D | 6 | NOV 2012 |
| 03 | VL9221 | CAD for VLSI Circuits | 3 | B | 8 | NOV 2012 |
| 03 | VL9253 | VLSI Signal Processing | 3 | B | 8 | NOV 2012 |
| 03 | AP9234 | Project Work (Phase I) | 6 | S | 10 | NOV 2012 |
| 04 | AP9241 | Project Work (Phase II) | 12 | S | 10 | JUN 2013 |

*** End of Statement ***

Cumulative Grade Point Average : 7.99

Classification : FIRST CLASS



Medium of Instruction : ENGLISH

Chennai - 25.

Date : 19/08/2013

SIGNATURE OF THE STUDENT



CONTROLLER OF EXAMINATIONS



ANNA UNIVERSITY, CHENNAI - 25
M.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES

S099306319614M

Folio No. AUCD328405



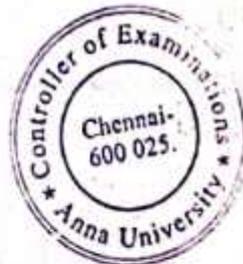
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|----------------------------|--|--------|------|---------------------------------|--------------|
| NAME OF THE CANDIDATE | SURESH M | | | REGISTER NO | 921311401023 |
| DATE OF BIRTH | 07-MAR-89 | GENDER | MALE | MONTH & YEAR OF LAST APPEARANCE | JUN 2013 |
| COLLEGE OF STUDY | PSNA COLLEGE OF ENGINEERING AND TECHNOLOGY | | | REGULATIONS | 2009 |
| PROGRAMME & SPECIALIZATION | M.E. Applied Electronics | | | | |

| SEM. NO. | COURSE CODE | COURSE TITLE | CREDITS | LETTER GRADE | GRADE POINT | MONTH & YEAR OF PASSING |
|----------|-------------|---|---------|--------------|-------------|-------------------------|
| 01 | AP9211 | Advanced Digital Signal Processing | 3 | A | 9 | JAN 2012 |
| 01 | AP9212 | Advanced Digital System Design | 3 | C | 7 | JAN 2012 |
| 01 | AP9213 | Advanced Microprocessors and Microcontrollers | 3 | A | 9 | JAN 2012 |
| 01 | MA9217 | Applied Mathematics for Electronics Engineers | 4 | D | 6 | JAN 2012 |
| 01 | VL9212 | VLSI Design Techniques | 3 | C | 7 | JAN 2012 |
| 01 | AP9251 | Digital Image Processing | 3 | C | 7 | JAN 2012 |
| 01 | AP9217 | Electronics Design Laboratory I | 2 | A | 9 | JAN 2012 |
| 02 | AP9221 | Analysis and Design of Analog Integrated Circuits | 3 | C | 7 | JUN 2012 |
| 02 | AP9222 | Computer Architecture and Parallel Processing | 3 | C | 7 | JUN 2012 |
| 02 | AP9223 | Digital Control Engineering | 3 | E | 5 | JUN 2012 |
| 02 | AP9224 | Embedded Systems | 3 | C | 7 | JUN 2012 |
| 02 | CP9212 | High Performance Computer Networks | 3 | D | 6 | JUN 2012 |
| 02 | VL9211 | DSP Integrated Circuits | 3 | B | 8 | JUN 2012 |
| 02 | AP9227 | Electronics Design Laboratory II | 2 | A | 9 | JUN 2012 |
| 03 | AP9252 | Neural Networks and Its Applications | 3 | D | 6 | NOV 2012 |
| 03 | VL9221 | CAD for VLSI Circuits | 3 | B | 8 | NOV 2012 |
| 03 | VL9253 | VLSI Signal Processing | 3 | B | 8 | NOV 2012 |
| 03 | AP9234 | Project Work (Phase I) | 6 | S | 10 | NOV 2012 |
| 04 | AP9241 | Project Work (Phase II) | 12 | S | 10 | JUN 2013 |

*** End of Statement ***

Cumulative Grade Point Average : 7.99

Classification : FIRST CLASS



Medium of Instruction : ENGLISH

Chennai - 25.

Date : 19/08/2013

SIGNATURE OF THE STUDENT



CONTROLLER OF EXAMINATIONS



"Kalvithonthai"
Thiru. R.S. Kothandaraman
Founder



PSNA



College of Engineering & Technology

(Approved by AICTE, Accredited by National Board of Accreditation, New Delhi
Affiliated to Anna University, Chennai)
Best Engineering College Award - 2007 by ISTE, New Delhi

TRANSFER CERTIFICATE

TC No: 244

Roll No.: 11MEAE23

1. Name of the student : SURESH M
2. Date of birth as entered in SSLC Book/Mark Sheet : 07-Mar-89 SEVENTH MARCH NINETEEN EIGHTY NINE
3. Name of the Parent : MARIAPPAN
4. Sex : MALE
5. Nationality : INDIAN
6. Community : BC
7. Religion & Caste : HINDU - VANIYAR
8. Date of Admission : 16-Jun-11
9. Medium of Study : ENGLISH
10. Date of Leaving : 16-Jul-13
11. Class in which the student was studying at the time of leaving : FINAL YEAR M.E. (APPLIED ELECTRONICS - 2 YEAR FULL TIME) PROGRAMME
12. Whether qualified for promotion to the higher class : REFER MARK SHEETS
13. Whether the student paid all the dues payable to the College : YES
14. Reason for Leaving : COURSE COMPLETED
15. Whether the student participated in ragging punished for : NO
16. Conduct and Character : *Good*
17. Date on which the application for Transfer Certificate was made : *09.10.2013*



[Signature]
Principal
PRINCIPAL
P.S.N.A College of Engg & Tech
Dindigul - 624 622.



RajaRajeswari College of Engineering

(Approved by AICTE, New Delhi, Govt. of Karnataka & Affiliated to Visvesvaraya Technological University, Belgaum)

Sponsored by: MOOGAMBIGAI CHARITABLE AND EDUCATIONAL TRUST



Ref: RRCE-Blr/EST/2022-23/1906

Date: 16.09.2022

SERVICE CERTIFICATE

This is to certify that Mr. Suresh M has been working in our esteemed Institution, the RajaRajeswari College of Engineering, Bengaluru as Assistant Professor in the Department of Electronics & Communication Engineering from 21.07.2015 to till date.

This certificate is issued for the purpose of applying for registration for Ph.D. entrance examination.

Principal
Principal
RAJARAJESWARI
COLLEGE OF ENGINEERING
Ramohalli Cross, Bengaluru-77

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आयकर विभाग

भारत सरकार

INCOME TAX DEPARTMENT



GOVT. OF INDIA



स्थायी लेखा संख्या कार्ड

Permanent Account Number Card

CIGPSS9382H

नाम / Name

SURESH M

पिता का नाम / Father's Name

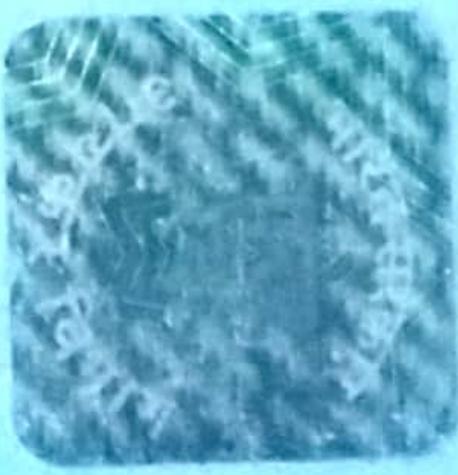
MARIAPPAN

जन्म की तारीख / Date of Birth

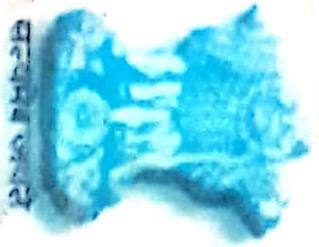
07/03/1989

S. Suresh

हस्ताक्षर / Signature



01042017



India Driving Licence(Tamilnadu)

Form 7

DOI 06/12/2013

D.L.No : TN79 20130004418

Name : SURESH M

S/D/W of : MARIAPPAN

Address :
30/2, BALAVINAYAGAR KOIL STREET
T.N.PUTHUGUDI, PULIYANGUDI PO
SIVAGIRI TK

Temp. Addr:
RAJASEKAR DRIVING SCHOOL

D.O.B. : 07/03/1989

B.G.:

Punishments:



Licensed to drive throughout India, vehicles of the following descriptions
M/CYCL. WG 06/12/2013 TN79

Non-Transport Veh. Valid upto 05/12/2033



[Handwritten Signature]
Signature/L.T.I
of the Holder

[Handwritten Signature]
Asst. Licensing Authority
RTO, SANKARANKOVIL

R.Dis. 264 /2006 dt.

BC

சான்றிதழ் எண் :
Certificate No. :மாவட்டக் குறியீடு எண் :
District Code :வட்டக் குறியீடு எண் :
Taluk Code :கிராமக் குறியீடு எண் :
Village Code :

| | |
|---|---|
| 1 | 8 |
| 0 | 6 |
| 0 | 2 |
| 0 | 0 |

5677614

சாதிச் சான்றிதழ்

COMMUNITY CERTIFICATE

திருநெல்வேலி

முனிவகுடி

மாவட்டம்

வட்டம்

செலவன்/நகரம், திரு/திருமதி/செல்வி/

செல்வன் சரேஜ்

தகப்பனார்/கணவர்

பெயர் மாரியப்பன்

இத்து - உரிமையர்

வகுப்பைச் சார்ந்தவர். அரசு ஆணை நிலை எண் 28, பிற்பட்ட மற்றும் மிகவும் பிற்பட்ட பிரிவின் நலத் துறை, நாள் 19-7-1994 வரிசை எண் 131 படி, பிற்பட்ட பிரிவினைச் சார்ந்தவர் எனச் சான்றளிக்கப்படுகிறது.

This is to certify that..... Son / Daughter of
Thiru..... of.....
..... Village / Town,..... Taluk,.....

..... District of the State of Tamil Nadu belongs to
..... Community, which is recognised as
a Backward Class as per Government Order (Ms.) No. 28, Backward Classes
and Most Backward Classes Welfare, dated 19th July 1994
vide Serial No.....

1. திரு/திருமதி/செல்வன்/செல்வி சரேஜ்

என்பவரும் அவருடைய குடும்பத்தினரும் தமிழ்நாட்டில் திருநெல்வேலி

மாவட்டத்தில் வட்டத்தில் முனிவகுடி

கிராமத்தில்/நகரத்தில் கசித்து வருகிறார்கள் எனச் சான்றளிக்கப்படுகிறது.

2. It is certified that Thiru/Tmt./Selvan/Selvi..... and
his/her family ordinarily reside(s) at Village/
Town..... Taluk District of

Tamil Nadu

கையொப்பம் :
Signature :நாள் :
Date :பெயர் (குனி எழுத்துக்களில்) :
Name (in Capital Letters) :பதவிமண்டல துணை வட்டாட்சியர்
Designation : சிவசிரி.

2/5/06



ANNA UNIVERSITY CHENNAI, CHENNAI - 600 025

B.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF MARKS

Folio No. H936310

309772010052

| NAME OF THE CANDIDATE | | SURESH M | | | | REGISTER NO. | 90206106041 | | | | | | |
|-----------------------|--------------|--|-----|-----|---------------|---------------------------------|-------------|--------------|--|-----|-----|---------------|-------------------------|
| COLLEGE OF STUDY | | 902: Bharath Niketan Engineering College | | | | MONTH & YEAR OF LAST APPEARANCE | April 2010 | | | | | | |
| PROGRAMME & BRANCH | | B.E. Electronics and Communication Engineering | | | | REGULATIONS | 2004 | | | | | | |
| SEM. | SUBJECT CODE | SUBJECT TITLE | MAX | MIN | MARKS SECURED | MONTH & YEAR OF PASSING | SEM. | SUBJECT CODE | SUBJECT TITLE | MAX | MIN | MARKS SECURED | MONTH & YEAR OF PASSING |
| 01 | CY1X01 | Engineering Chemistry | 100 | 50 | 052 | APR 2007 | 06 | GE1352 | Communication Skills Laboratory | 100 | 50 | 086 | APR 2009 |
| 01 | EC1X01 | Electron Devices | 100 | 50 | 055 | APR 2007 | 07 | EC1009 | Digital Image Processing | 100 | 50 | 068 | NOV 2009 |
| 01 | EC1X02 | Circuit Analysis | 100 | 50 | 087 | APR 2007 | 07 | EC1401 | VLSI Design | 100 | 50 | 062 | NOV 2009 |
| 01 | GE1X01 | Engineering Graphics | 100 | 50 | 052 | APR 2007 | 07 | EC1402 | Optical Communication | 100 | 50 | 056 | NOV 2009 |
| 01 | GE1X02 | Computer Programming | 100 | 50 | 060 | APR 2007 | 07 | EC1403 | Microwave Engineering | 100 | 50 | 068 | NOV 2009 |
| 01 | HS1X01 | Technical English | 100 | 50 | 066 | APR 2007 | 07 | EC1003 | Computer Hardware and Interfacing | 100 | 50 | 072 | NOV 2009 |
| 01 | MA1X01 | Engineering Mathematics - I | 100 | 50 | 062 | APR 2007 | 07 | MG1401 | Total Quality Management | 100 | 50 | 063 | NOV 2009 |
| 01 | PH1X01 | Engineering Physics | 100 | 50 | 068 | APR 2007 | 07 | EC1404 | VLSI Lab | 100 | 50 | 096 | NOV 2009 |
| 01 | GE1X03 | Engineering Practices Laboratory | 100 | 50 | 084 | APR 2007 | 07 | EC1405 | Optical and Microwave Lab | 100 | 50 | 096 | NOV 2009 |
| 01 | PC1X01 | Physics and Chemistry Laboratory | 100 | 50 | 075 | APR 2007 | 08 | EC1451 | Mobile Communication | 100 | 50 | 064 | APR 2010 |
| 03 | CS1151 | Data Structures | 100 | 50 | 062 | NOV 2007 | 08 | EC1013 | Wireless networks | 100 | 50 | 057 | APR 2010 |
| 03 | CY1201 | Environmental Science and Engineering | 100 | 50 | 076 | NOV 2007 | 08 | EC1014 | Telecommunication Switching and Networks | 100 | 50 | 057 | APR 2010 |
| 03 | EC1201 | Digital Electronics | 100 | 50 | 065 | NOV 2007 | 08 | EC1452 | Project Work | 200 | 100 | 192 | APR 2010 |
| 03 | EC1203 | Electronic Circuits - I | 100 | 50 | 070 | NOV 2007 | | | | | | | |
| 03 | EE1211 | Electrical Machines | 100 | 50 | 058 | NOV 2007 | | | | | | | |
| 03 | MA1201 | Mathematics - III | 100 | 50 | 061 | NOV 2007 | | | | | | | |
| 03 | CS1152 | Data Structure Lab | 100 | 50 | 095 | NOV 2007 | | | | | | | |
| 03 | EC1204 | Electronic Devices and Circuits Lab I | 100 | 50 | 099 | NOV 2007 | | | | | | | |
| 03 | EE1261 | Electrical Machines Lab | 100 | 50 | 096 | NOV 2007 | | | | | | | |
| 04 | EC1251 | Electronic Circuits II | 100 | 50 | 062 | APR 2008 | | | | | | | |
| 04 | EC1252 | Signals and Systems | 100 | 50 | 063 | APR 2008 | | | | | | | |
| 04 | EC1253 | Electromagnetic Fields | 100 | 50 | 062 | APR 2008 | | | | | | | |
| 04 | EC1254 | Linear Integrated Circuits | 100 | 50 | 074 | APR 2008 | | | | | | | |
| 04 | EC1255 | Measurements and Instrumentation | 100 | 50 | 067 | APR 2008 | | | | | | | |
| 04 | MA1254 | Random Processes | 100 | 50 | 056 | APR 2008 | | | | | | | |
| 04 | EC1256 | Electronics circuits II and simulation lab | 100 | 50 | 097 | APR 2008 | | | | | | | |
| 04 | EC1257 | Linear Integrated Circuit Lab | 100 | 50 | 095 | APR 2008 | | | | | | | |
| 04 | EC1258 | Digital Electronics lab | 100 | 50 | 097 | APR 2008 | | | | | | | |
| 05 | EC1301 | Communication Theory | 100 | 50 | 062 | NOV 2008 | | | | | | | |
| 05 | EC1302 | Digital Signal Processing | 100 | 50 | 068 | NOV 2008 | | | | | | | |
| 05 | EC1303 | Microprocessors and Its Applications | 100 | 50 | 066 | NOV 2008 | | | | | | | |
| 05 | EC1304 | Control Systems | 100 | 50 | 062 | NOV 2008 | | | | | | | |
| 05 | EC1305 | Transmission Lines and Waveguides | 100 | 50 | 072 | NOV 2008 | | | | | | | |
| 05 | MA1251 | Numerical Methods | 100 | 50 | 075 | NOV 2008 | | | | | | | |
| 05 | EC1306 | Digital Signal Processing Lab | 100 | 50 | 098 | NOV 2008 | | | | | | | |
| 05 | EC1307 | Microprocessor and Application Lab | 100 | 50 | 099 | NOV 2008 | | | | | | | |
| 06 | CS1251 | Computer Architecture | 100 | 50 | 056 | APR 2009 | | | | | | | |
| 06 | CS1302 | Computer Networks | 100 | 50 | 071 | APR 2009 | | | | | | | |
| 06 | EC1351 | Digital Communication | 100 | 50 | 064 | APR 2009 | | | | | | | |
| 06 | EC1352 | Antenna and Wave Propagation | 100 | 50 | 056 | APR 2009 | | | | | | | |
| 06 | MG1351 | Principles of Management | 100 | 50 | 077 | APR 2009 | | | | | | | |
| 06 | EC1020 | Speech Processing | 100 | 50 | 072 | APR 2009 | | | | | | | |
| 06 | EC1353 | Communication on System Lab | 100 | 50 | 098 | APR 2009 | | | | | | | |
| 06 | EC1354 | Networks Lab | 100 | 50 | 096 | APR 2009 | | | | | | | |
| 06 | EC1355 | Electronic System Design Lab | 100 | 50 | 099 | APR 2009 | | | | | | | |

* Annual Pattern is followed in the First Year of study and Semester Pattern is followed from Second Year onwards.
End of Statement
Classification : FIRST CLASS WITH DISTINCTION
Total Marks (from 3rd to 8th semester) : 3683 / 4900
Percentage (rounded to nearest integer) : 75



Medium of Instruction : ENGLISH

Chennai - 600 025.

Date : 04/05/2010

V. S. M.
Controller of Examinations

ANNEXURE-I

CERTIFICATE FROM THE ORGANISATION WHERE THE CANDIDATE IS EMPLOYED

Certified that Mr./Ms./Mrs. M. SURESH is
employed as an ASSISTANT PROFESSOR (Designation) in the
ELECTRONICS AND COMMUNICATION ENGINEERING (Department/Division Name)
of RAJARAJESWAR COLLEGE OF ENGINEERING.
(Institution/Industry Name).

We have no objection in forwarding his/her application for the Ph.D. Research Programme.

FOR FULL TIME:

The candidate will be sanctioned leave for the duration of the research programme and will be relieved from duty from _____ to _____ to undertake the full time research work in the University.

FOR PART TIME: ✓

The candidate will be permitted to undertake part time study in the University/College and will be allowed to be present for discussions with the supervisor, attending course works, conduct of experiments and participations in seminars and related presentations. Further the required facilities at our organization will also be provided to the candidate for doing research.

Date: 04/11/2023.

RASEC 7/11/23
Signature of the Head of Organization with office seal

Principal
RAJARAJESWARI
COLLEGE OF ENGINEERING
Ramohalli Cross, Bengaluru-74