



Anand Nagar, Krishnankoil - 626126, Srivilliputtur (via), Virudhunagar District, Tamilnadu.

## APPLICATION FOR ADMISSION TO Ph.D. PROGRAMMES

Date of Application:27-09-2024

Department	ELECTRONICS AND COMMUNICATION ENGINEERING	Application No.	20240059
Area of Research	KALASALINGAM ACADEMY OF RESEARCH AND EDUCATION	Research Mode	PART TIME

**Name** :IYAPPAN P  
**Date of Birth / Age** :17-04-1992 / 32 Years  
**Gender** :MALE  
**Category** :OBC  
**e-Mail ID** :iyps1992@gmail.com  
**Mobile** :9500864673



<b>Father's/Husband's Name</b>	PIRAMANAYAGAM S	<b>Father's/Husband's Occupation</b>	FARMER
<b>Family Income</b>	180000	<b>Residential Type</b>	RURAL
<b>Birth Place</b>	ZAMIN SINGAMPATTI	<b>Mother Tongue</b>	TAMIL
<b>Religion</b>	HINDU	<b>Martial Status</b>	MARRIED
<b>Aadhaar No.</b>	370421867006	<b>PAN No.</b>	ADSPI0394L
<b>Physically Challenged</b>	NO	<b>Type of Disability</b>	-
<b>Address for Communication:</b> 1/73 MAIN ROAD / ZAMIN SINGAMPATTI AMBASAMUDRAM TIRUNELVELI DISTRICT TAMILNADU INDIA Pin-627416		<b>Permenant Address:</b> 1/73 MAIN ROAD / ZAMIN SINGAMPATTI AMBASAMUDRAM TIRUNELVELI DISTRICT TAMILNADU INDIA Pin-627416	

Qualification						
Degree	Discipline	College/university	Year Passed	AVG/CGPA	Class	Mode
SSLC	TAMIL	ST MICHAELS HIGH SCHOOL	2007	64%	1ST	REGULAR
HSC	COMPUTER SCIENCE	ST MARYS HIGHER SECONDARY SCHOOL	2009	63%	1ST	REGULAR
BE	ECE	SCAD COLLEGE OF ENGINEERING AND TECHNOLOGY	2018	6.58	2ND	REGULAR
ME	APPLIED ELECTRONICS	PSN COLLEGE OF ENGINEERING AND TECHNOLOGY	2024	7.1	1ST	REGULAR

Experience					
Organization	Designation	Experience From	Experience TO	Work Nature	
PSN POLYTECHNIC COLLEGE	LECTURER	2020-10-24	2022-04-07	TEACHING	
ANALOGICS TECH INDIA LTD	PCB DESIGNER	2016-07-17	2018-08-14	DESIGNING	
CLANCOR TECHNOVATES PVT LTD	DEVELOPER	2018-10-30	2020-09-25	DEVELOPING	

ELYSIUM ACADEMY	MANAGER	2023-06-24	2024-09-27	MANAGING
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Payment Details

Transaction ID	Reference	Date of transaction	Amount	Status
20240059_240927201147	ZHD5QIB01W0OA9	27-09-2024	600	SUCCESS

A research proposal format for the Department of Electronics and Communications Engineering should be detailed and well-structured. Below is a typical format:

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## 1. Title of the Research Proposal

- The title should be clear, concise, and descriptive of the research focus. It should reflect the core of the research problem in the field of electronics and communications.

## 2. Abstract

- A brief summary (around 250–300 words) outlining the key aspects of the proposal, including:
  - Research problem or gap.
  - Objectives.
  - Methodology.
  - Expected outcomes or contributions.

## 3. Introduction

- **Background and Context:** Explain the research topic's background and provide the context within the field of electronics and communications.
- **Research Problem/Statement:** Clearly define the specific problem or gap that your research aims to address.
- **Rationale:** Discuss the importance of the research in solving specific challenges in electronics and communications. Why is it worth pursuing?

## 4. Literature Review

- **Overview of Relevant Research:** Summarize existing literature, trends, and past research related to the topic.
- **Gap Identification:** Highlight the gaps in the current knowledge that your research will address.
- **Relation to Your Research:** Show how your research will build on or challenge existing work.

## 5. Research Objectives

- **Main Objective:** State the primary goal of your research.
- **Specific Objectives:** Break down the main objective into specific, measurable goals.

## 6. Research Questions or Hypothesis

- List the key questions your research will answer or the hypothesis you will test.
- These should align with the research objectives.

## 7. Methodology

- **Research Approach:** Describe whether your research is experimental, theoretical, simulation-based, or a combination.
- **Tools and Techniques:** Specify the tools, equipment, software, or models you will use (e.g., FPGA, MATLAB, HFSS, communication protocols, etc.).
- **Data Collection:** Describe how data will be gathered (e.g., simulations, measurements, hardware testing).
- **Data Analysis:** Explain the techniques you will use to analyze the data.
- **Timeline:** Include a timeline or project schedule with key milestones.

## 8. Expected Outcomes and Contributions

- **Predicted Results:** Outline the results you expect to achieve.
- **Contribution to the Field:** Discuss the practical and theoretical significance of your research in electronics and communications engineering. How will it advance the field?
- **Potential Applications:** Describe the real-world applications of your research.

## 9. Limitations

- Mention any potential limitations or challenges in your research, such as resource constraints, technical limitations, or assumptions that could affect the results.

## 10. Budget (if required)

- **Costs and Resources:** Provide a breakdown of the financial resources required, including equipment, software, testing facilities, and any other resources necessary to complete the research.

## 11. Ethical Considerations (if applicable)

- If your research involves human subjects, environmental concerns, or other ethical issues, briefly outline how you will address them.

## 12. Conclusion

- Summarize the significance of your research and reiterate the impact it could have on the field of electronics and communications.

## 13. References

- Include a comprehensive list of all sources cited in the proposal. Use the citation style required by your department (typically IEEE or APA for engineering disciplines).

## 14. Appendices (if applicable)

- Attach any supplementary materials such as diagrams, preliminary data, or technical specifications.

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This structure can be modified based on the specific guidelines provided by the Department of Electronics and Communications Engineering in your institution.

S. No. 04005



PSN COLLEGE OF ENGINEERING AND TECHNOLOGY, TIRUNELVELI-627 152, INDIA

(Autonomous Institution &amp; Affiliated to Anna University, Chennai)

DEGREE EXAMINATIONS

GRADE SHEET



NAME OF THE CANDIDATE	IYAPPAN P			REGISTER No.	95222220001
DATE OF BIRTH	17-04-1992	GENDER	Male	DATE OF PUBLICATION :	10-01-2023
MONTH & YEAR OF EXAMINATION	NOV - 2022			REGULATIONS	2022
PROGRAMME & SPECIALIZATION	M.E. Applied Electronics				

SEM. No.	COURSE CODE	COURSE NAME	CREDITS	LETTER GRADE	GRADE POINT	RESULT
01	MA620001	APPLIED MATHEMATICS FOR ENGINEERS	4	U	0	RA
01	AP620001	ADVANCED DIGITAL SYSTEM DESIGN	3	B	6	PASS
01	AP620002	ADVANCED DIGITAL SIGNAL PROCESSING	3	B+	7	PASS
01	AP620003	ADVANCED DIGITAL IMAGE PROCESSING	3	B+	7	PASS
01	AP620004	RESEARCH METHODOLOGY	3	U	0	RA
01	AP620501	TECHNICAL SEMINAR-I	1	A	8	PASS
01	AP620101	ELECTRONIC SYSTEM DESIGN LAB I	2	A	8	PASS
01	AP620205	SYSTEM ON CHIP DESIGN	3	B+	7	PASS

&lt;&lt; END OF STATEMENT &gt;&gt;

Semester	I	II	III	IV
Credits Registered	21			
Credits Earned	14			
Grade Points Earned	35			
Grade Point Average (GPA)	6.929			
Cumulative Credits Earned	15	Cumulative Grade Point Average (CGPA)		7
				Medium of Instruction : ENGLISH

RA – Reappearance is Required

W – Withdrawal

SE – Sports Exemption

\* Absent for End Semester Examination



SEAL

SIGNATURE OF THE CANDIDATE



CONTROLLER OF EXAMINATIONS

Tirunelveli-627 152

Date : 30-01-2023

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S. No. **04069**

**PSN COLLEGE OF ENGINEERING AND TECHNOLOGY, TIRUNELVELI-627 152, INDIA**

(Autonomous Institution & Affiliated to Anna University, Chennai)

**DEGREE EXAMINATIONS**

**GRADE SHEET**



NAME OF THE CANDIDATE	<b>IYAPPAN P</b>		REGISTER No.	<b>95222220001</b>
DATE OF BIRTH	<b>17-04-1992</b>	GENDER	<b>Male</b>	DATE OF PUBLICATION :
MONTH & YEAR OF EXAMINATION	<b>APR-2023</b>		REGULATIONS	<b>2022</b>
PROGRAMME & SPECIALIZATION	<b>M.E. Applied Electronics</b>			

SEM. No.	COURSE CODE	COURSE NAME	CREDITS	LETTER GRADE	GRADE POINT	RESULT
02	AP620005	ASIC AND FPGA DESIGN	3	B	6	PASS
02	AP620006	DIGITAL CONTROL ENGINEERING	3	U	0	RA*
02	AP620206	ELECTROMAGNETIC INTERFERENCE AND COMPATIBILITY IN SYSTEM DESIGN	3	B	6	PASS
02	AP620007	HIGH PERFORMANCE NETWORKS	3	U	0	RA*
02	AP620008	SATELLITE COMMUNICATION	3	B+	7	PASS
02	AP620102	ELECTRONIC SYSTEM DESIGN LAB II	2	A	8	PASS
02	AP620210	MICROWAVE REMOTE SENSING	3	B	6	PASS
01	MA620001	APPLIED MATHEMATICS FOR ENGINEERS	4	U	0	RA
01	AP620004	RESEARCH METHODOLOGY	3	U	0	RA*
<< END OF STATEMENT >>						

Semester	I	II	III	IV
Credits Registered	7	20		
Credits Earned	0	14		
Grade Points Earned	0	33		
Grade Point Average (GPA)	0	6.5		
Cumulative Credits Earned	29		Cumulative Grade Point Average (CGPA)	6.759
Medium of Instruction : ENGLISH				

RA – Reappearance is Required

W – Withdrawal

SE – Sports Exemption

\* Absent for End Semester Examination



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Date : 30-07-2023

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DEGREE EXAMINATIONS

GRADE SHEET

S. No. 04678



NAME OF THE CANDIDATE	IYAPPAN P		REGISTER No.	95222220001	
DATE OF BIRTH	17-04-1992	GENDER	Male	DATE OF PUBLICATION	10-01-2024
MONTH & YEAR OF EXAMINATION	NOV - 2023		REGULATIONS	2022	
PROGRAMME & SPECIALIZATION	M.E. Applied Electronics				

SEM. No.	COURSE CODE	COURSE NAME	CREDITS	LETTER GRADE	GRADE POINT	RESULT
03	CS624219	SOFT COMPUTING TECHNIQUES	3	U	0	RA
03	AP620218	ADVANCED MICROPROCESSOR AND MICROCONTROLLER	3	U	0	RA
03	ES625903	REAL TIME EMBEDDED SYSTEM	3	B+	7	PASS
03	AP620301	PROJECT PHASE- I	8	A	8	PASS
02	AP620006	DIGITAL CONTROL ENGINEERING	3	B+	7	PASS
02	AP620007	HIGH PERFORMANCE NETWORKS	3	B	6	PASS
01	MA620001	APPLIED MATHEMATICS FOR ENGINEERS	4	U	0	RA*
01	AP620004	RESEARCH METHODOLOGY	3	U	0	RA*

<< END OF STATEMENT >>

Semester	I	II	III	IV
Credits Registered	7	6	17	
Credits Earned	0	6	11	
Grade Points Earned	0	13	15	
Grade Point Average (GPA)	0	6.5	7.727	
Cumulative Credits Earned	46	Cumulative Grade Point Average (CGPA)		6.957
Medium of Instruction : ENGLISH				

RA - Reappearance is Required

W - Withdrawal

SE - Sports Exemption

\* Absent for End Semester Examination

Tirunelveli-627 152  
Date : 30-01-2024

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# PSN COLLEGE OF ENGINEERING AND TECHNOLOGY, TIRUNELVELI-627 152, INDIA

(Autonomous Institution & Affiliated to Anna University, Chennai)

## DEGREE EXAMINATIONS

### GRADE SHEET

S. No. **04670**



NAME OF THE CANDIDATE	<b>IYAPPAN P</b>		REGISTER No.	<b>95222220001</b>	
DATE OF BIRTH	<b>17-04-1992</b>	GENDER	<b>Male</b>	DATE OF PUBLICATION	<b>30-06-2024</b>
MONTH & YEAR OF EXAMINATION	<b>APR-2024</b>			REGULATIONS	<b>2022</b>
PROGRAMME & SPECIALIZATION	<b>M.E. Applied Electronics</b>				

SEM. No.	COURSE CODE	COURSE NAME	CREDITS	LETTER GRADE	GRADE POINT	RESULT
04	AP620502	TECHNICAL SEMINAR - II	1	A	8	PASS
04	AP620302-	PROJECT PHASE - II & JOURNAL PUBLICATION	12	A+	9	PASS
03	CS624219	SOFT COMPUTING TECHNIQUES	3	B+	7	PASS
03	AP620218	ADVANCED MICROPROCESSOR AND MICROCONTROLLER	3	A+	9	PASS
01	MA620001	APPLIED MATHEMATICS FOR ENGINEERS	4	C	5	PASS
01	AP620004	RESEARCH METHODOLOGY	3	A	8	PASS
<< END OF STATEMENT >>						

Semester	I	II	III	IV
Credits Registered	7		6	
Credits Earned	7		6	
Grade Points Earned	13		16	
Grade Point Average (GPA)	6.286		8	
Cumulative Credits Earned	72		Cumulative Grade Point Average (CGPA)	7.333
Medium of Instruction : <b>ENGLISH</b>				

RA – Reappearance is Required

W – Withdrawal

SE – Sports Exemption

\* Absent for End Semester Examination

Tirunelveli-627 152  
Date : 30-07-2024



SEAL

SIGNATURE OF THE CANDIDATE



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# Anna University



Reg.No. 952812106309/RG

*The Syndicate of the Anna University hereby makes known that*  
**IYAPPAN P** *has been admitted to the* **DEGREE OF BACHELOR OF ENGINEERING** *in* **ELECTRONICS AND COMMUNICATION ENGINEERING** *under the Faculty of Information and Communication Engineering, having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in* **SECOND CLASS** *at the Examination held in*  
**NOVEMBER 2018 .**

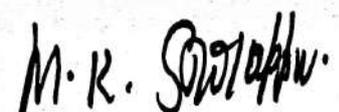
*Given under the Seal of the University*



Chennai 600 025  
India  
July 2019

  
Controller of Examinations

  
Registrar

  
Vice-Chancellor



# ANNA UNIVERSITY, CHENNAI - 600 025

## B.E. DEGREE EXAMINATIONS CONSOLIDATED STATEMENT OF GRADES

Folio No. AUR1280106  
11131795699634P



NAME OF THE CANDIDATE							REGISTER NO.		REGULATIONS				
IYAPPAN P							952812106309		2008				
COLLEGE OF STUDY							GENDER		DATE OF BIRTH				
SCAD COLLEGE OF ENGINEERING AND TECHNOLOGY							MALE		17-APR-92				
PROGRAMME & BRANCH							MONTH & YEAR OF LAST APPEARANCE		MEDIUM OF INSTRUCTION				
B.E. Electronics and Communication Engineering							November 2018		English				
SEM	COURSE CODE	COURSE TITLE	C	LG	GP	MONTH & YEAR OF PASSING	SEM	COURSE CODE	COURSE TITLE	C	LG	GP	MONTH & YEAR OF PASSING
03	EC2201	Electrical Engineering	3	A	9	NOV 2013	07	EC2403	RF and Microwave Engineering	3	C	7	NOV 2015
03	EC2202	Data Structures and Object Oriented Programming in C++	3	E	5	APR 2014	07	EC2027	Advanced Microprocessors	3	C	7	NOV 2015
03	EC2203	Digital Electronics	4	C	7	NOV 2013	07	EC2029	Digital Image Processing	3	C	7	NOV 2015
03	EC2204	Signals and Systems	4	D	6	NOV 2014	07	GE2025	Professional Ethics in Engineering	3	C	7	NOV 2015
03	EC2205	Electronic Circuits - I	4	E	5	NOV 2013	07	EC2404	Electronics System Design Lab	2	S	10	NOV 2015
03	MA2211	Transforms and Partial Differential Equations	4	E	5	NOV 2013	07	EC2405	Optical & Microwave Lab	2	S	10	NOV 2015
03	EC2207	Digital Electronics Lab	2	A	9	NOV 2013	08	EC2045	Satellite Communication	3	E	5	APR 2016
03	EC2208	Electronics Circuits Lab - I	2	B	8	NOV 2013	08	EC2051	Wireless Sensor Networks	3	E	5	APR 2016
03	EC2209	Data Structures and Object Oriented Programming Lab	2	C	7	NOV 2013	08	EC2451	Project Work	6	A	9	APR 2016
04	EC2251	Electronics Circuits II	4	E	5	APR 2014							
04	EC2252	Communication Theory	4	E	5	APR 2014							
04	EC2253	Electromagnetic Fields	4	E	5	APR 2014							
04	EC2254	Linear Integrated Circuits	3	E	5	APR 2014							
04	EC2255	Control Systems	3	E	5	APR 2014							
04	MA2261	Probability and Random Processes	4	E	5	NOV 2018							
04	EC2257	Electronics Circuits II and Simulation Lab	2	S	10	APR 2014							
04	EC2258	Linear Integrated Circuit Lab	2	A	9	APR 2014							
04	EC2259	Electrical Engineering and Control System Lab	2	A	9	APR 2014							
05	EC2301	Digital Communication	3	D	6	NOV 2014							
05	EC2302	Digital Signal Processing	4	D	6	NOV 2014							
05	EC2303	Computer Architecture and Organization	3	C	7	NOV 2014							
05	EC2304	Microprocessors and Microcontrollers	4	E	5	NOV 2014							
05	EC2305	Transmission Lines and Wave Guides	4	E	5	NOV 2014							
05	GE2021	Environmental Science and Engineering	3	D	6	NOV 2014							
05	EC2306	Digital Signal Processing Lab	2	A	9	NOV 2014							
05	EC2307	Communication Systems Lab	2	B	8	NOV 2014							
05	EC2308	Microprocessor and Microcontroller Lab	2	A	9	NOV 2014							
06	EC2351	Measurements and Instrumentation	3	E	5	APR 2015							
06	EC2352	Computer Networks	3	D	6	APR 2015							
06	EC2353	Antenna and Wave Propagation	4	E	5	APR 2015							
06	EC2354	VLSI Design	3	C	7	APR 2015							
06	MG2351	Principles of Management	3	C	7	APR 2015							
06	EC2021	Medical Electronics	3	E	5	APR 2015							
06	EC2356	Computer Networks Lab	2	S	10	APR 2015							
06	EC2357	VLSI Design Lab	2	S	10	APR 2015							
06	GE2321	Communication Skills Laboratory	2	B	8	APR 2015							
07	EC2401	Wireless Communication	3	E	5	NOV 2015							
07	EC2402	Optical Communication and Networking	3	B	8	NOV 2015							

\*\*\* End of Statement \*\*\*  
Cumulative Grade Point Average : 6.58  
Classification : SECOND CLASS



SEM - Semester, C - Credits, LG - Letter Grade, GP - Grade Point

Range of Marks	91 - 100	81 - 90	71 - 80	61 - 70	57 - 60	50 - 56	< 50
Letter Grade	S	A	B	C	D	E	U
Grade Point	10	9	8	7	6	5	0

Chennai - 600 025

Date 15/04/2019

SIGNATURE OF THE STUDENT

$$CGPA = \frac{\sum C_i GP_i}{\sum C_i}$$

where  $C_i$  - is the credits assigned to the course  
 $GP_i$  - is the point corresponding to the grade obtained for each course  
 $n$  - is number of all courses successfully cleared during all the semesters



CONTROLLER OF EXAMINATIONS

# PSN COLLEGE OF ENGINEERING AND TECHNOLOGY

(An Autonomous Institution)

Approved by AICTE and Affiliated to Anna University  
(Accredited by NAAC with A+ Grade in Third Cycle)

Melathediyoor, Palayamkottai Taluk, Tirunelveli - 627 15



## TRANSFER & CONDUCT CERTIFICATE

T.C. No. 7683

Register No. 95222220001

- 1 Name of the Student (in Block letters) : **IYAPPAN P**
- 2 Name of the Father or Guardian : **Piramanayagam S**
- 3 Sex : **Male**
- 4 Date of Birth : **17.04.1992**
- 5 Date of Admission : **03.11.2022**
- 6 Course to which the student was admitted : **M.E../ Applied Electronics**
- 7 Duration of Course : **2 Years**
- 8 Semester studied at the time of leaving : **IV Semester**
- 9 Whether qualified to proceed to the next higher semester : **Yes**
- 10 Whether completed the course and become eligible for the award of the Degree : **Refer Mark Sheet**
- 11 Whether received the Degree Certificate : **No**
- 12 Actual date of leaving : **15.04.2024**
- 13 Whether paid all the fees and cleared the dues to the Institution : **Yes**
- 14 Character and Conduct : **Good**
- 15 Remarks, if any : **---**

Place

Date **03 JUN 2024**



*[Handwritten Signature]*  
24/6/24

**PRINCIPAL**

PSN COLLEGE OF ENGINEERING & TECHNOLOGY  
MELATHEDIYOOR, PALAYAMKOTTAI TALUK  
TIRUNELVELI DIST. - 627 152. *[Handwritten mark]*

Declaration of the Parent or Guardian :

I here by declare that the particulars recorded are correct and that no charge will be demanded by me in future

Signature of the Parent / Guardian

**ClanCor Technovates India Private Limited**

PAY SLIP FOR THE MONTH OF JANUARY-2020

<b>Employee Details</b>			
EMP ID	CSI001		
EMP Name	IYAPPAN P	Total Days in Month	31
Designation	Service Engineer	Total Paid Days	31
<b>GROSS EARNINGS INR</b>		<b>DEDUCTIONS</b>	
Basic and DA	14000.00	P.F	NIL
HRA	2000.00	ESI	NIL
Medical Allowance (MA)	-	TDS	NIL
Conveyance Allowance	-		
Performance Allowance	4000.00		
Spl. Allowance	-		
<b>GROSS SALARY</b>	<b>20000.00</b>	<b>TOTAL DEDUCTIONS</b>	<b>-</b>
<b>NET SALARY</b>	<b>INR 20000.00</b>		
<b>NET SALARY IN WORDS :</b>	<b>Rupees Twenty Thousand only</b>		

For Clancor Technovates India Private Limited

Authorised Signatory  
Accounts Manager

आयकर विभाग  
INCOME TAX DEPARTMENT



भारत सरकार  
GOVT. OF INDIA

IYAPPAN P

PRAMANAYAGAM

17/04/1992

Permanent Account Number

ADSPI0394L

*Iyappa*

Signature



*In case this card is lost / found, kindly inform / return to :*

Income Tax PAN Services Unit, UTIITSL

Plot No. 3, Sector 11, CBD Belapur,

Navi Mumbai - 400 614.

इस कार्ड के खोने/पाने पर कृपया सूचित करें/लौटाएं :

आयकर पैन सेवा यूनिट, यूटीआईआईटीएसएल

प्लॉट नं: ३, सेक्टर ११, सी.बी.डी.बेलापूर,

नवी मुंबई-४०० ६१४.

*Plw*

Licensed to drive throughout India, vehicles of the following descriptions  
M/CYCL. WG 21/05/2012 TN69z LMV 21/05/2012 TN69z

Non-Transport Veh. Valid upto 20/05/2032



  
Signature of the Holder

  
Asst. Licensing Authority  
UNIT OFFICE, KOVILPATTI



## India Driving Licence(Tamilnadu)

Form 7

DOI 21/05/2012

D.L.No : TN69z20120003588

Name : IYAPPAN P

S D W of : PIRAMANAYAGAM

Address :

1/73, MAIN ROAD

AMBASAMUDHRAM,

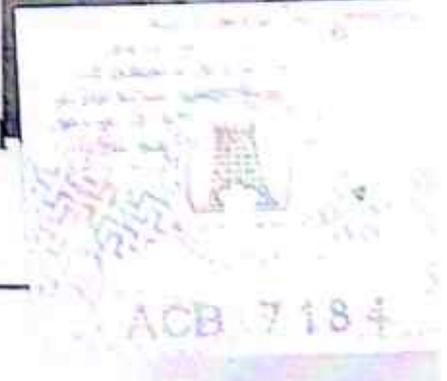
TIRUNELVELI DT 627416

Temp. Addr:

LAKSHMIAMMAL POLYTECHNIC COLLEGE DR

D.O.B. : 17/04/1992

B.G. :



Punishments:



09314

C. No. 835/07  
GCP-26/8-1,62,820 Cps-11-7-2001[p2-1]

R.Dis...../200 dt.

**DNC**



சான்றிதழ் எண் :  
Certificate No. :

**775906**

மாவட்டக் குறியீடு எண் :  
District Code :  
வட்டக் குறியீடு எண் :  
Taluk Code :  
கிராமக் குறியீடு எண் :  
Village Code :

1	8
0	7
0	3
1	

**சாதிச் சான்றிதழ்  
COMMUNITY CERTIFICATE**

திருவெங்கலைய மாவட்டம் அம்பாசமுத்தூர் வட்டம்  
ஐக்கின்கம்பட்டி கிராமம்/நகரம். திரு / திருமதி / செல்வி /  
செல்வன் இய்யான் தகப்பனார்/மனைவர்  
பெயர் பிரமநாயகம்  
திருச்சி மறவரி

வகுப்பைச் சார்ந்தவர். அரக ஆணை நிலை எண் 28, பிற்பட்ட மற்றும் மிகவும் பிற்பட்ட பிரிவின் நலத் துறை. நாள் 19-7-1994 வரிசை எண் 36 படி சீமரபினர் பிரிவினைச் சார்ந்தவர் எனச் சான்றளிக்கப்படுகிறது.

This is to certify that..... Son / Daughter of Thiru..... of..... Village / Town..... Taluk.....

..... District of the State of Tamil Nadu belongs to..... Community, which is recognised as a Denotified Community as per Government Order (Ms.) No. 28, Backward Classes and Most Backward Classes Welfare, dated 19th July 1994. vide Serial No.....

2. திரு / திருமதி / செல்வன் / செல்வி இய்யான் என்பவரும் அவருடைய குடும்பத்தினரும் தமிழ்நாட்டில் திருவெங்கலைய மாவட்டத்தில் அம்பாசமுத்தூர் வட்டத்தில் ஐக்கின்கம்பட்டி கிராமத்தில் / நகரத்தில் வசித்து வருகிறார்கள் எனச் சான்றளிக்கப்படுகிறது.

2. It is certified that Thiru/Tmt./Selvan/Selvi..... and his/her family ordinarily reside(s) at ..... Village/Town ..... Taluk ..... District of Tamil Nadu.

முத்திரை Seal



கையொப்பம் Signature

நாள் Date

பதவி Designation

சி. சி. பி. லக்ஷ்மின்  
16/6/07  
ZONAL DEPUTY TAHSILDAR  
AMBASAMUDRAM.

The **Annexure 1** format for a PhD thesis in the Electronics and Communication Engineering (ECE) department serves as a supplementary section that provides detailed information, technical data, or supporting materials related to the research. Below is a generic format for **Annexure 1** that you can customize based on your specific research project.

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## **Annexure 1: [Title of Annexure]**

(For example: "Detailed Experimental Setup and Results," "Simulation Parameters," or "Circuit Diagrams and Specifications")

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### **1.1 Title of First Section**

- Provide a brief description of the first item or component included in Annexure 1.
  - This could include technical details, diagrams, charts, or any supplementary information that supports the main thesis.
  - Example: *Circuit Design Schematic of the Proposed Wireless Communication System*.
  - **Diagram/Figure:** Include a high-resolution circuit schematic or block diagram.
  - **Description:** A brief explanation of the design, components used, and its role in the overall research.
- 

### **1.2 Title of Second Section**

- Another section with relevant supporting materials, such as simulation setups, algorithm details, or measurement procedures.
- Example: *Simulation Setup and Parameters*.
- **Simulation Software:** State the software used for simulations (e.g., MATLAB, HFSS, Cadence).
- **Simulation Parameters:** List parameters such as frequency, power levels, signal types, etc.
- Example Table:

<b>Parameter</b>	<b>Value</b>	<b>Unit</b>
Input Frequency	2.4	GHz
Signal Power	-10	dBm
Bandwidth	20	MHz

---

### **1.3 Title of Third Section**

- Data, results, or experimental measurements that are too detailed to include in the main body of the thesis.
- Example: *Test Results from Hardware Implementation*.
- **Table of Results:** A table summarizing test or experiment results.

Test Condition	Frequency (GHz)	Power (dBm)	Signal-to-Noise Ratio (dB)	Bit Error Rate (BER)
Condition 1	2.4	-3	30	$10^{-6}$
Condition 2	5.0	-5	25	$10^{-4}$

---

#### 1.4 Title of Fourth Section

- Additional information, such as a block diagram, algorithm description, or hardware setup.
- Example: *Block Diagram of System Architecture*.
- **Diagram/Figure:** Include relevant figures or flowcharts to explain complex processes.

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#### 1.5 Title of Fifth Section (Optional)

- Any other supplementary information that is relevant to your research.
- Example: *Raw Data from Experiments* or *MATLAB Code for Algorithm Simulation*.

---

#### Formatting Guidelines:

1. **Labeling:** Each annexure should be clearly labeled (Annexure 1, Annexure 2, etc.), with subsections like 1.1, 1.2, etc.
2. **Cross-Referencing:** Ensure you reference each annexure properly in the main thesis document where relevant.
3. **Presentation:** Maintain a clean, professional layout. Use consistent formatting for tables, figures, and descriptions.

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#### Example Outline for Annexure 1 in PhD ECE:

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##### Annexure 1: Circuit Diagrams and Test Results of the Communication System

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## 1.1 Circuit Schematic of the Transmitter Module

- Detailed circuit diagram of the transmitter used in the wireless communication system.
  - **Figure:** Circuit schematic showing component placement and connections.
- 

## 1.2 Simulation Setup and Parameters

- Parameters used for simulating the communication system.
- **Table:**

Parameter	Value	Unit
Input Frequency	2.4	GHz
Signal Power	-10	dBm
Bandwidth	20	MHz

---

## 1.3 Measured Test Results

- Results from hardware testing under different conditions.
- **Table:**

Test Condition	Frequency (GHz)	Power (dBm)	Signal-to-Noise Ratio (dB)	Bit Error Rate (BER)
Condition 1	2.4	-3	30	$10^{-6}$
Condition 2	5.0	-5	25	$10^{-4}$

---

## 1.4 Block Diagram of System Architecture

- A high-level block diagram showing the overall system architecture.
  - **Figure:** System block diagram.
- 

## Notes:

- The annexure should include technical materials like schematics, code, test results, or any detailed supplementary information that would support the findings presented in the main thesis.
- Make sure the annexure is numbered correctly and referred to throughout the document for clarity.

