



ANNA UNIVERSITY, CHENNAI - 25
M.E. DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF GRADES



D119157731380R

Folio No. AUQ1307107

NAME OF THE CANDIDATE	DHURGA DEVI G R			REGISTER NO.	961617419006
DATE OF BIRTH	16-AUG-95	GENDER	FEMALE	MONTH & YEAR OF LAST APPEARANCE	APR 2019
COLLEGE OF STUDY	MARTHANDAM COLLEGE OF ENGINEERING AND TECHNOLOGY			REGULATIONS	2017
PROGRAMME & BRANCH	M.E. VLSI Design				

SEM NO	COURSE CODE	COURSE TITLE	CREDITS	LETTER GRADE	GRADE POINT	MONTH & YEAR OF PASSING
01	AP5151	Advanced Digital System Design	3	B+	7	JAN 2018
01	MA5152	Applied Mathematics for Electronics Engineers	4	B	6	JAN 2018
01	VL5101	CMOS Digital VLSI Design	3	A	8	JAN 2018
01	VL5102	CAD for VLSI Circuits	3	B+	7	JAN 2018
01	VL5103	Analog IC Design	4	B	6	NOV 2018
01	VL5191	DSP Integrated Circuits	3	A	8	JAN 2018
01	VL5111	VLSI Design Laboratory I	2	O	10	JAN 2018
02	VL5201	Testing of VLSI Circuits	3	B+	7	APR 2018
02	VL5202	Low Power VLSI Design	3	A	8	APR 2019
02	VL5291	VLSI Signal Processing	3	A	8	APR 2018
02	AP5191	Embedded System Design	3	A	8	APR 2018
02	DS5191	DSP Processor Architecture and Programming	3	B+	7	APR 2018
02	VL5002	RF IC Design	3	B+	7	APR 2018
02	VL5211	VLSI Design Laboratory II	2	O	10	APR 2018
02	CP5281	Term Paper Writing and Seminar	1	O	10	APR 2018
03	VL5301	Analog to Digital Interfaces	3	A	8	APR 2019
03	AP5292	Digital Image Processing	3	B+	7	APR 2019
03	VL5012	Selected Topics in IC Design	3	B+	7	APR 2019
03	VL5311	Project Work Phase - I	6	O	10	NOV 2018
04	VL5411	Project Work Phase - II	12	O	10	APR 2019

*** End of Statement ***

Cumulative Grade Point Average : 8.13

Classification : FIRST CLASS

Self Attested
[Signature]



Medium of Instruction : ENGLISH

27/08/2019

[Signature]
SIGNATURE OF THE CANDIDATE



[Signature]
CONTROLLER OF EXAMINATIONS